

FIG. 1

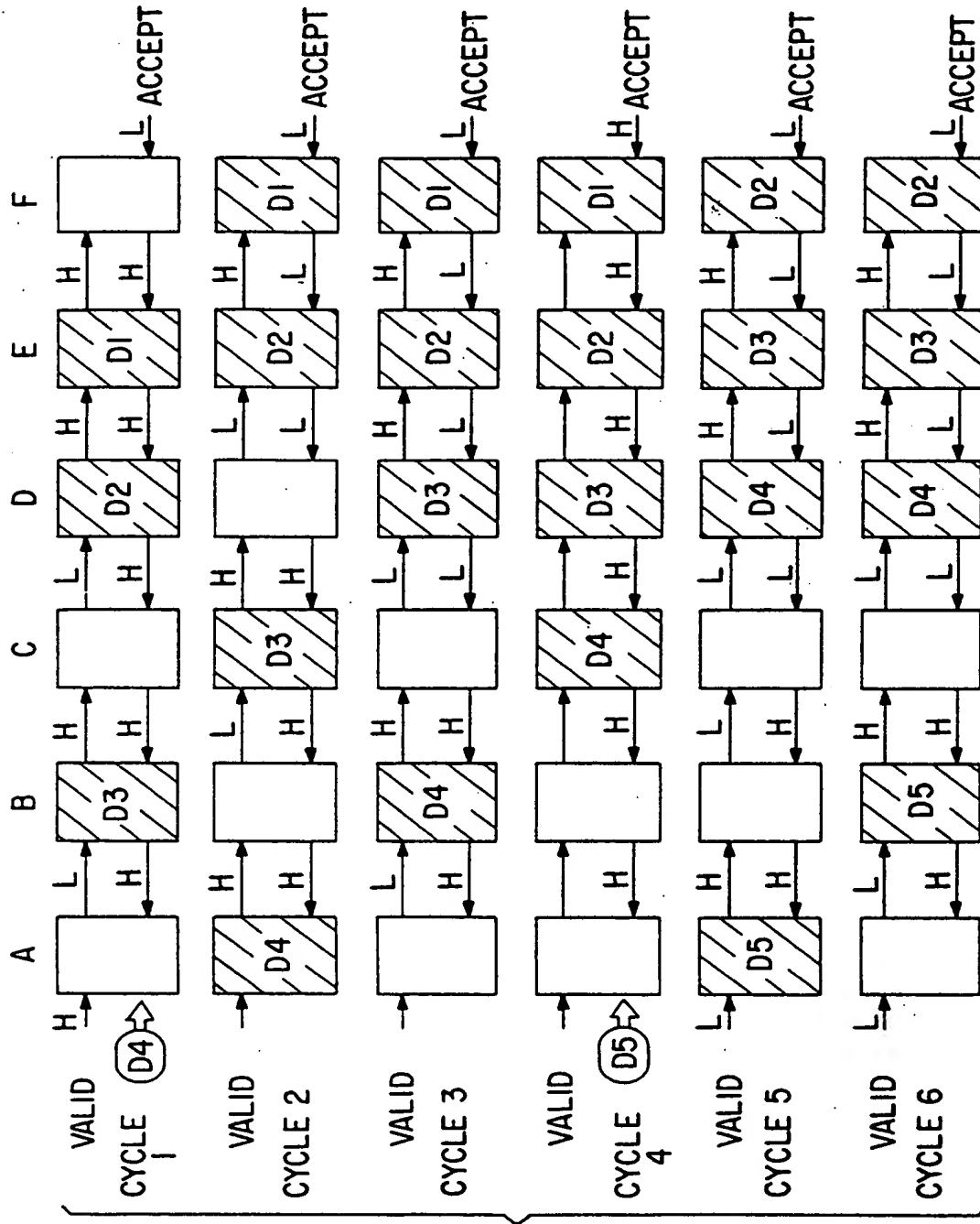


FIG. 1

FIG. 2(A)

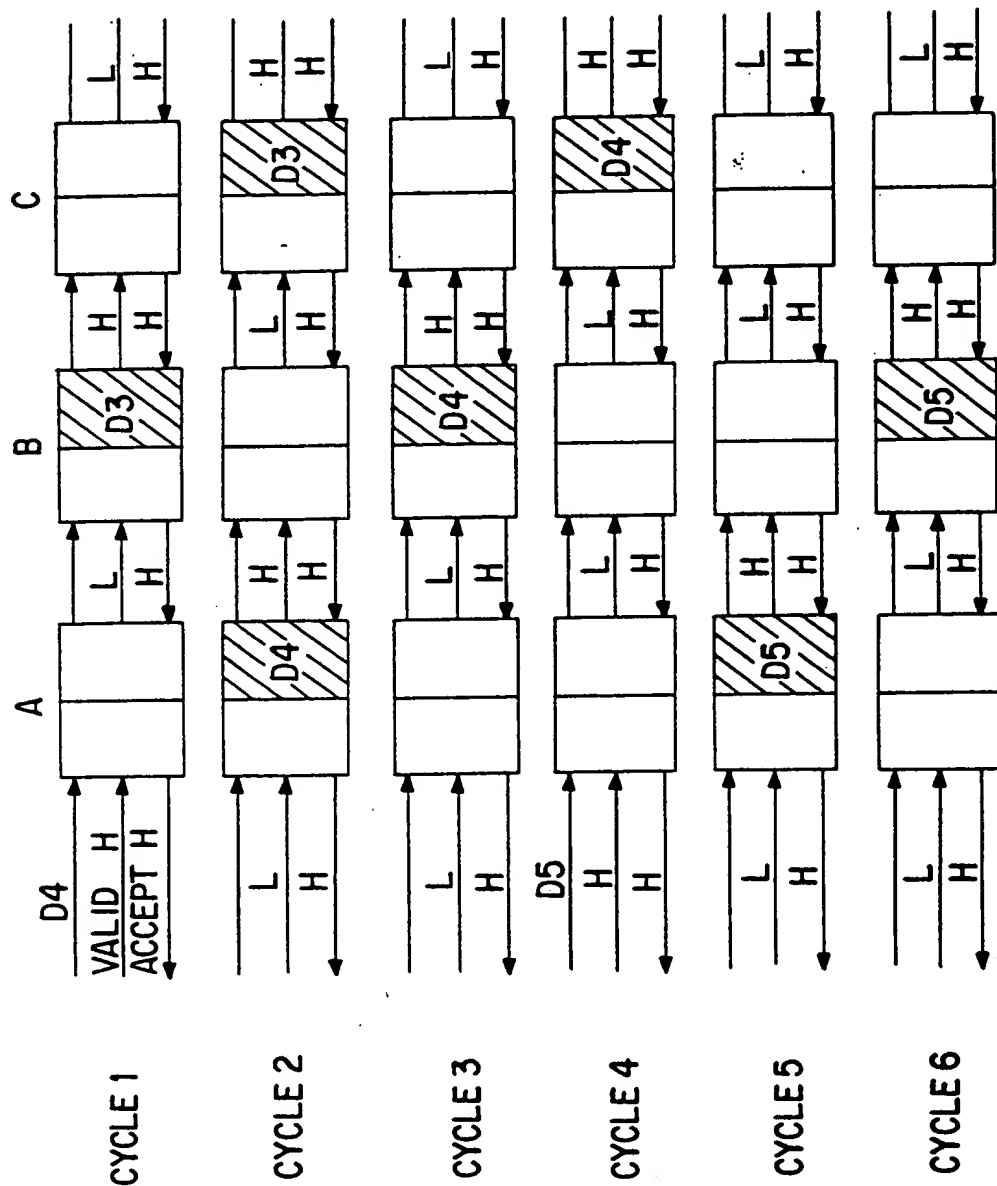


FIG. 2(A)

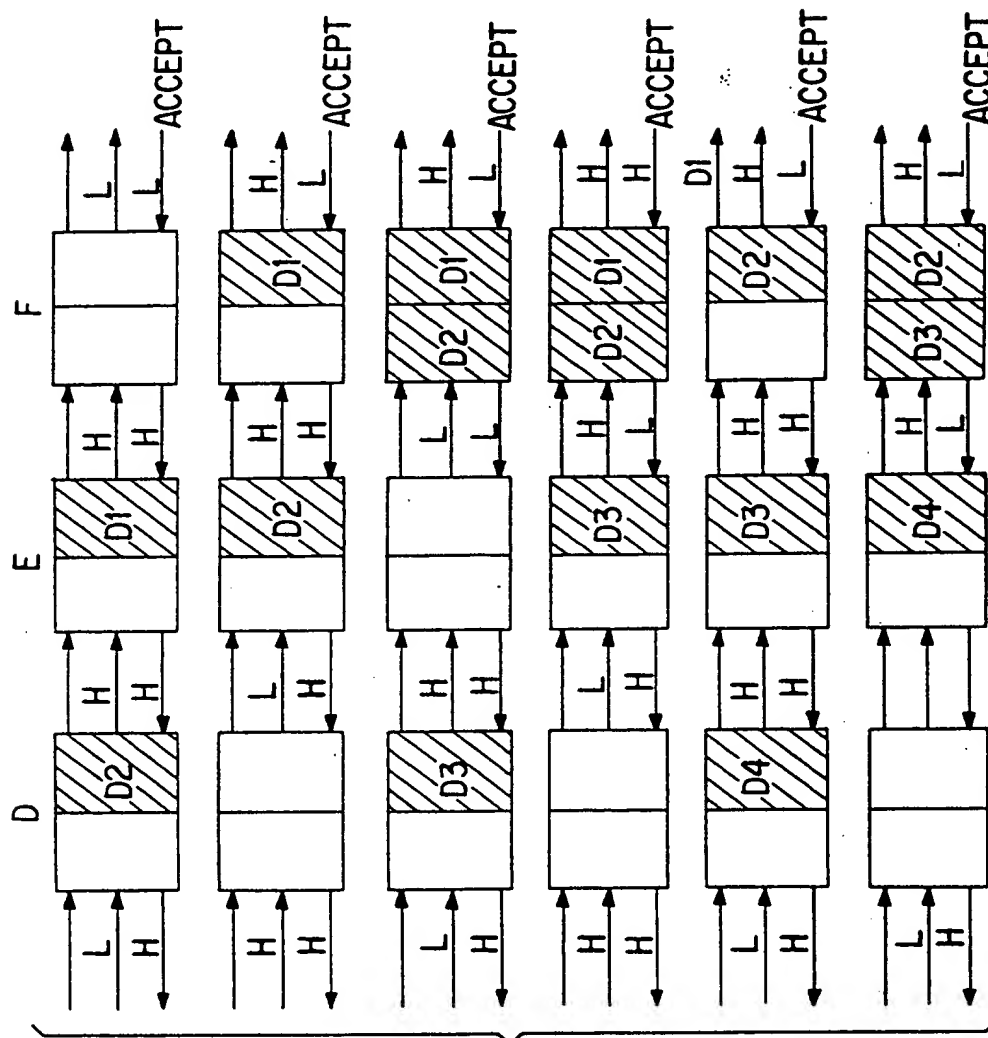


FIG. 2(B)

FIG. 3A-1

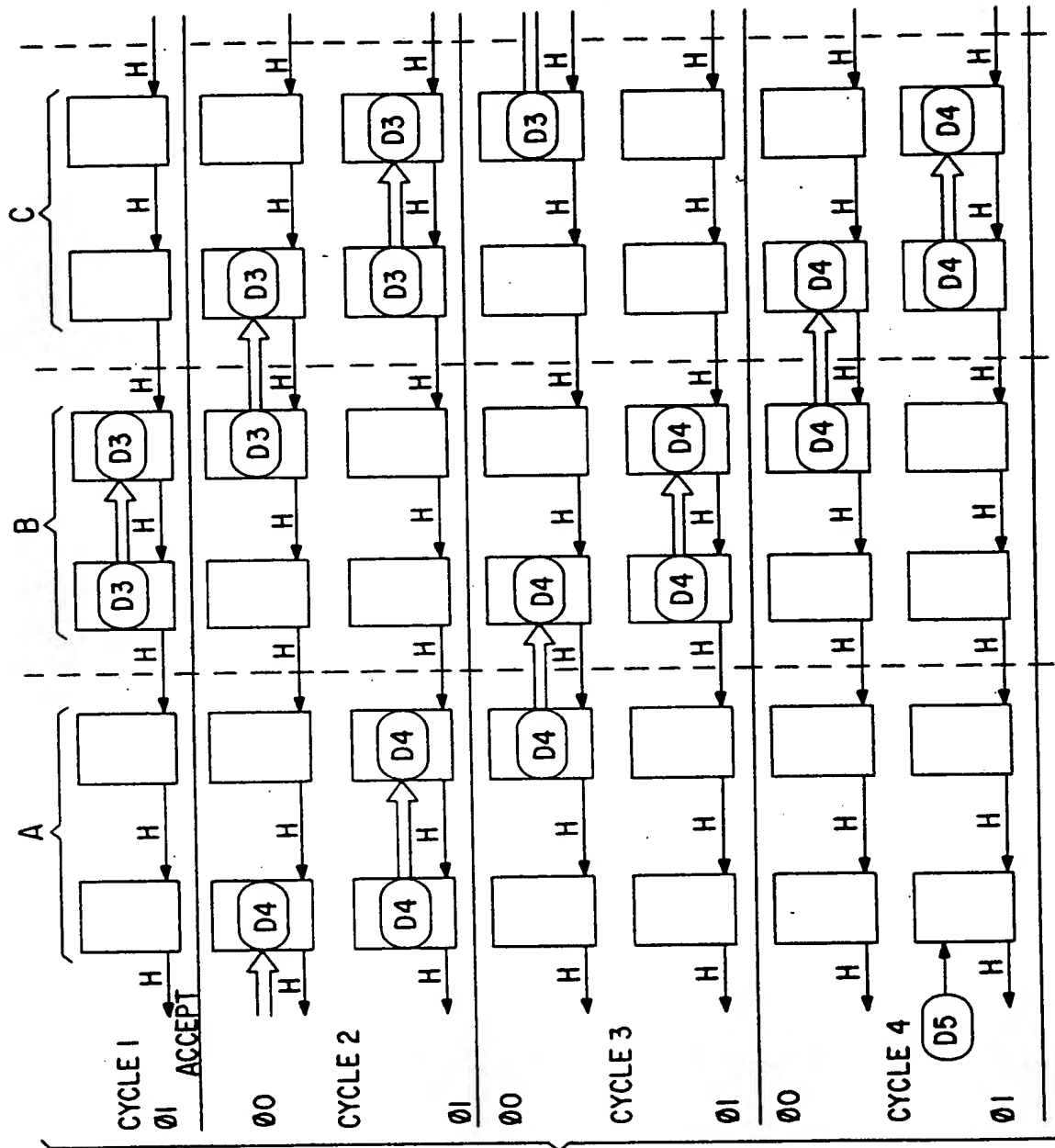


FIG. 3A-1



FIG. 3A-2

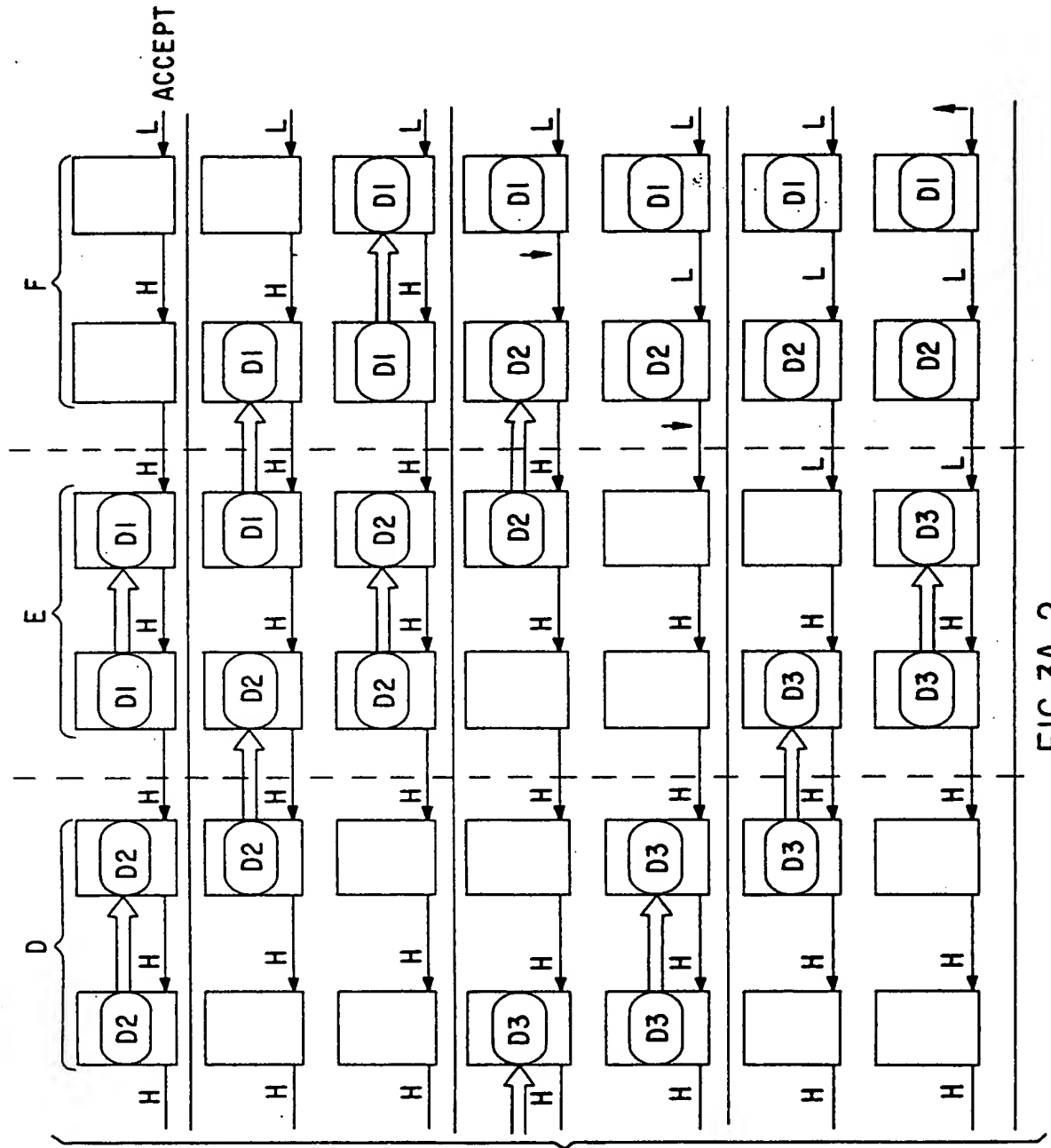


FIG. 3A-2

FIG. 3B-1

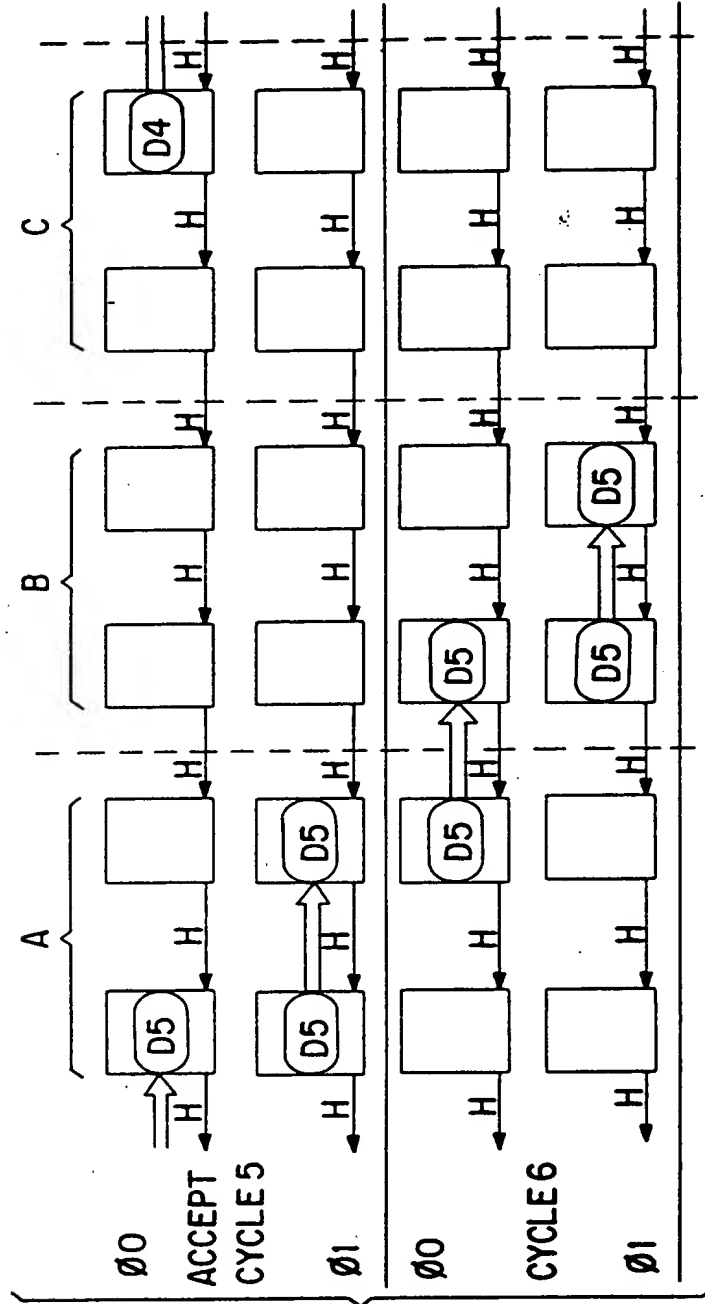


FIG. 3B-1

FIG. 3B-2

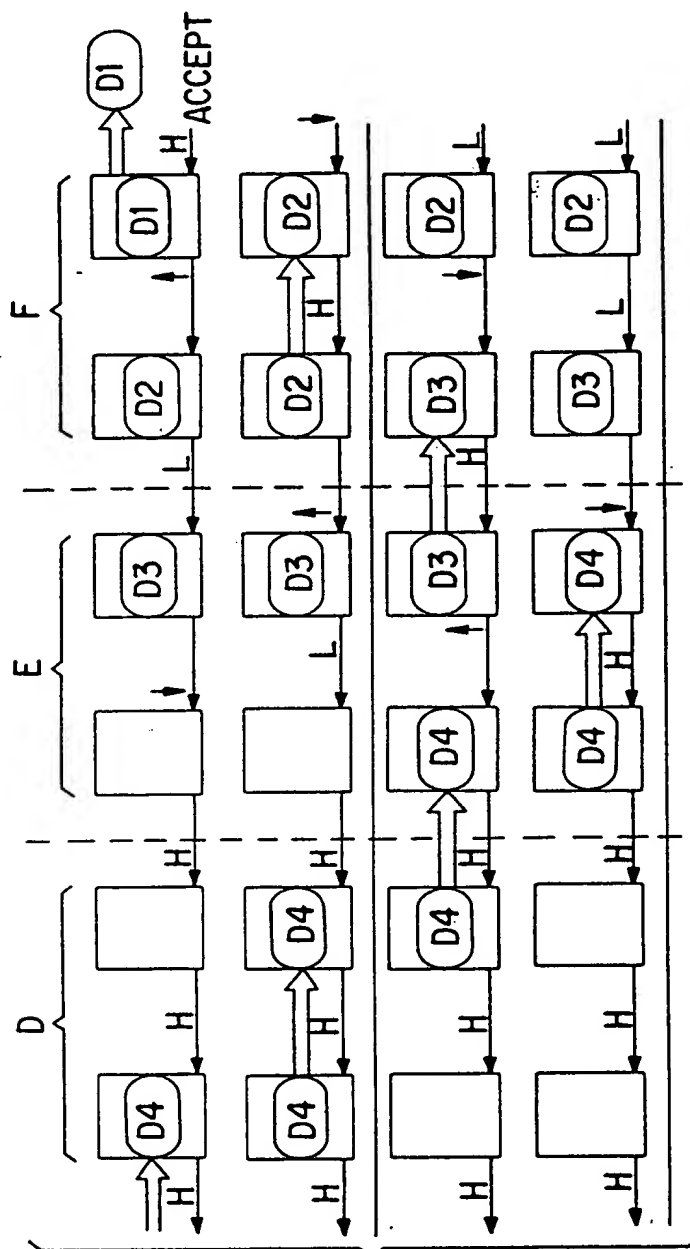


FIG. 3B-2

FIG. 4

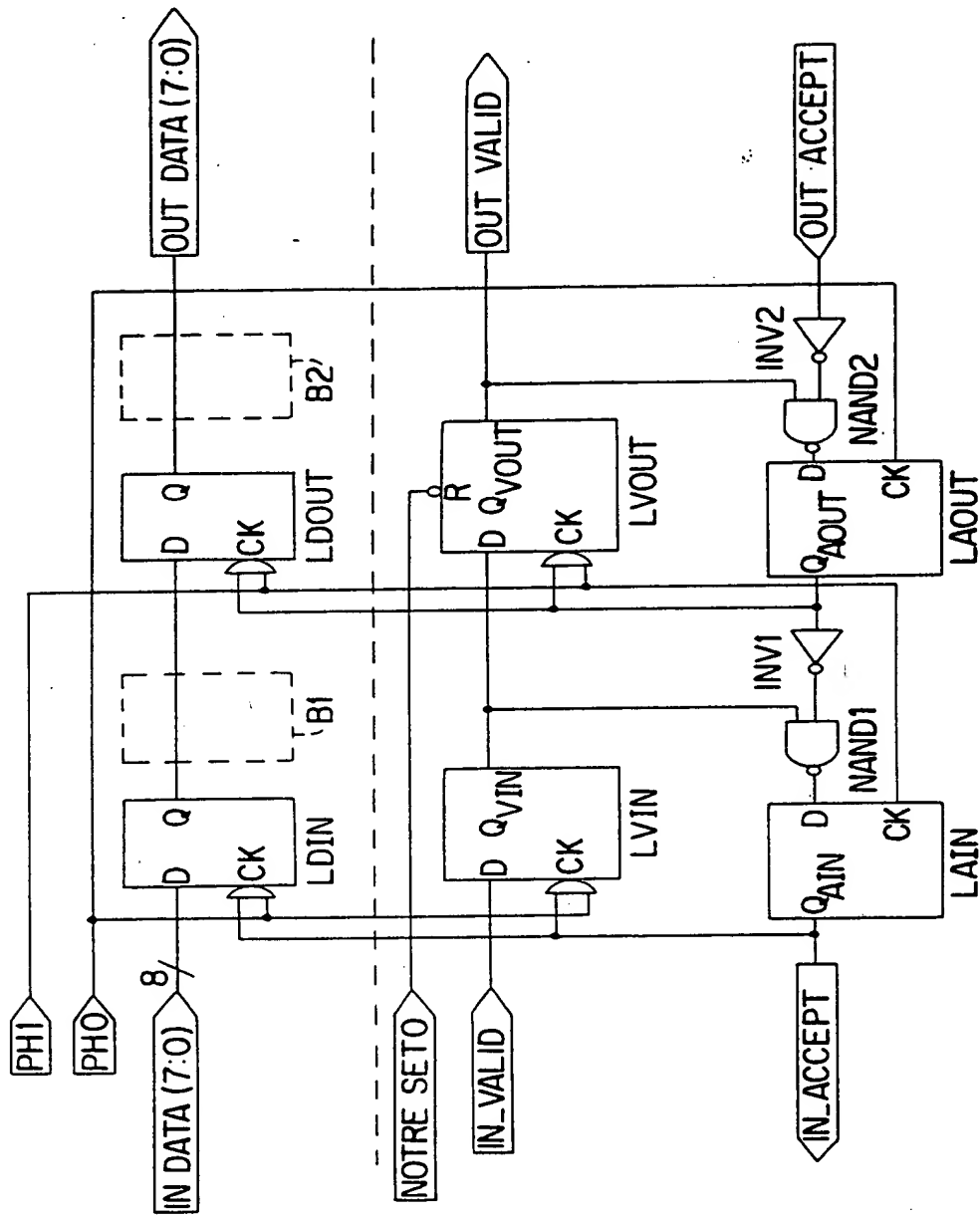
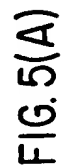


FIG. 4



00771052.012001

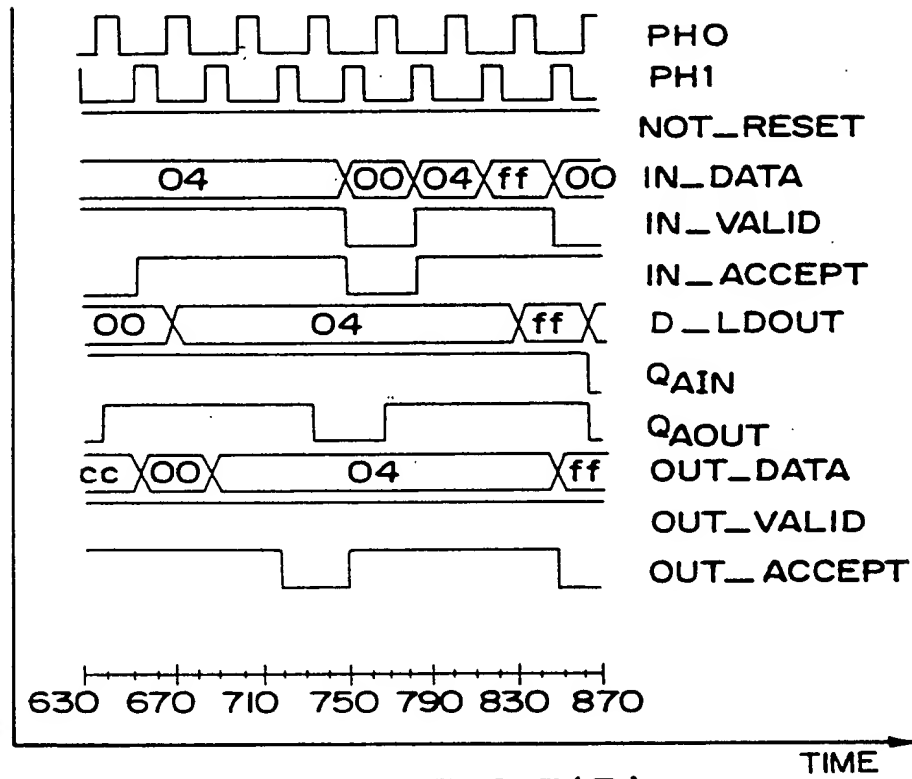


FIG. 5(B)

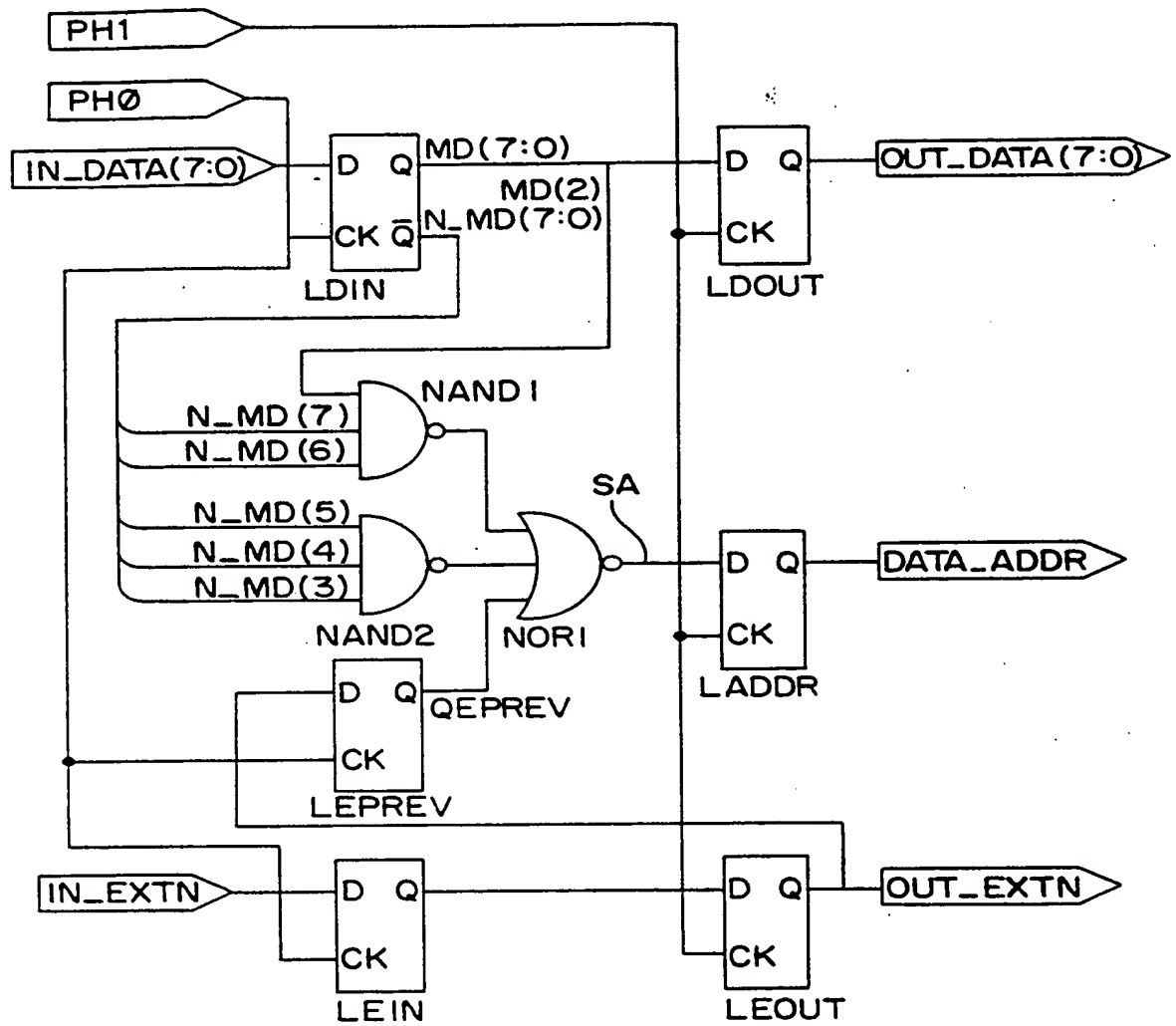


FIG. 6

0074063-013074

FIG. 7

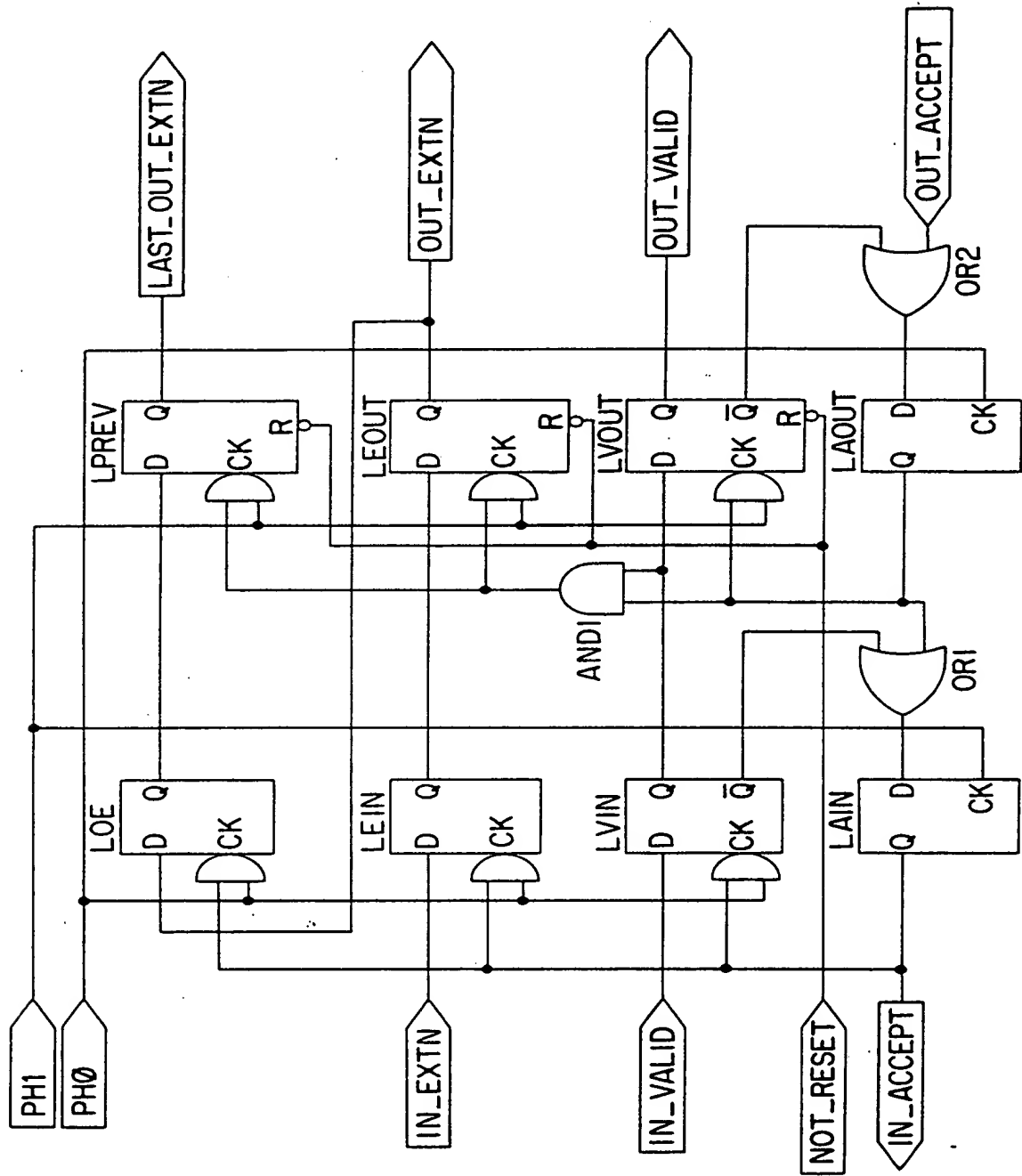


FIG. 7



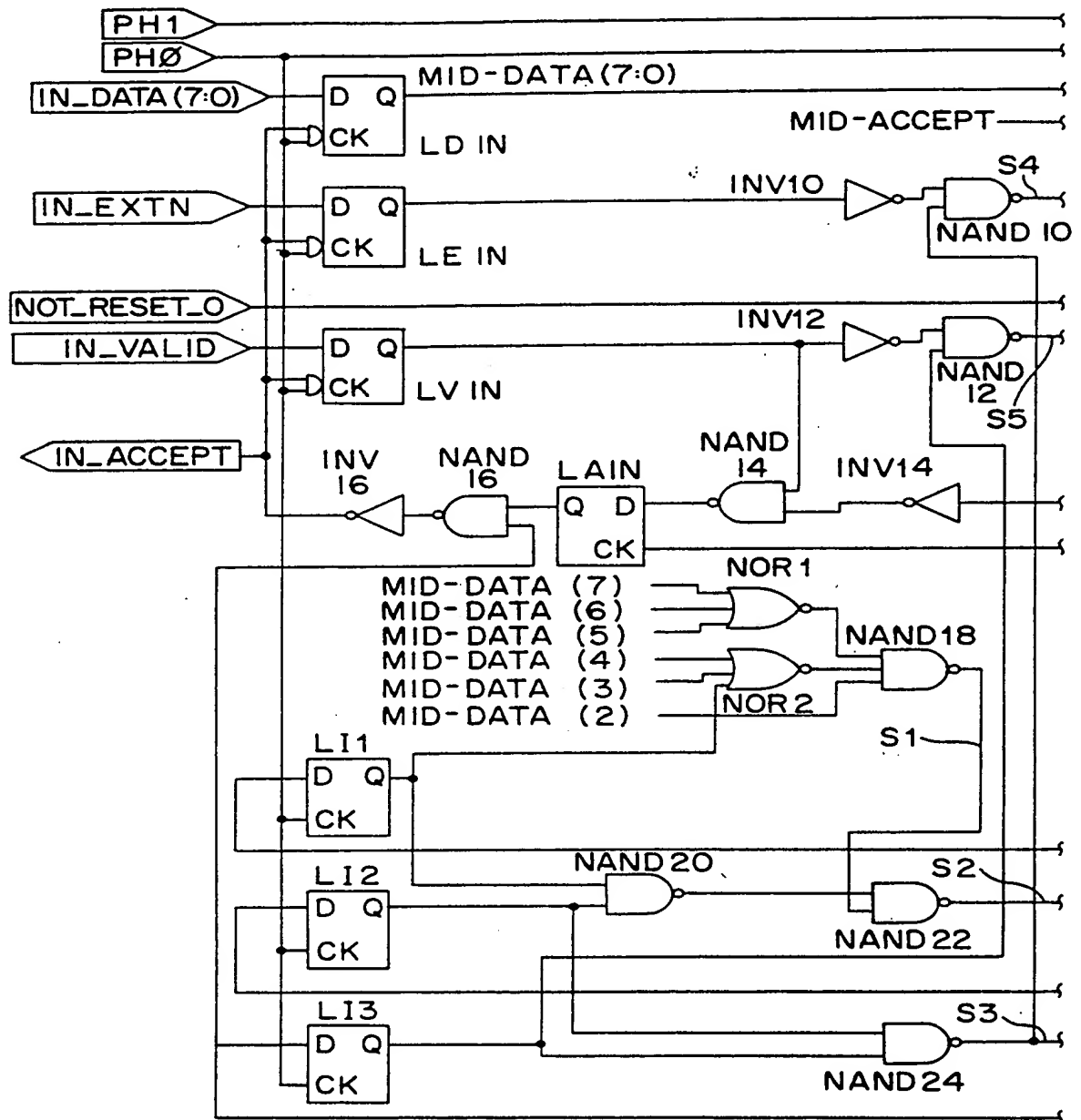


FIG. 8(A)

00771062-012001

00771062.012001

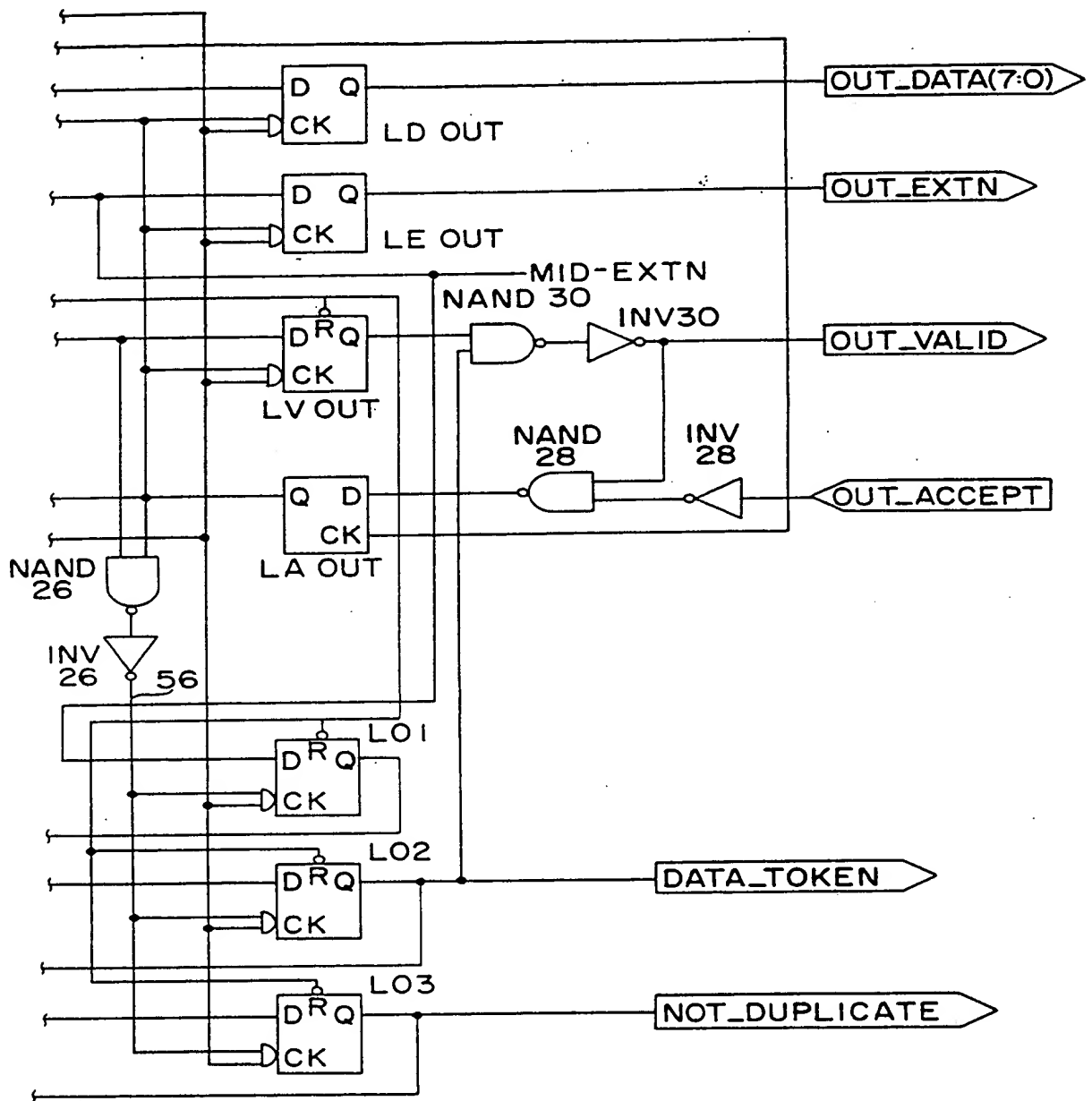


FIG. 8(B)

21-22

[illegible]

143 208 273 338 403 468 533 598 663 728 793 858 923 TIME

0071052 101001

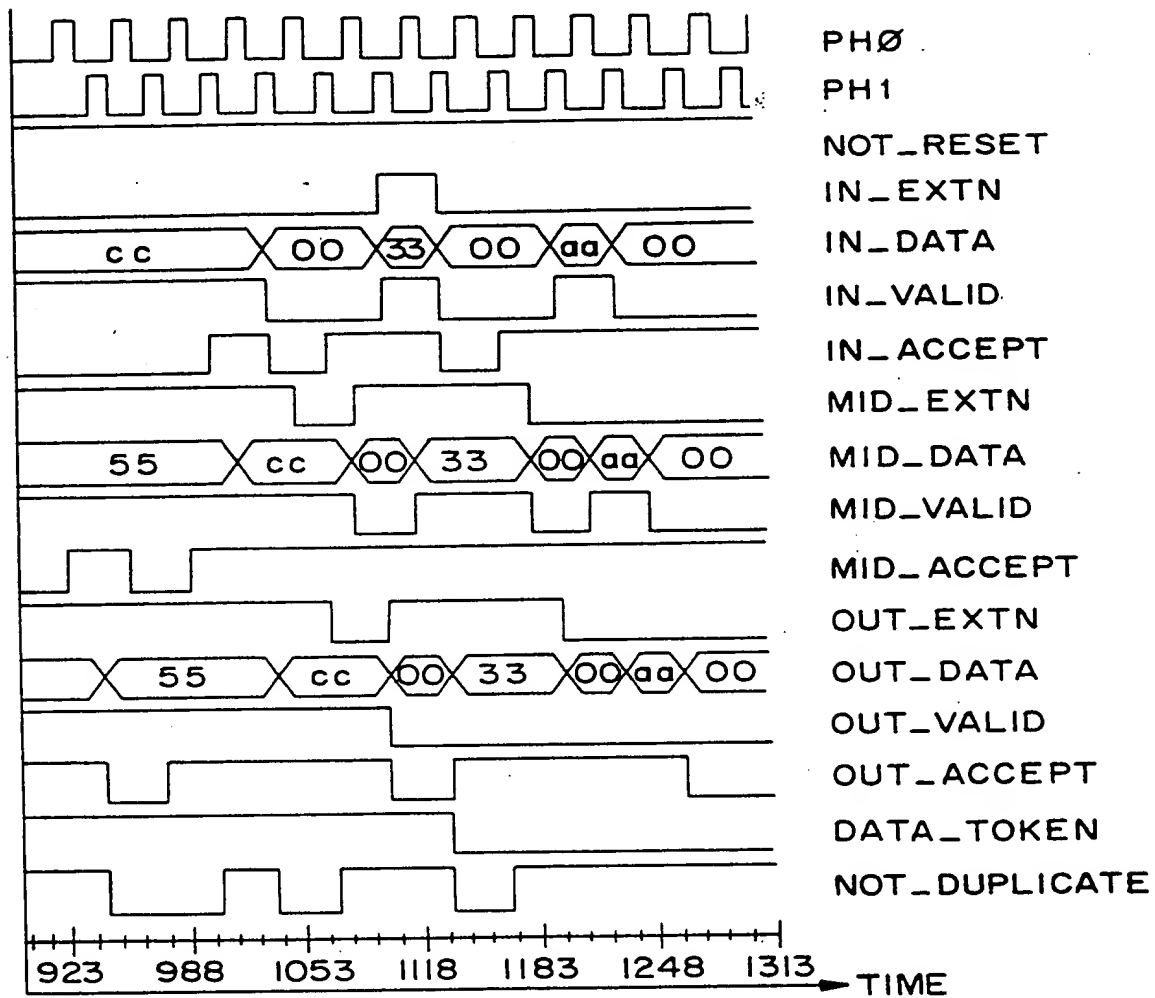


FIG. 9(B)

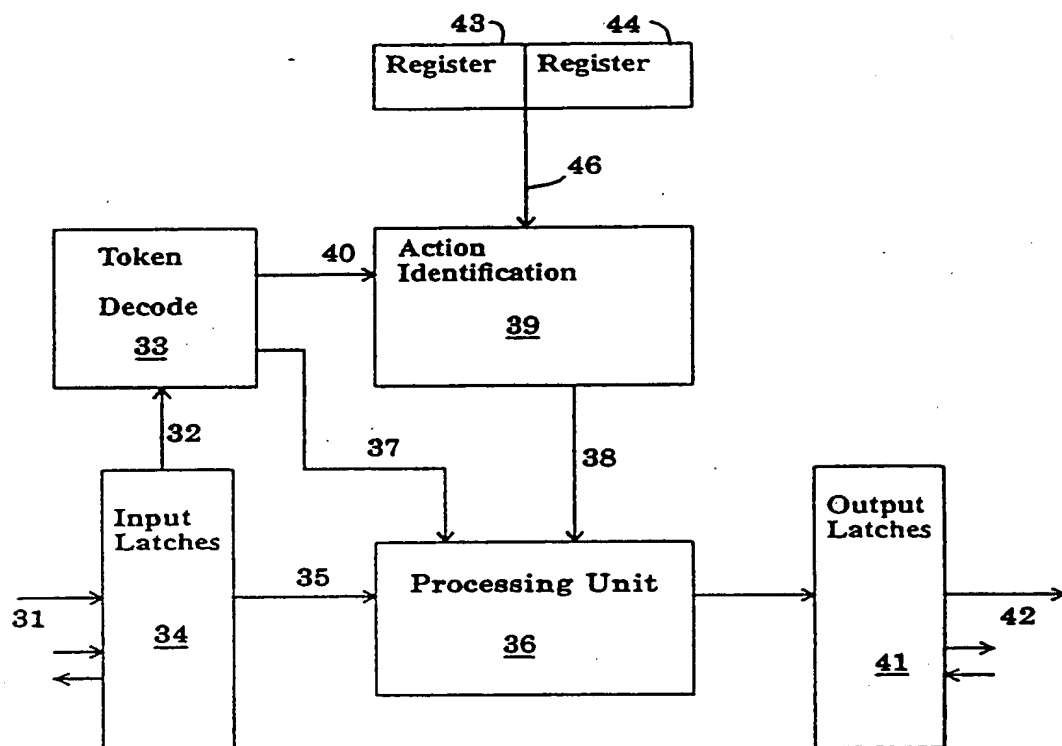


FIG. 10

FIG. 10 - 230 F 2200

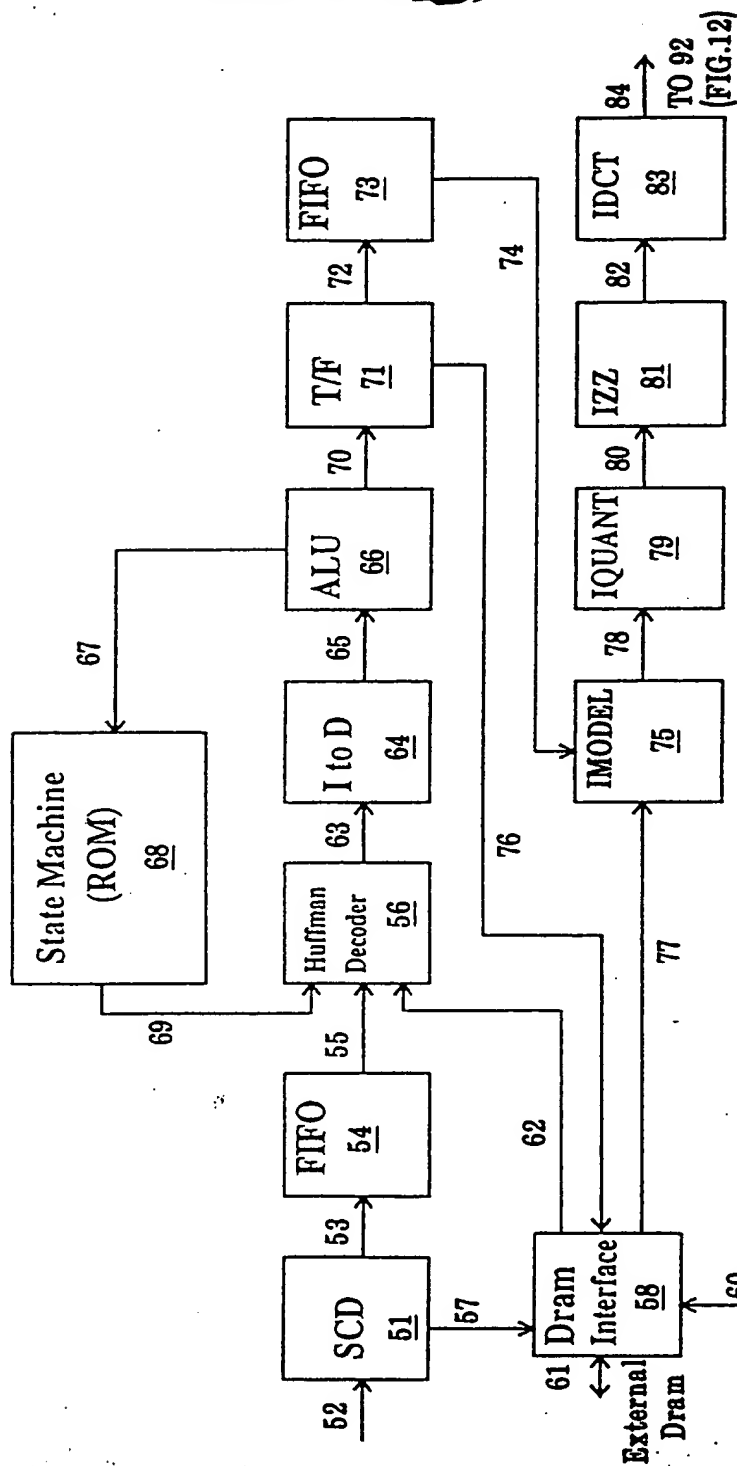


FIG. 11

FIG. 10-25072260

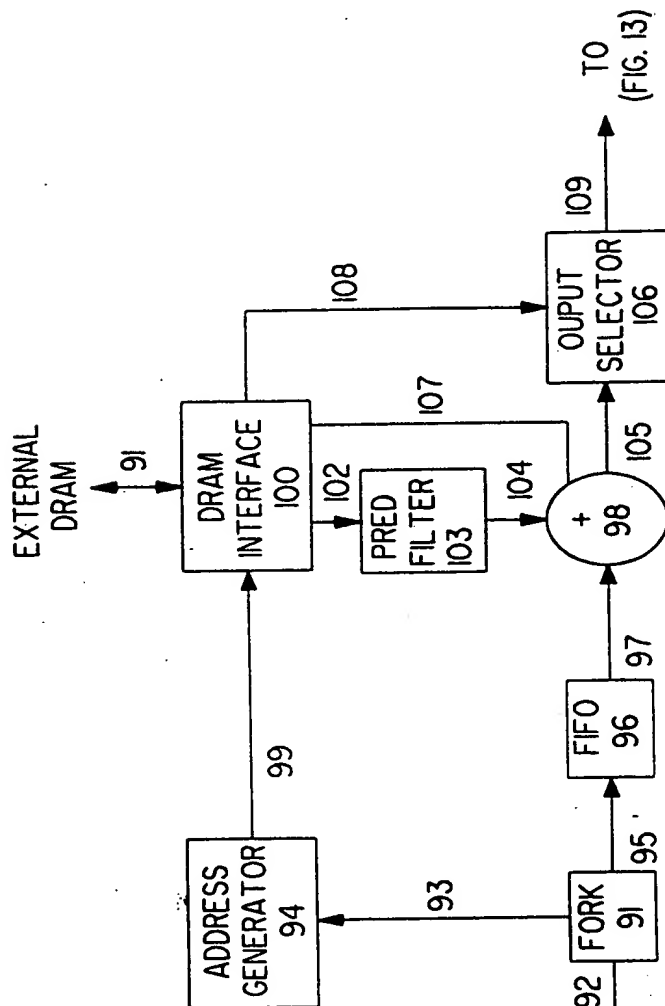


FIG. 12

FIG. 10

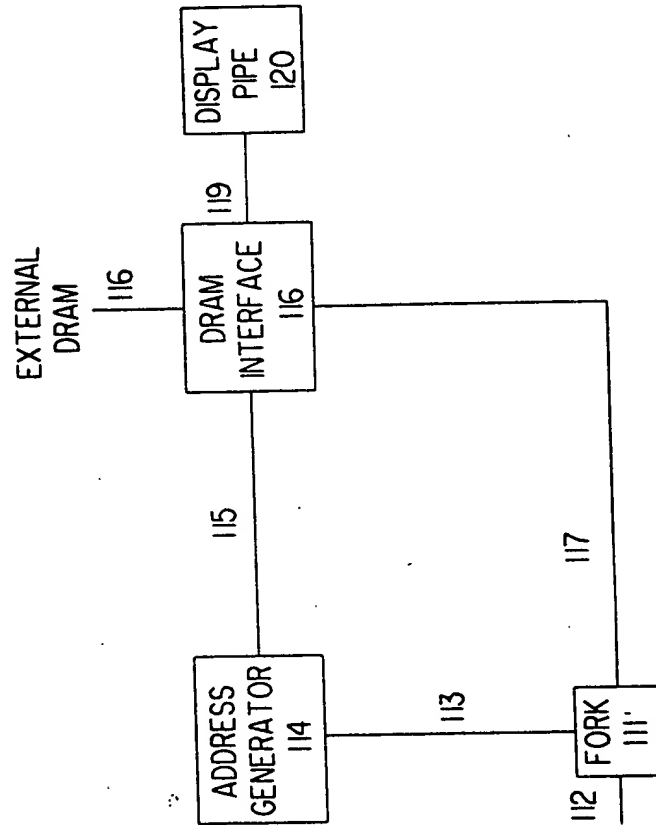


FIG. 13



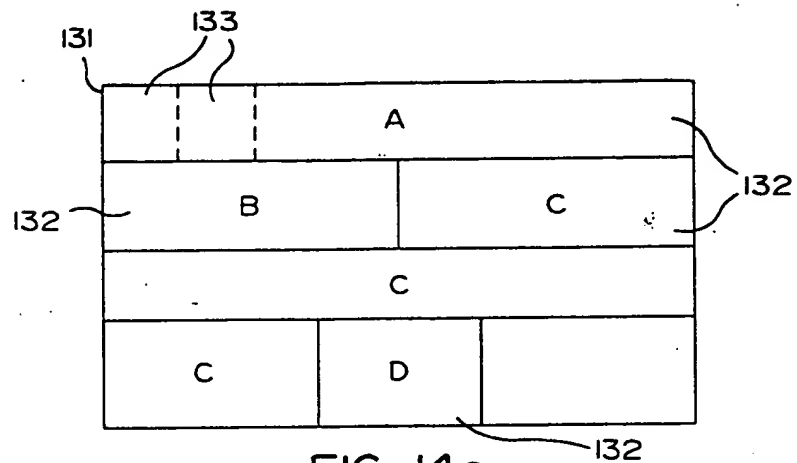


FIG. 14a

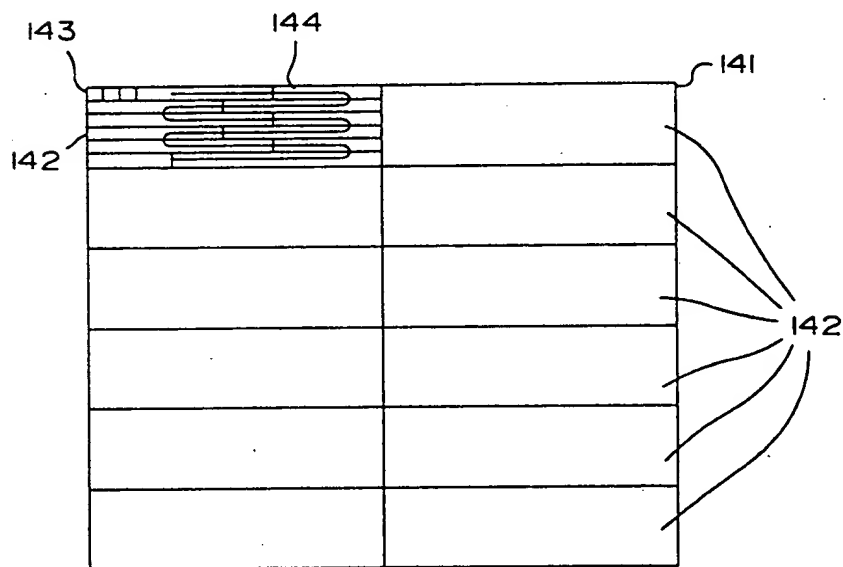


FIG. 14b

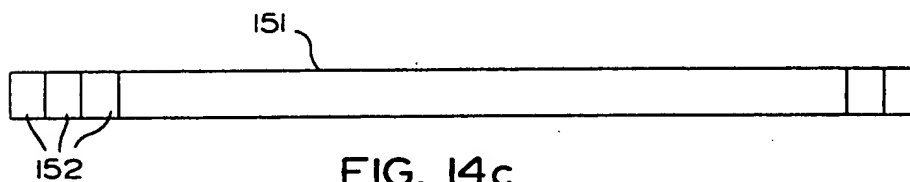


FIG. 14c

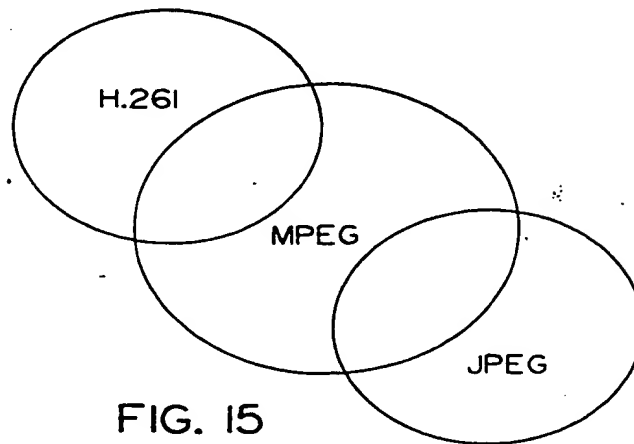


FIG. 15

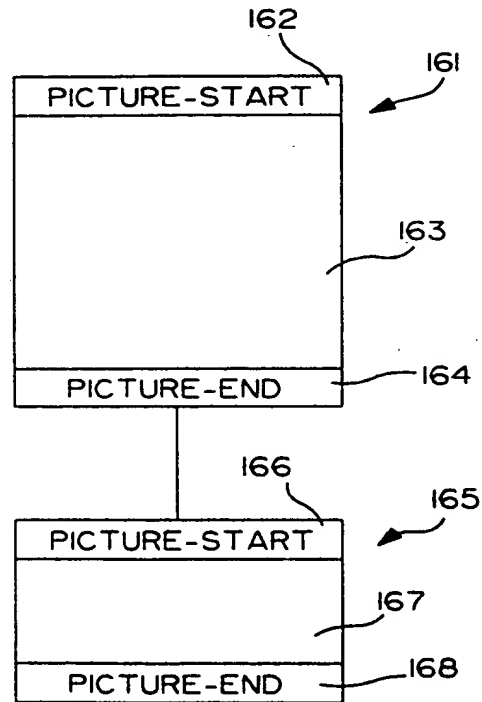


FIG. 16

00774062.012004

FIG. 10 25072260

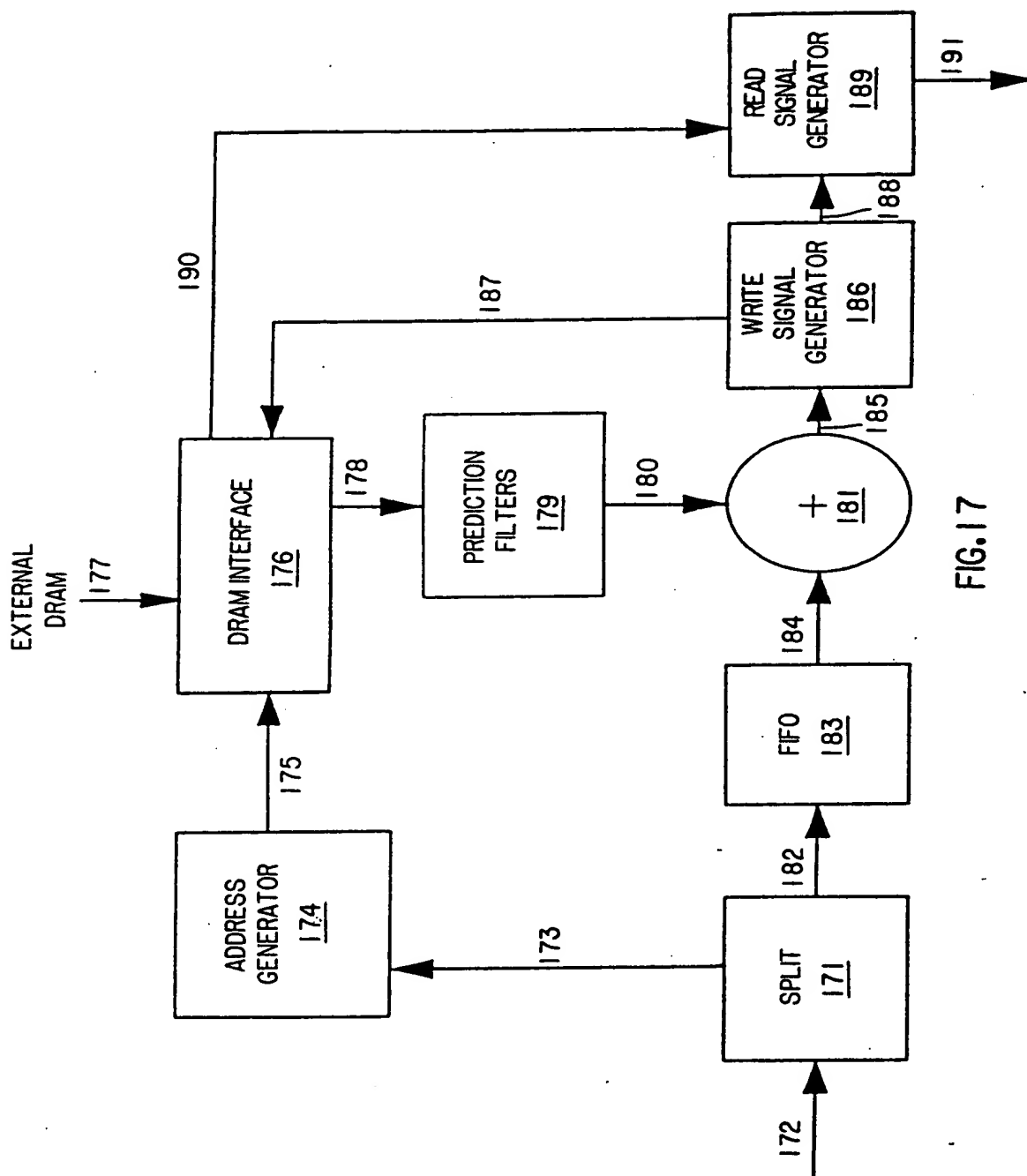


FIG. 17

00771052.012001

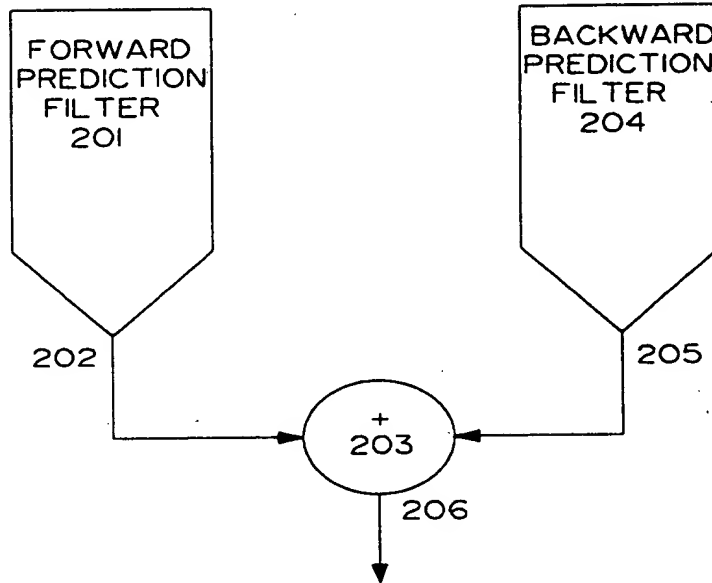


FIG. 18

FIG. 19

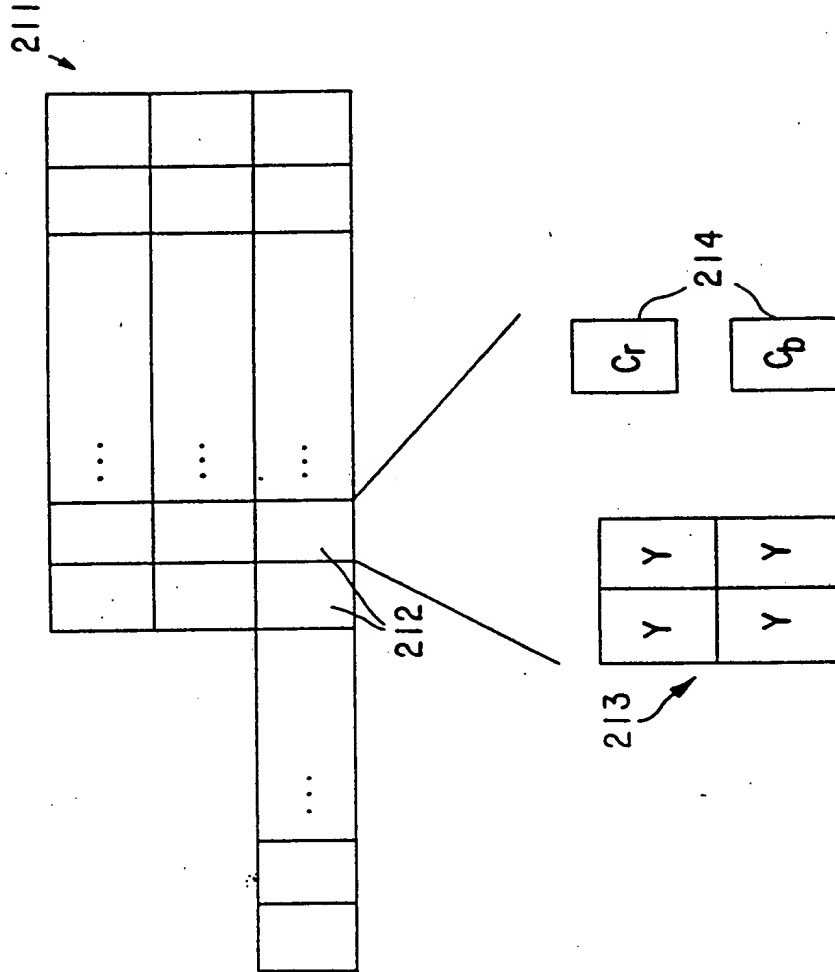


FIG. 19

FIG. 20

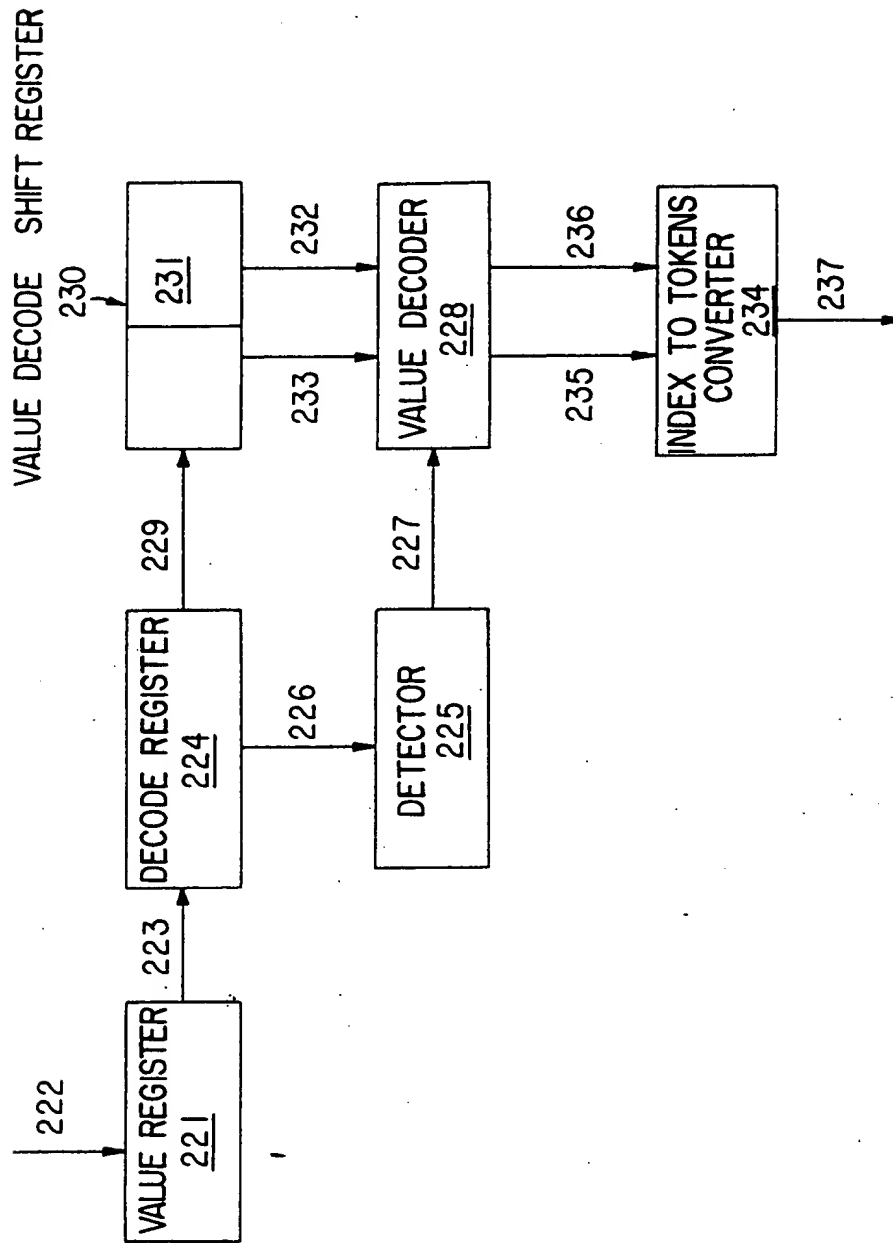


FIG.20

FIG. 21

PRINT OF DRAWINGS  
AS ORIGINALLY FILED

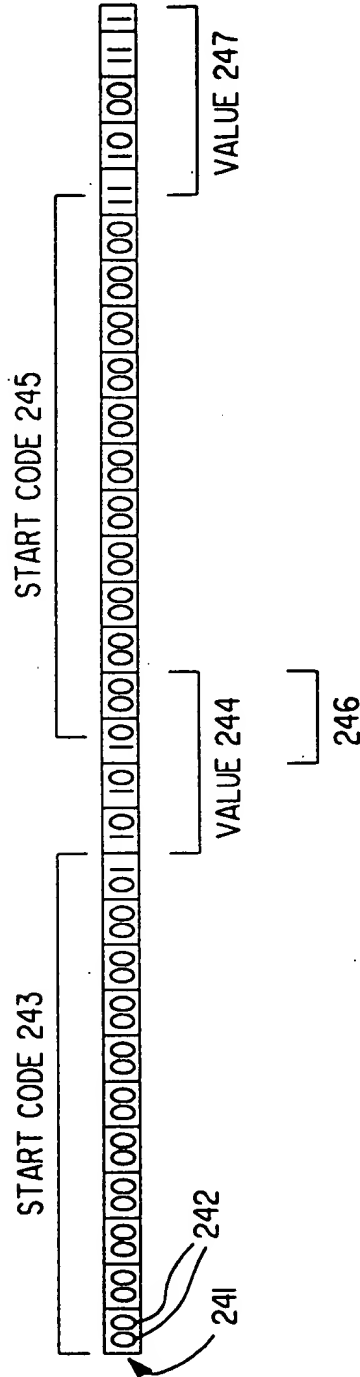


FIG. 21

FIG. 22

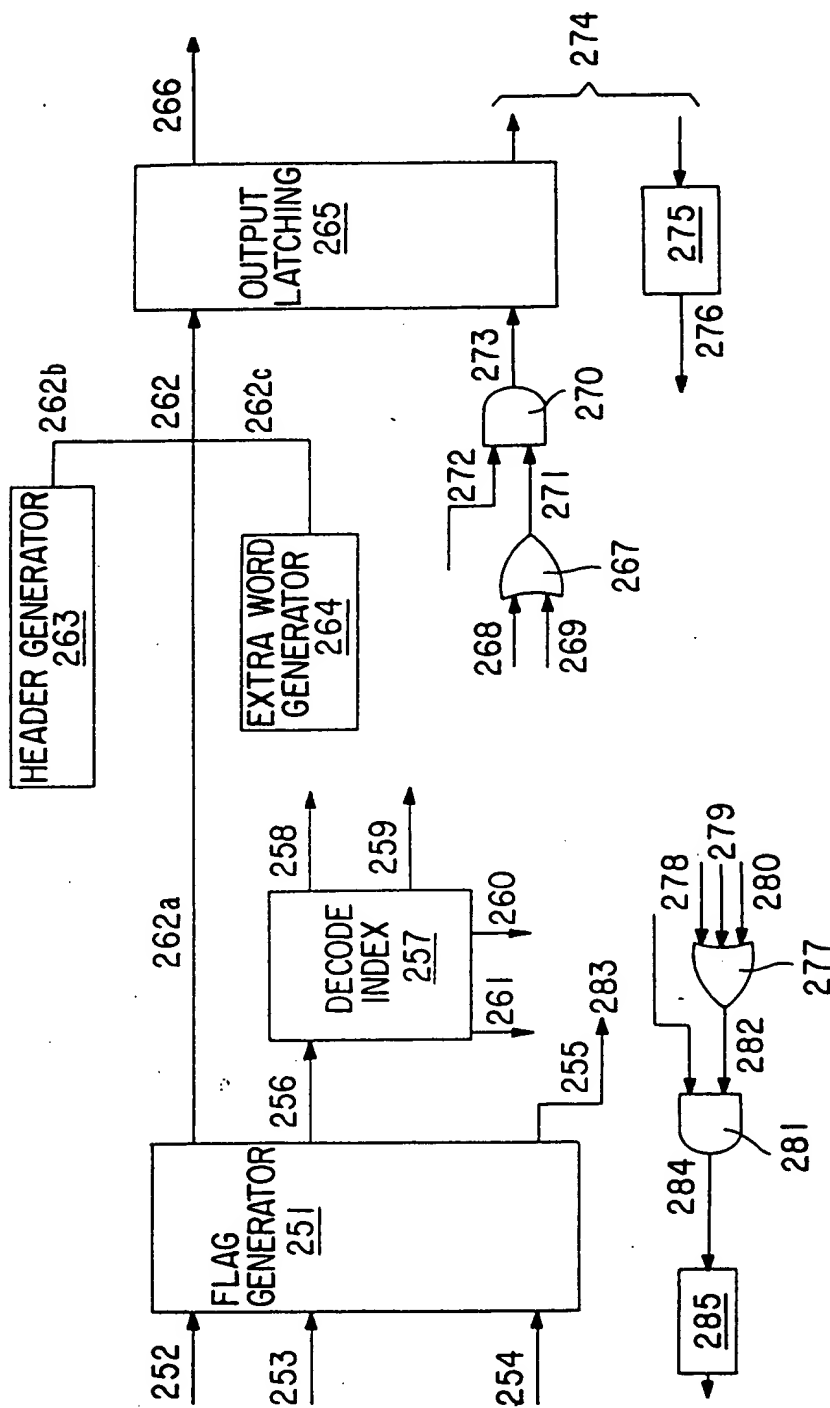


FIG. 22



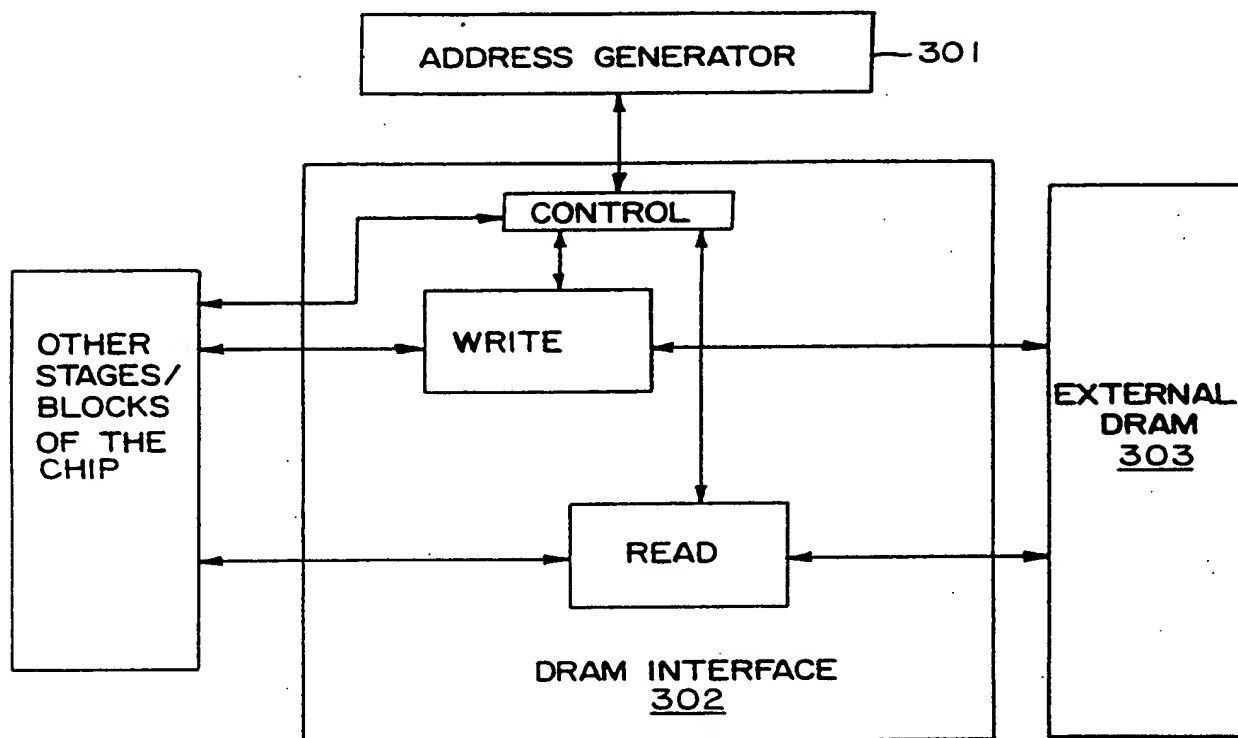


FIG.23

00771052.012001

FIG. 24

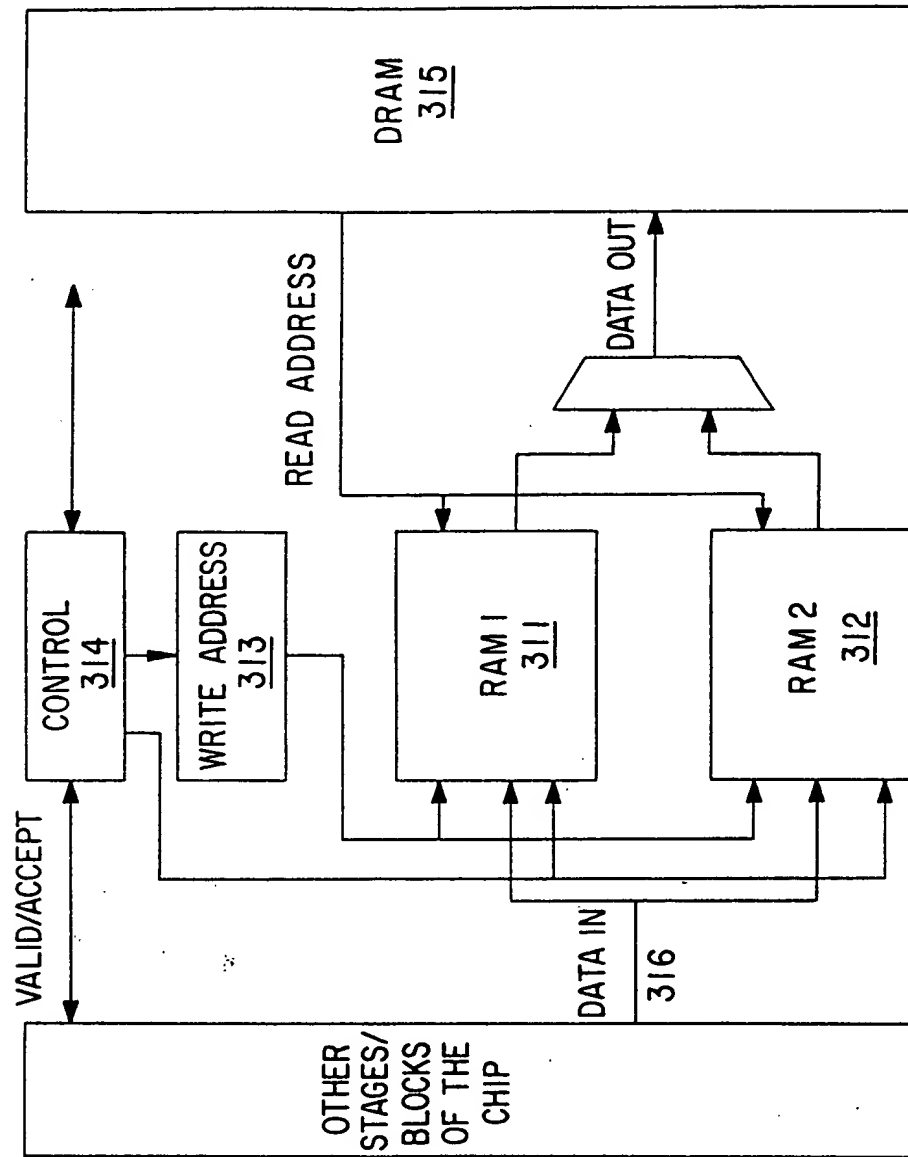


FIG. 24

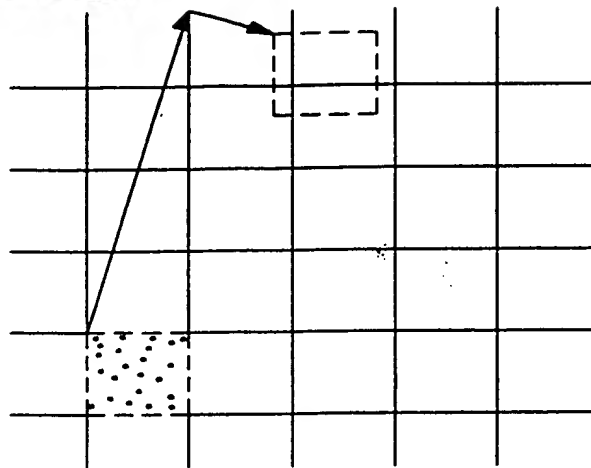


FIG. 25

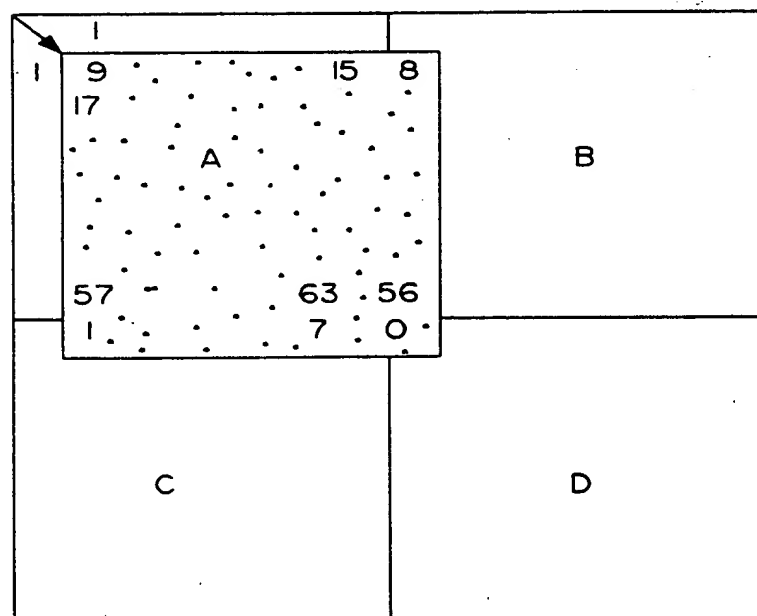


FIG. 26

09771062-012004

00771062.01200

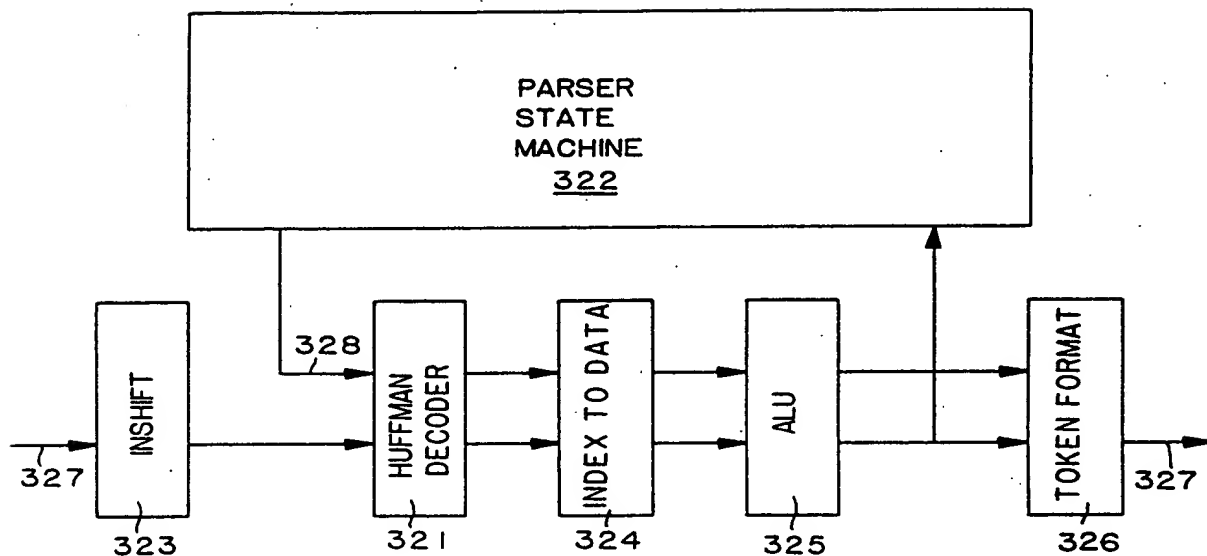


FIG.27

09771052.012001

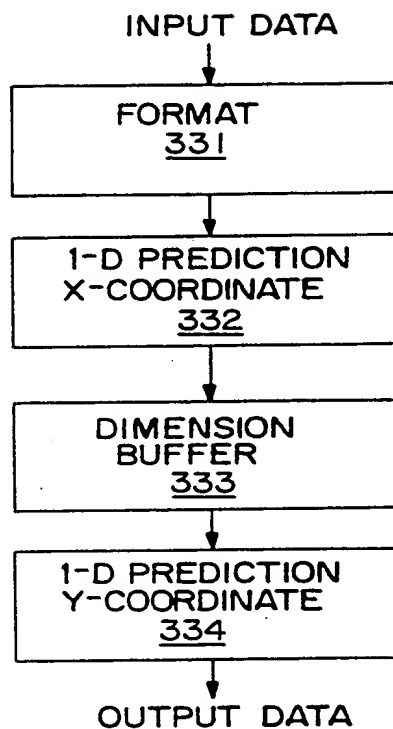


FIG.28

Multiplexed audio/video  
data

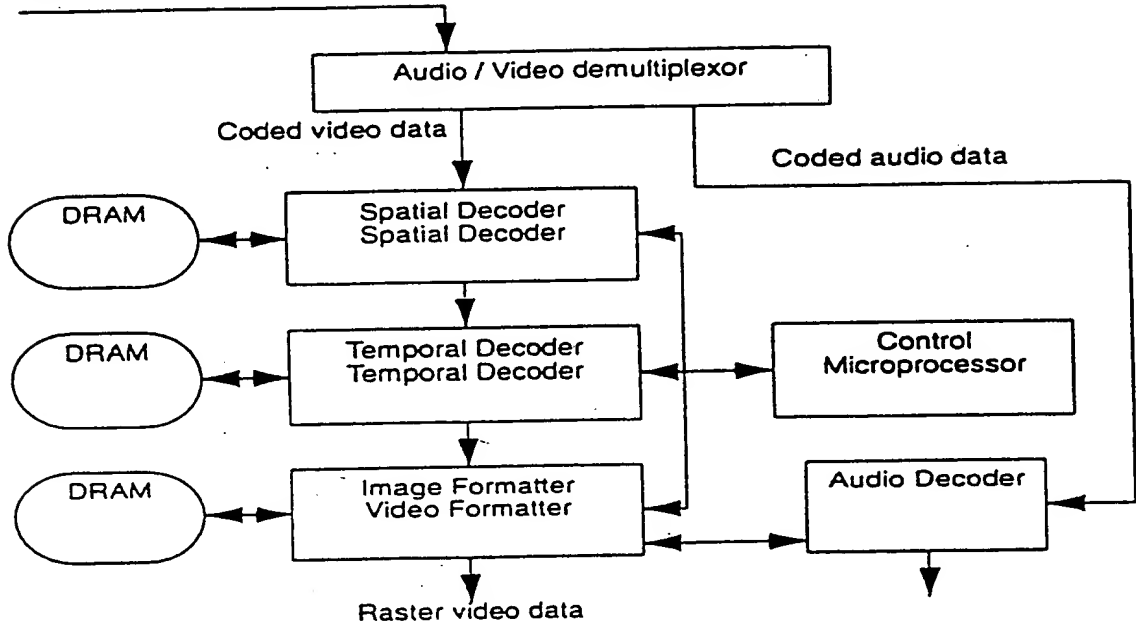


FIG.29

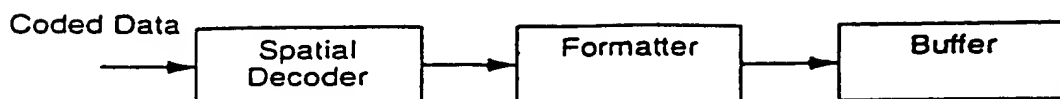


FIG.30

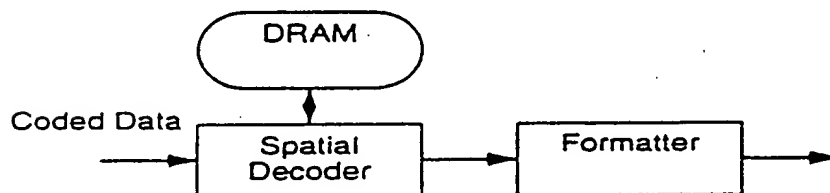


FIG.31

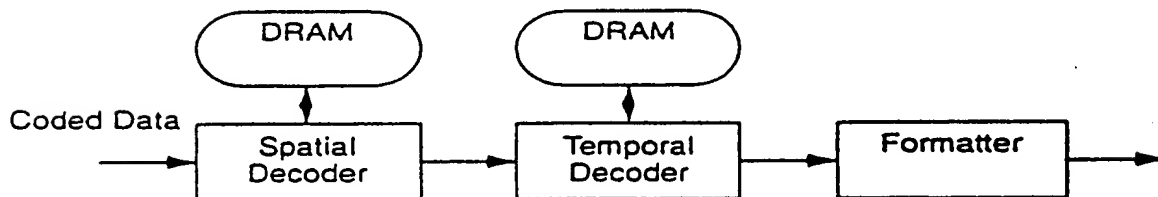


FIG.32

00771062 012001

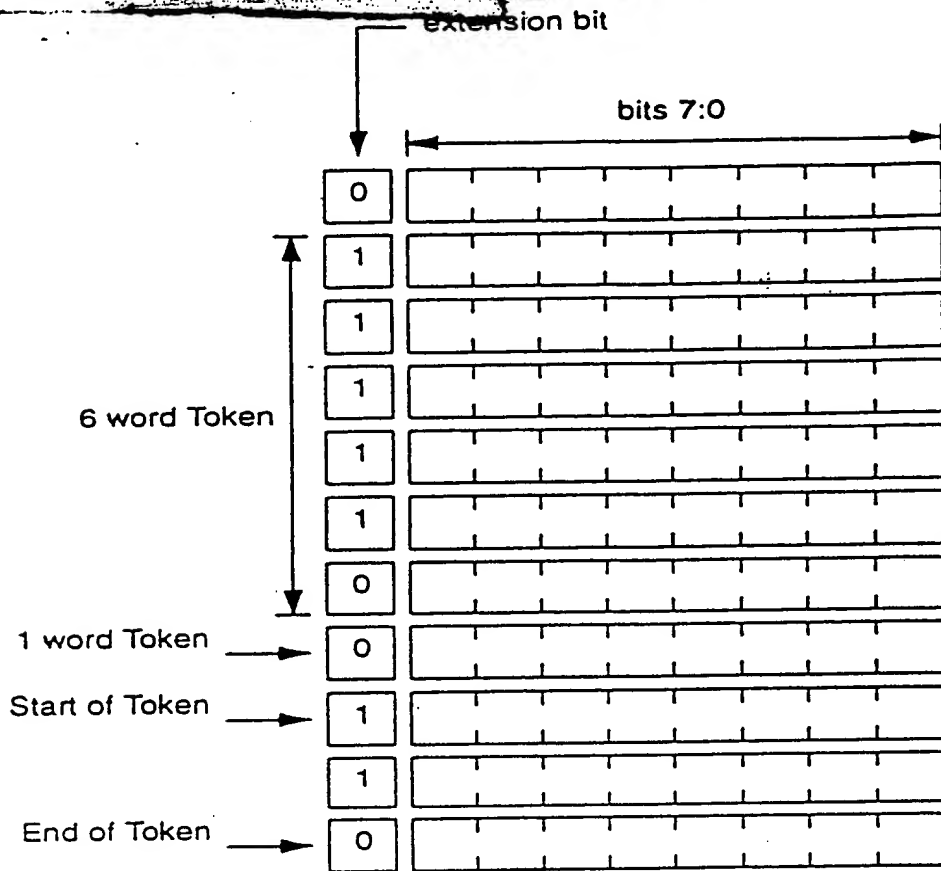


FIG.33

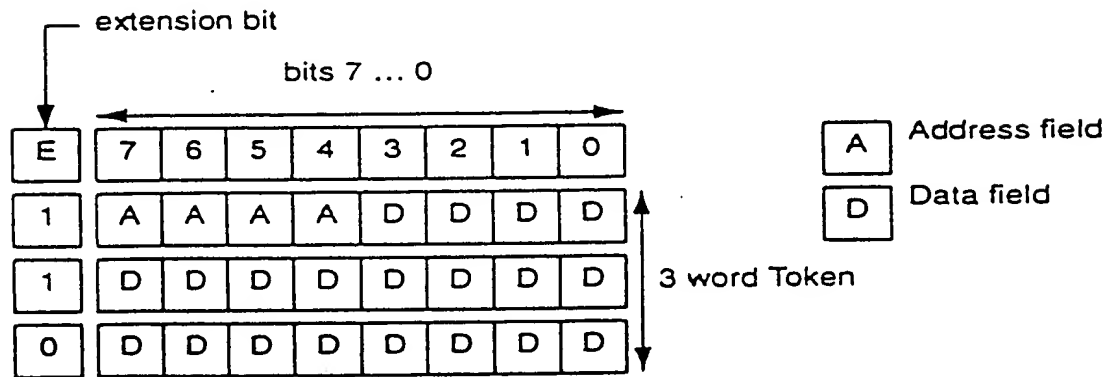


FIG.34



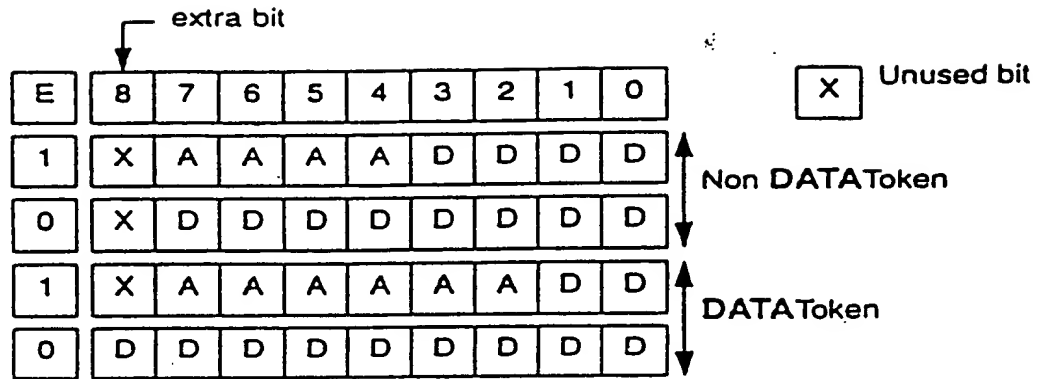
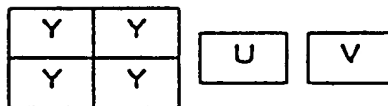


FIG.35



MPEG 4:2:0  
macroblock

FIG.36A



JPEG 2:1:1  
macroblock

FIG.36B

00771062.012901

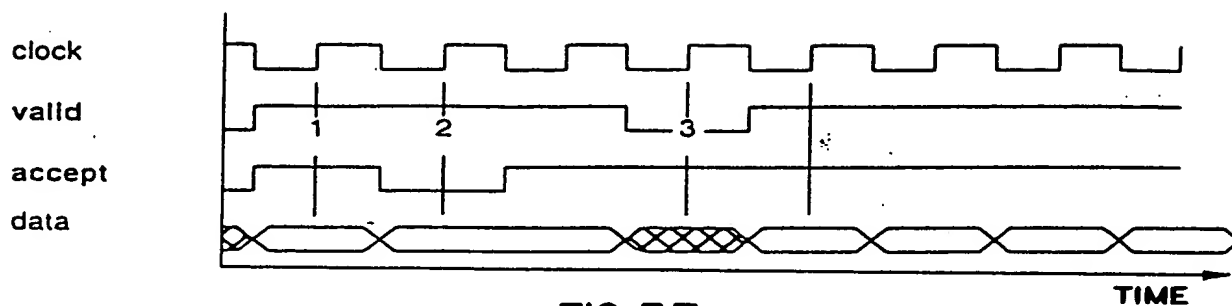


FIG.37

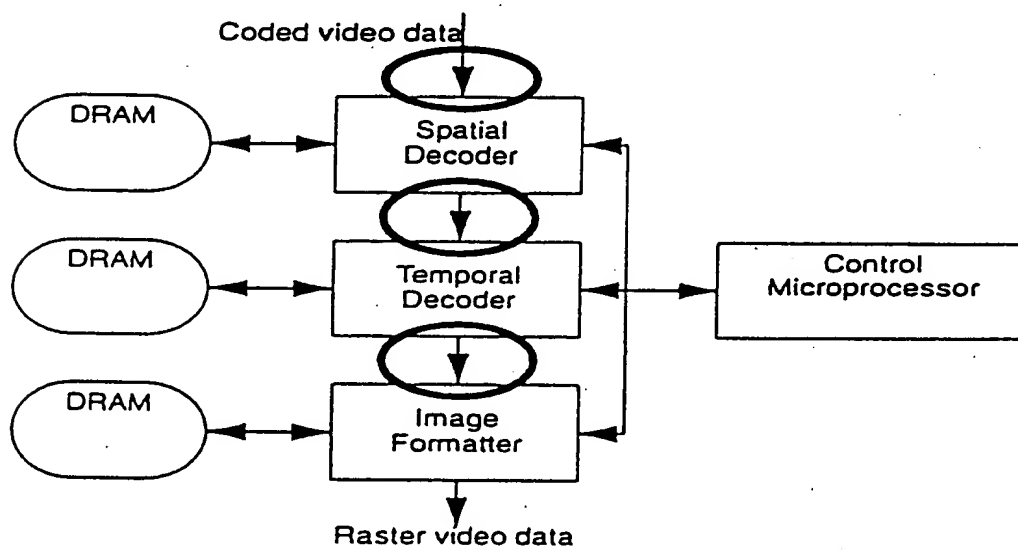


FIG.38

00771062.012001

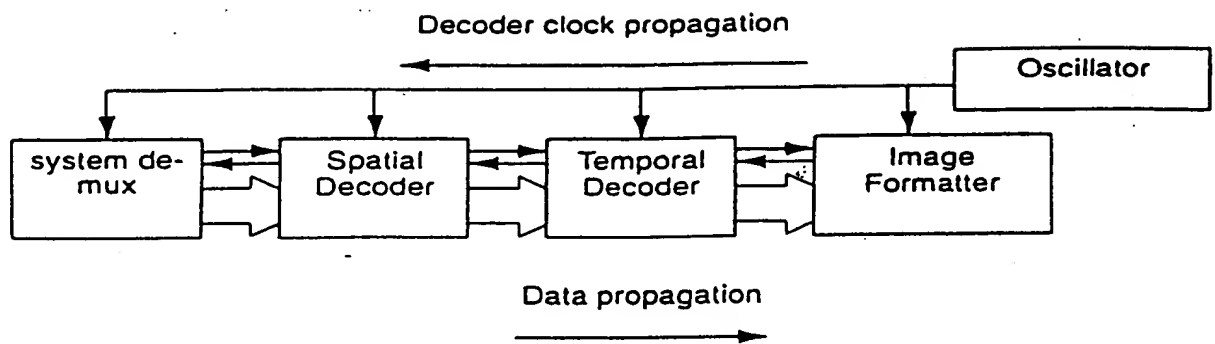


FIG.39

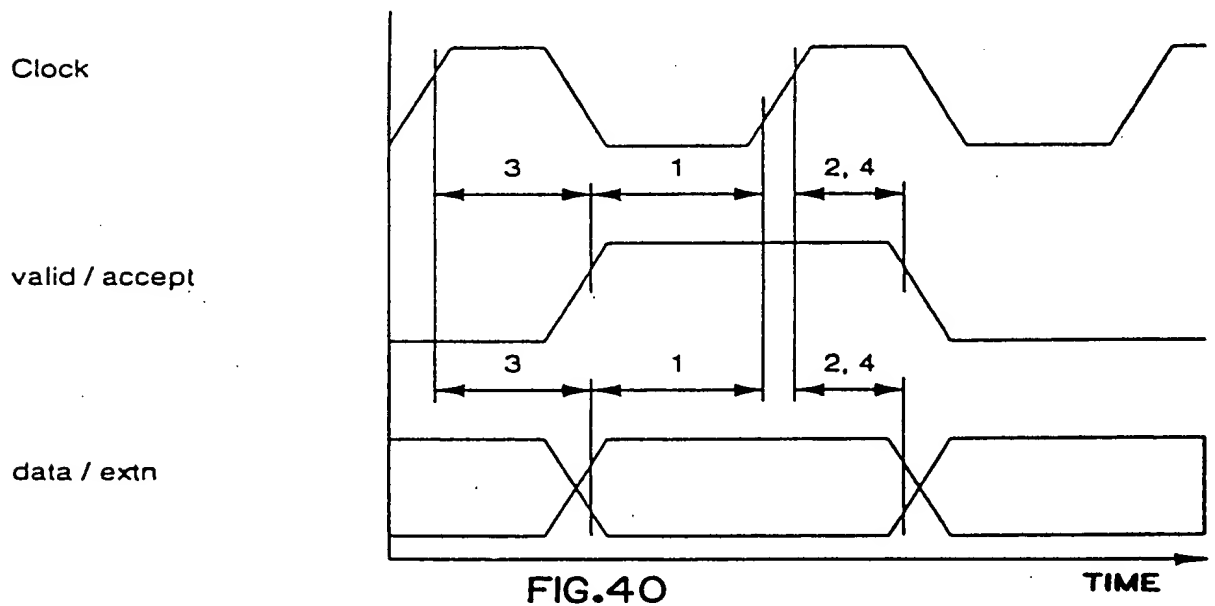


FIG.40

09774052 012001



FIG. 41

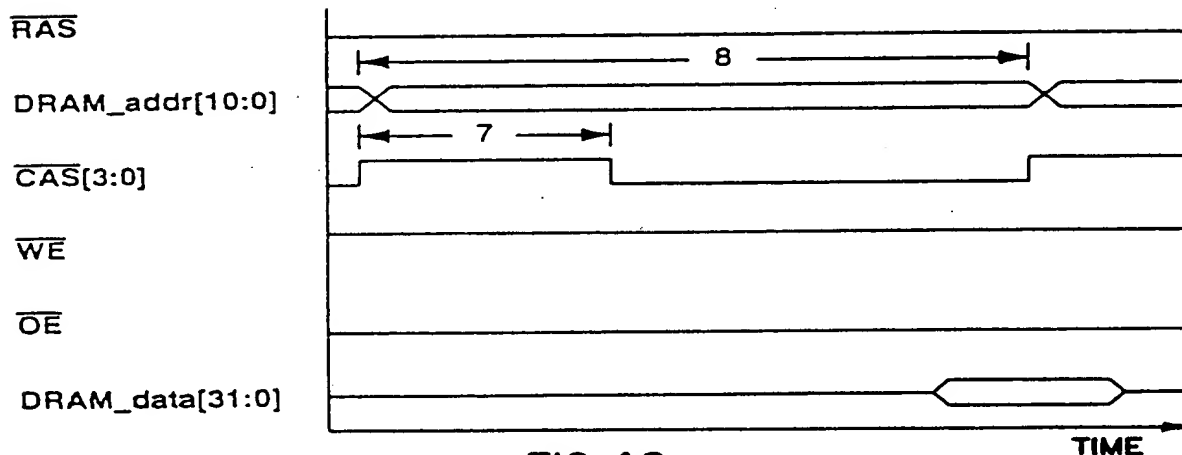


FIG. 42

00771062.012001

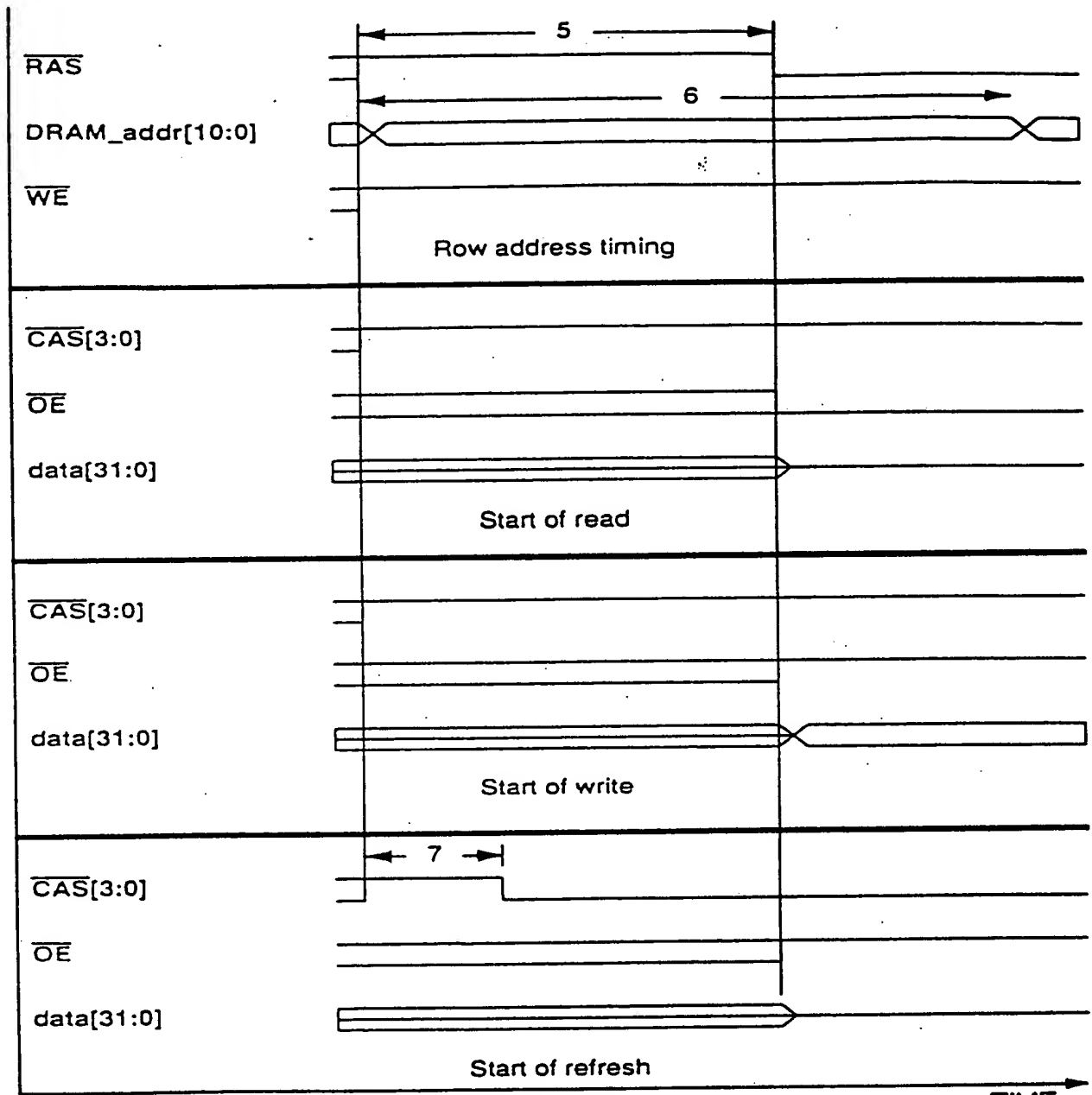


FIG.43

TIME

00774062 013001

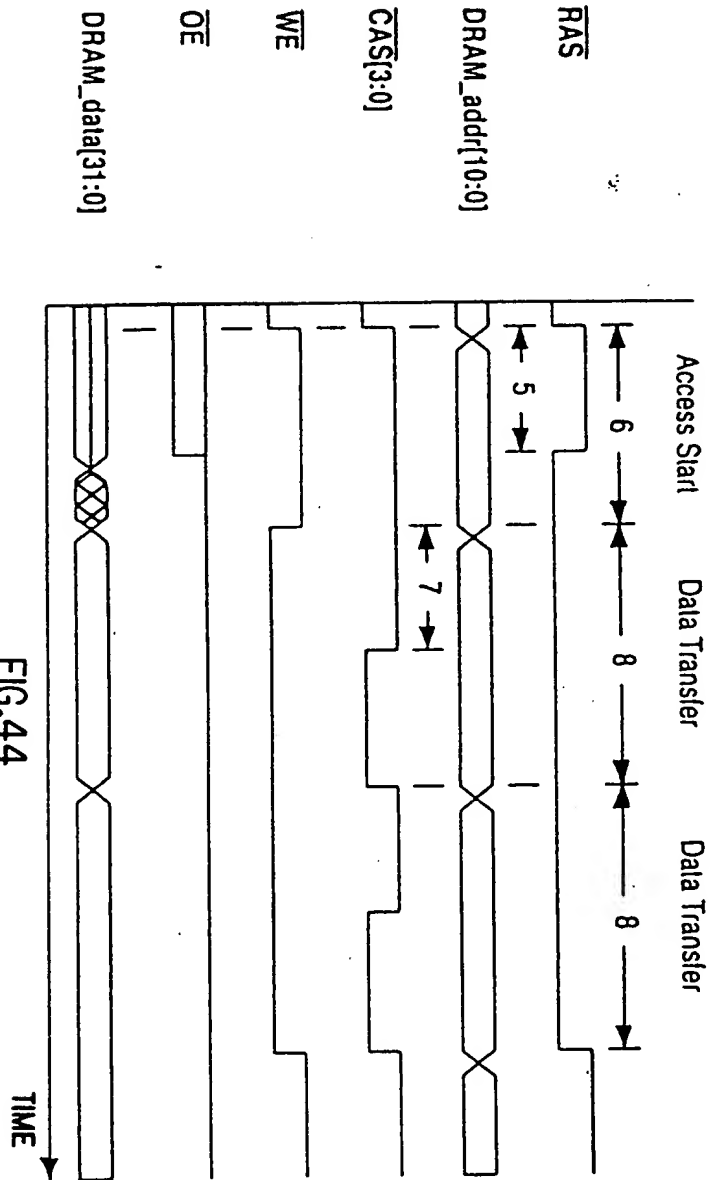


FIG.44

00771052\_012001

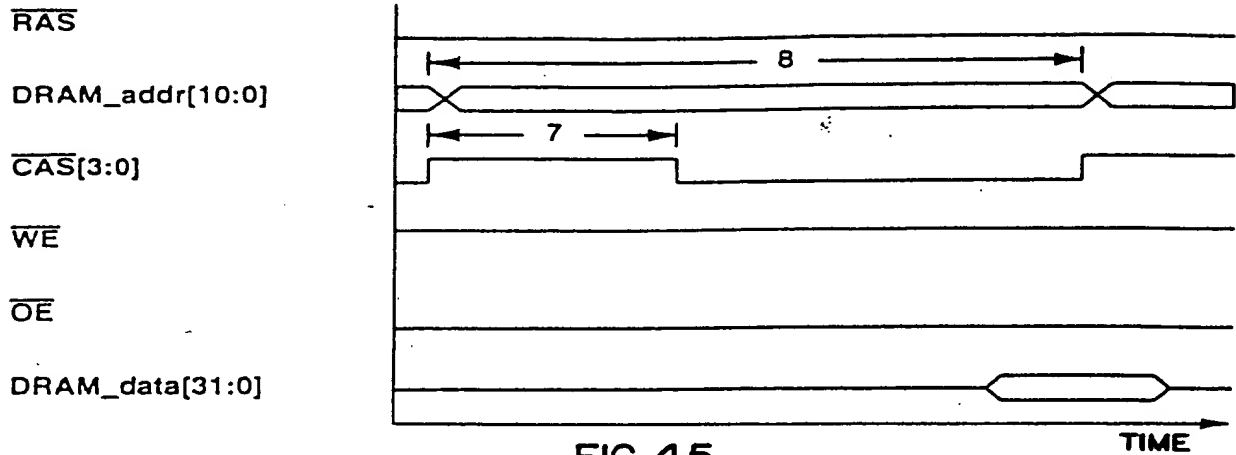


FIG. 45

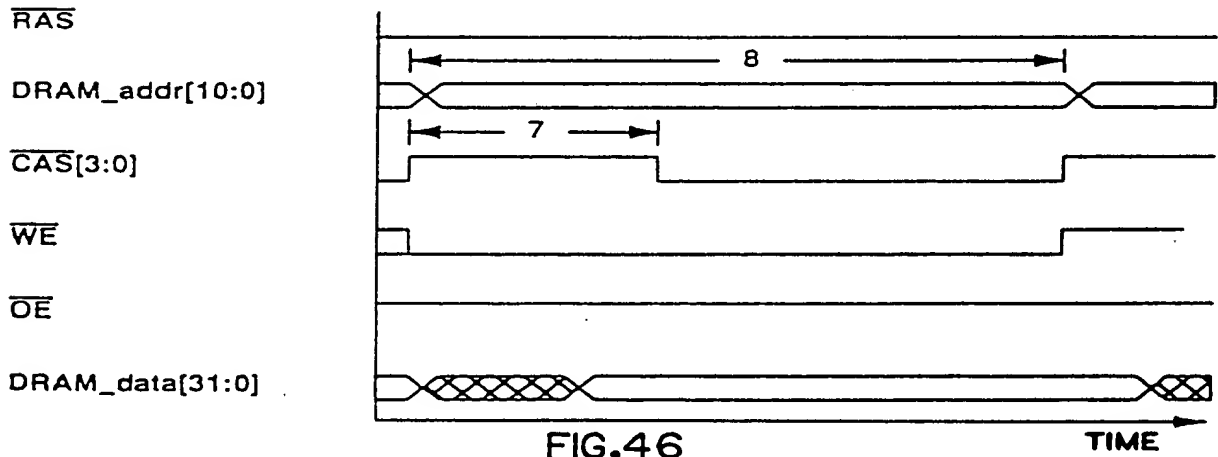


FIG. 46

09771062-012004

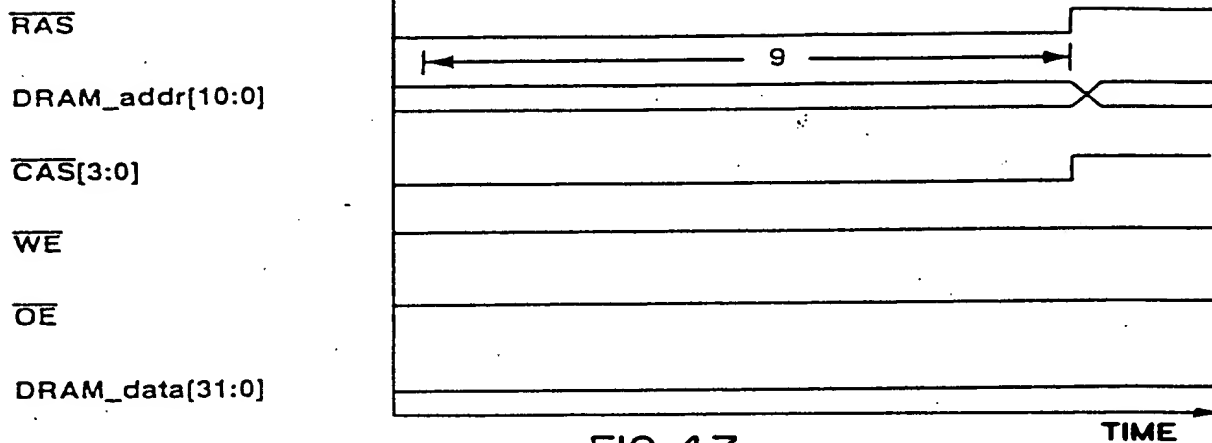


FIG.47

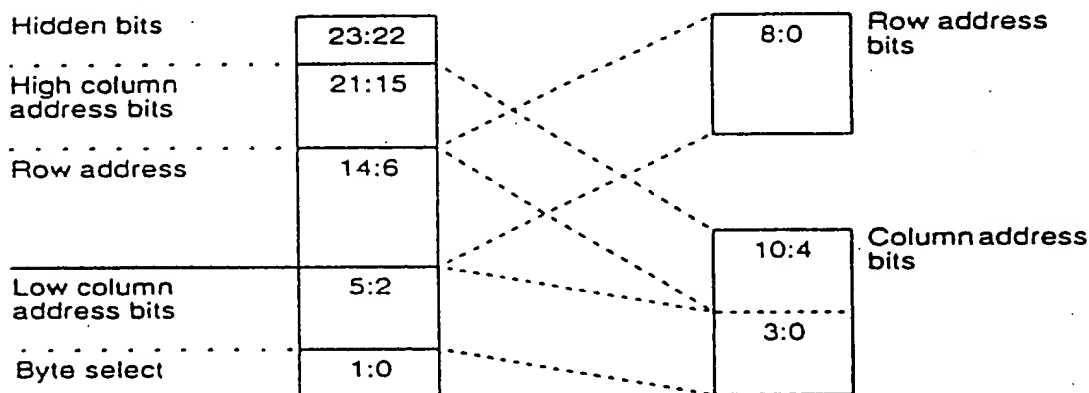


FIG.48

0071062-012001



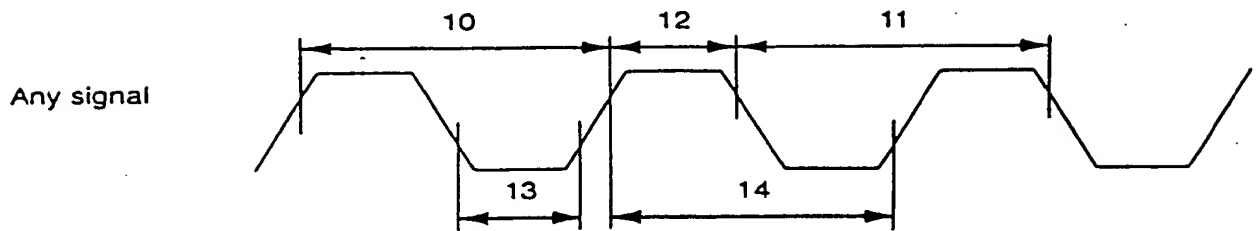


FIG. 49

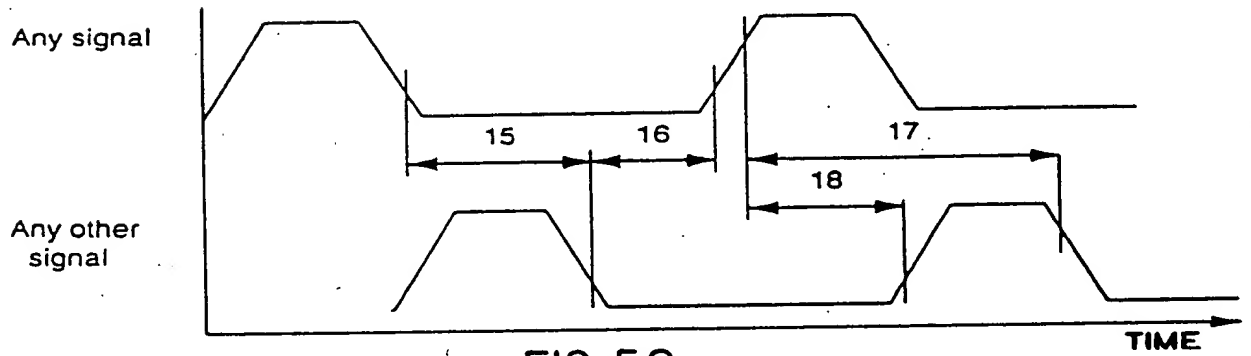
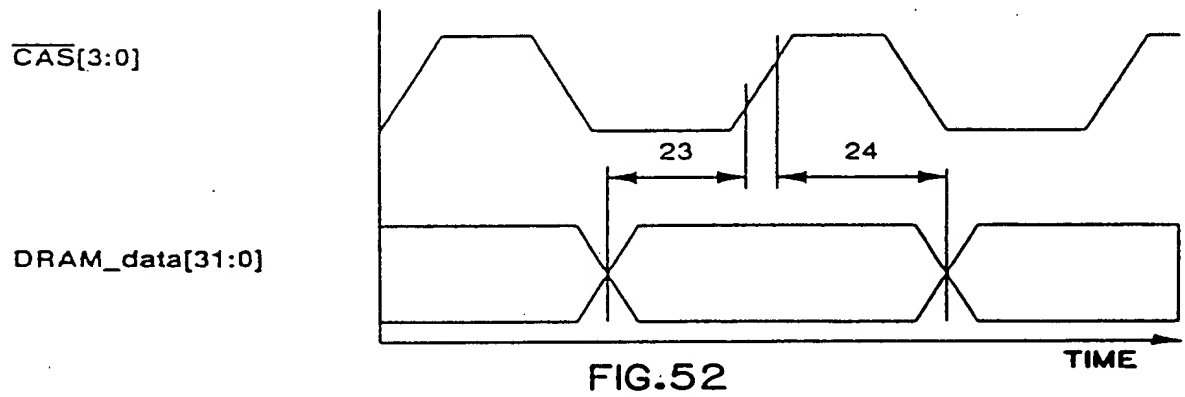
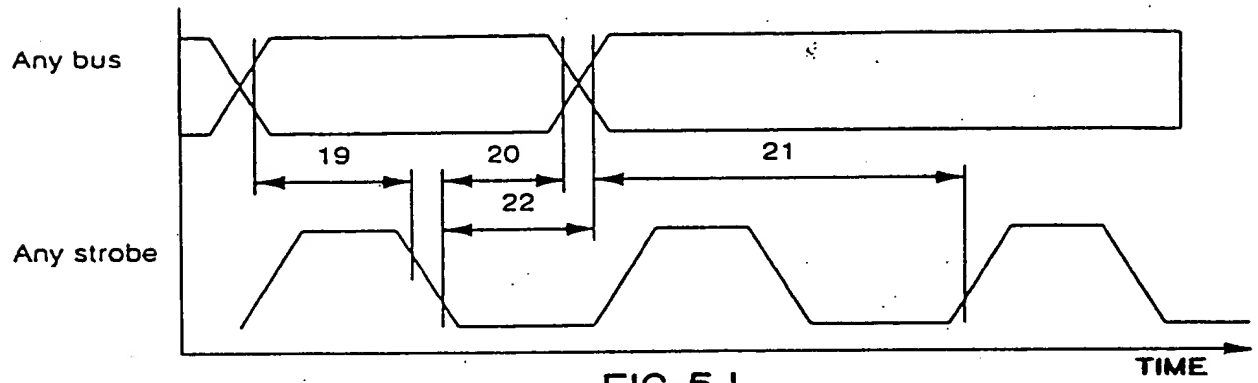
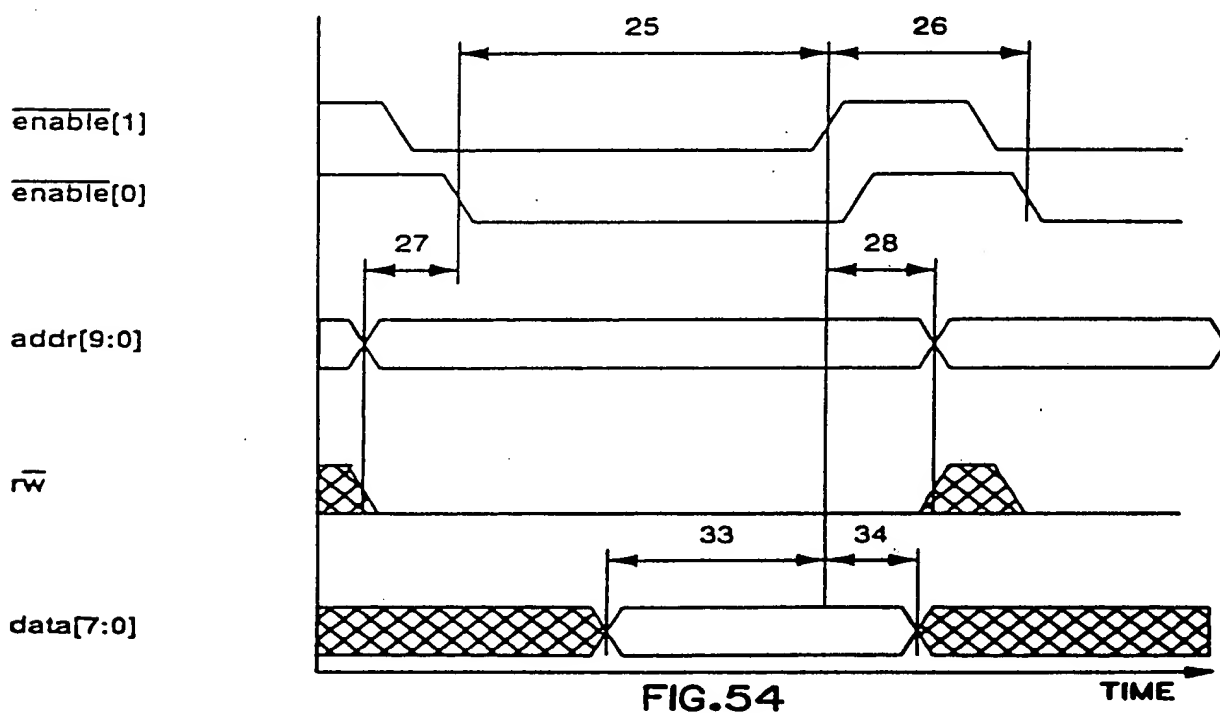
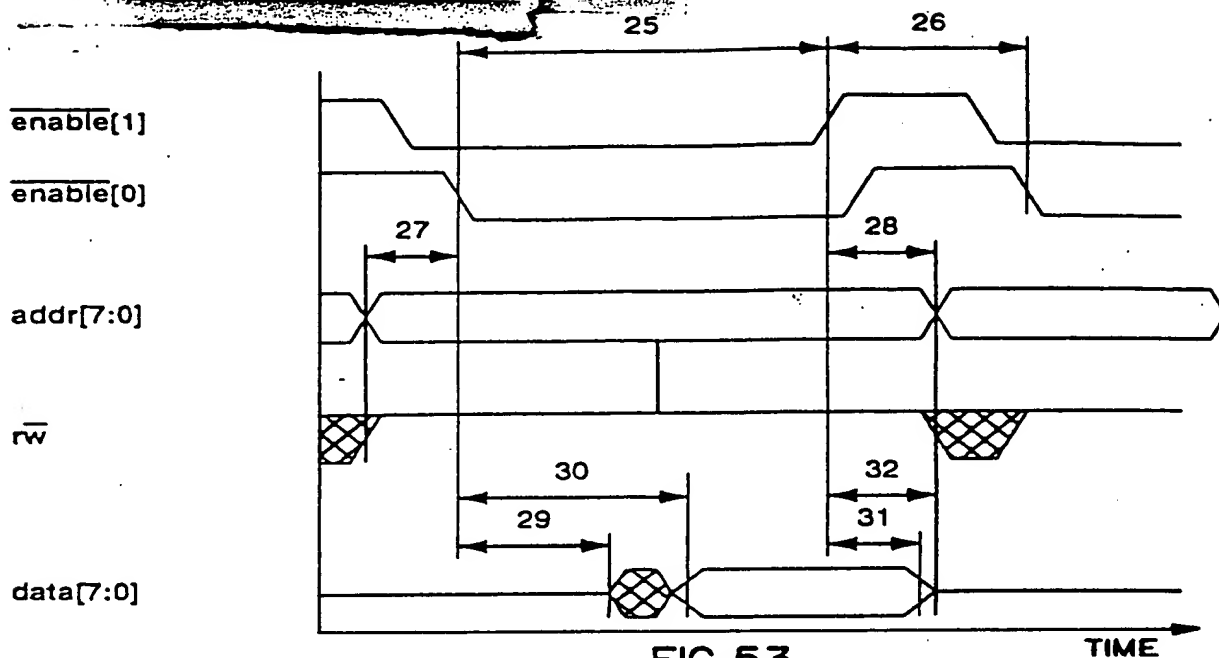


FIG. 50

09771062-012001

00771062-012901





0074062-013004

00771052-013901

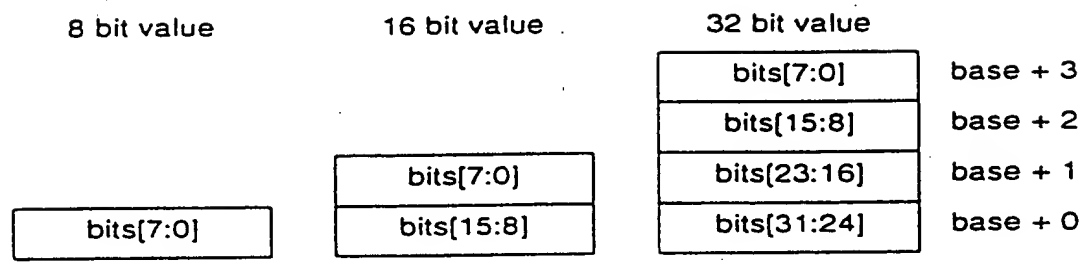


FIG.55

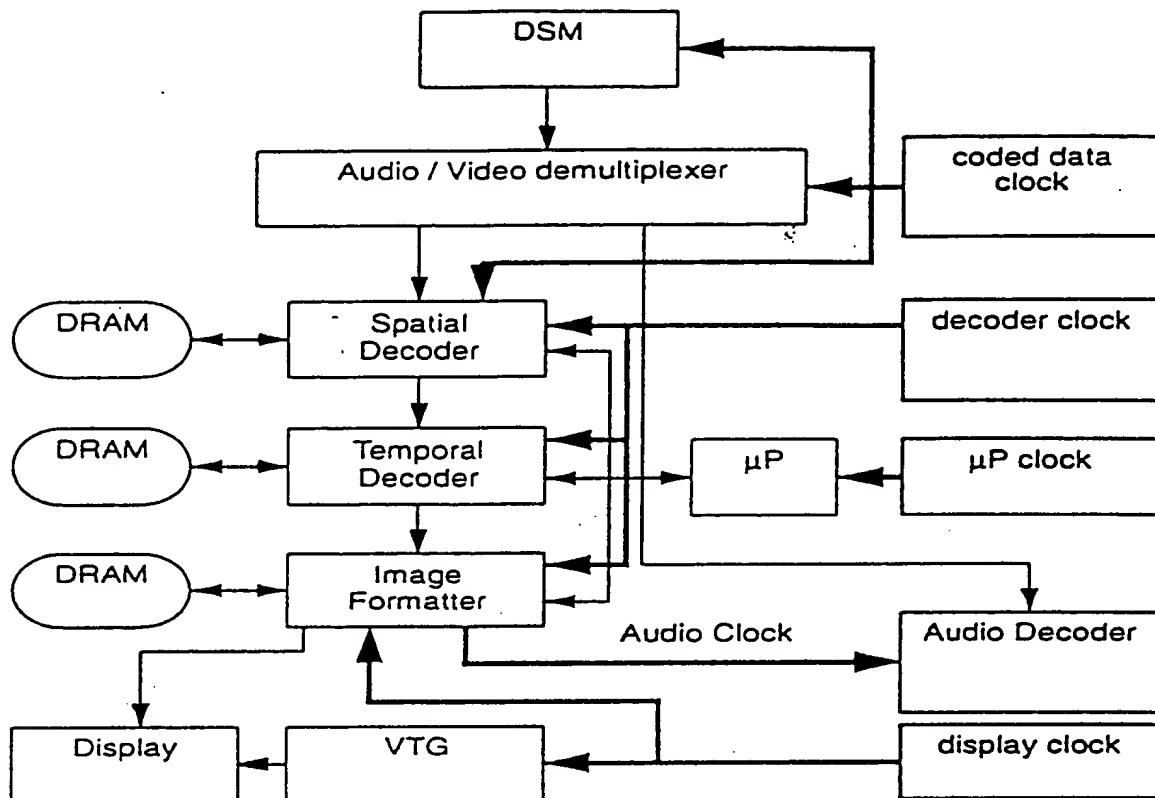


FIG.56

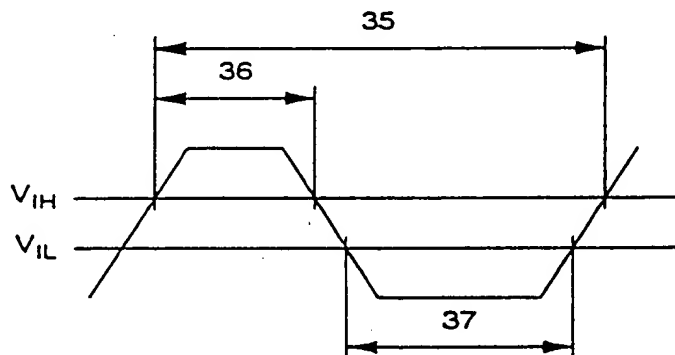


FIG.57

00771062-012001

0071053-01000  
F00010-29012200

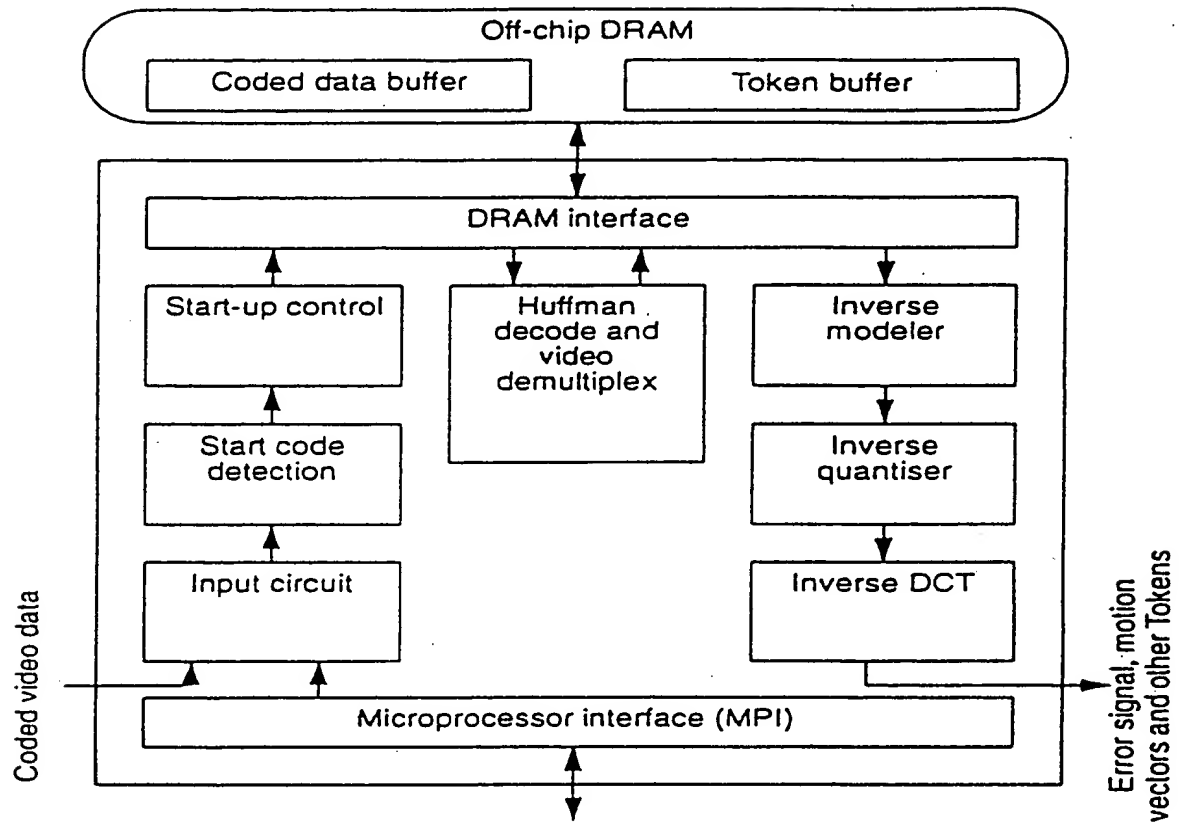


FIG.58

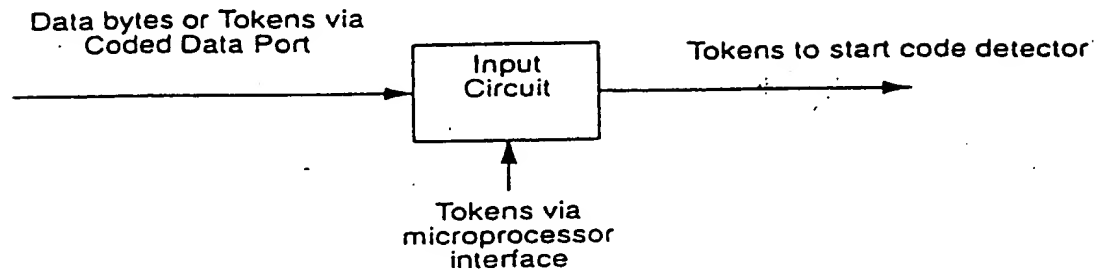


FIG.59

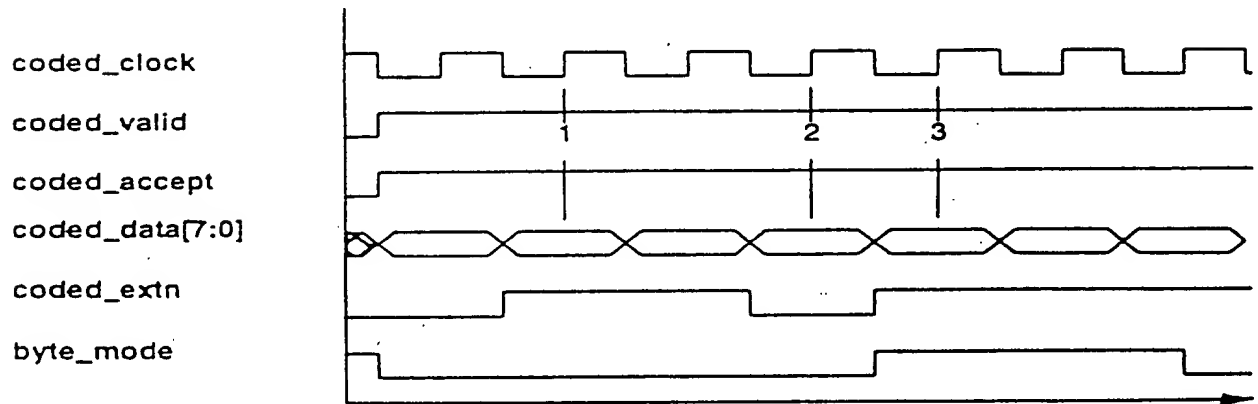


FIG.60

TIME

00771062 013001

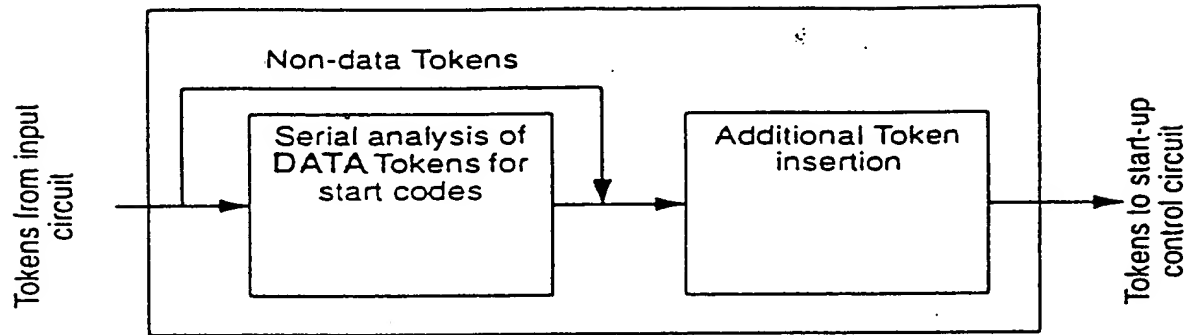


FIG. 61

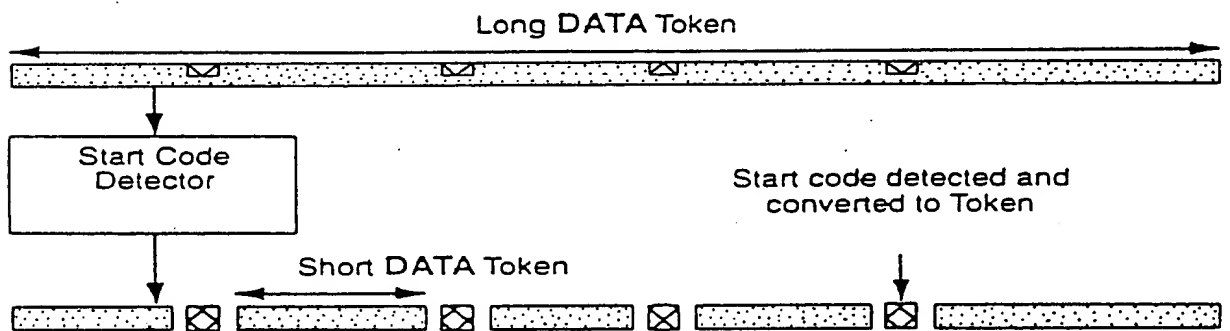


FIG. 62



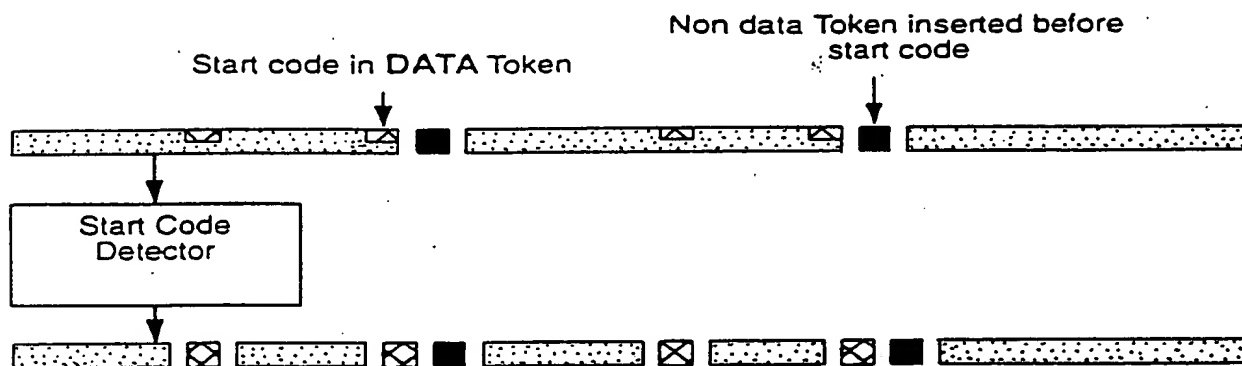


FIG.63

This looks like an MPEG picture start

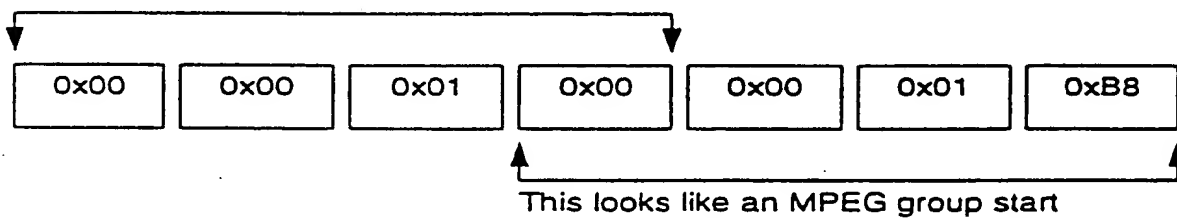


FIG.64

This looks like an MPEG slice start (0x28)

00000000 00000000 00000001 00101000 00000000 00000000 00001000

This looks like the prefix for a non-aligned  
MPEG start code

FIG.65

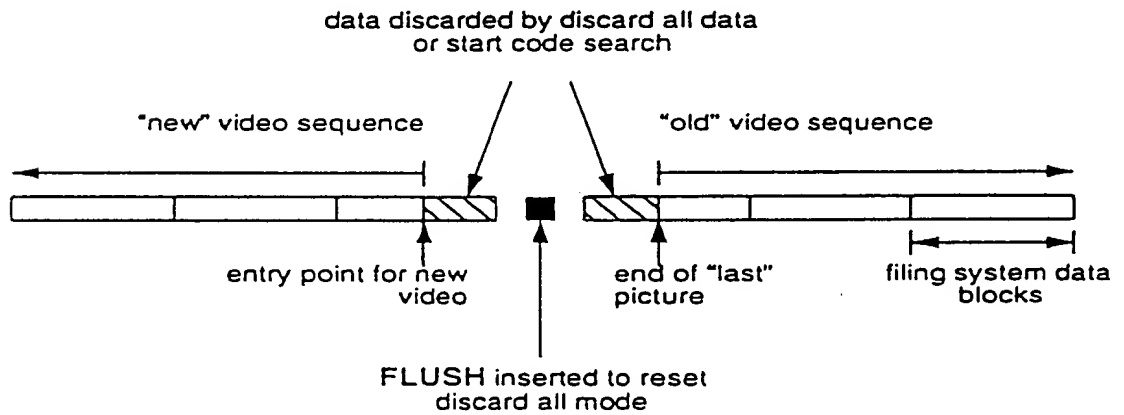


FIG.66

00771062.012001

00771062.012001

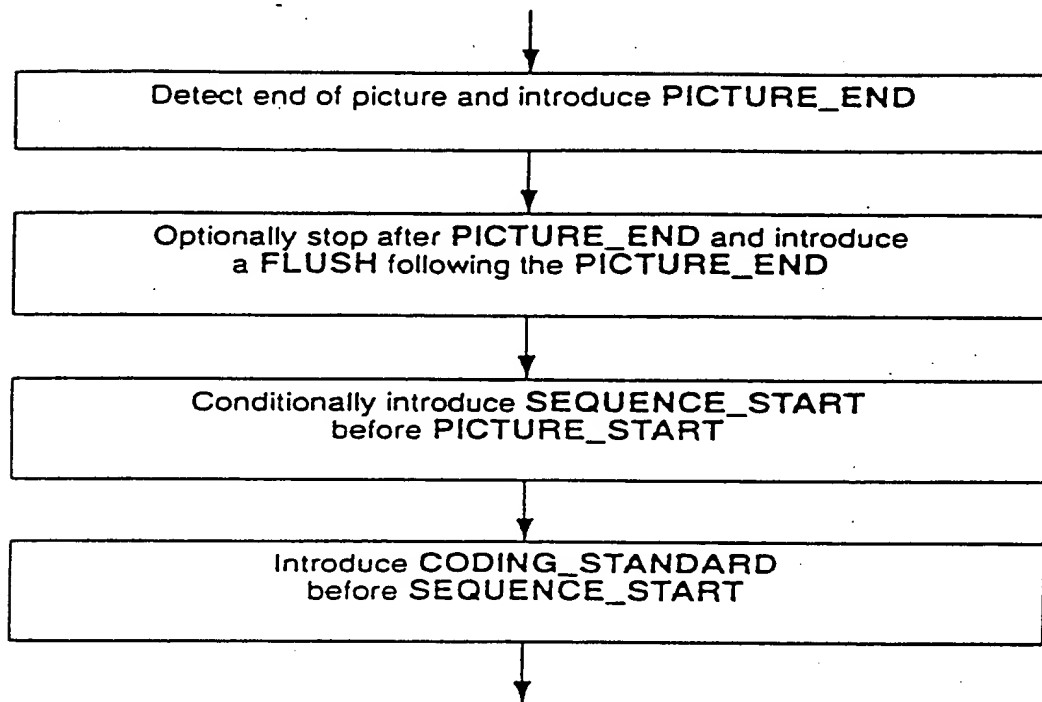


FIG.67

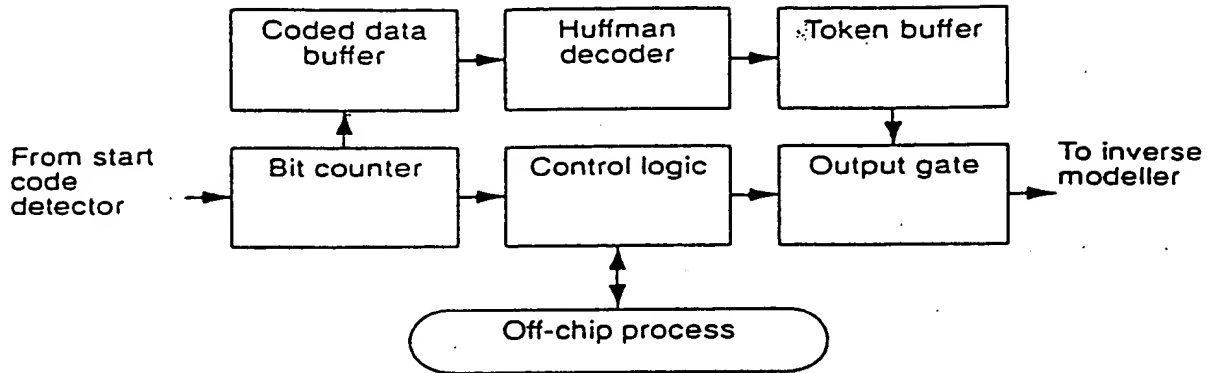


FIG.68

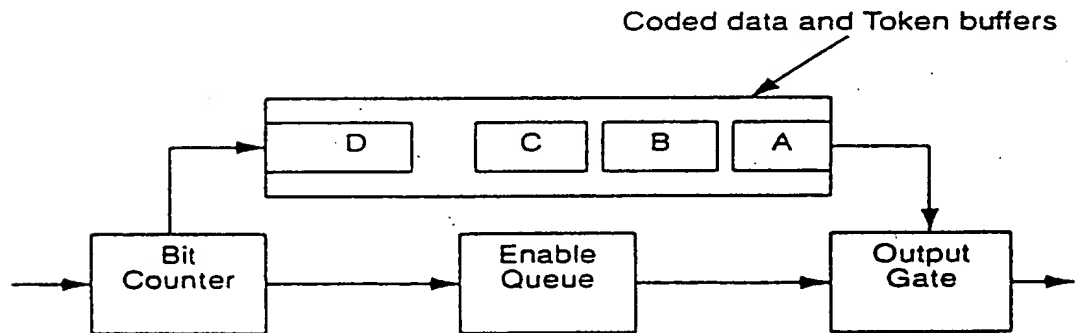


FIG.69

00771052.012001

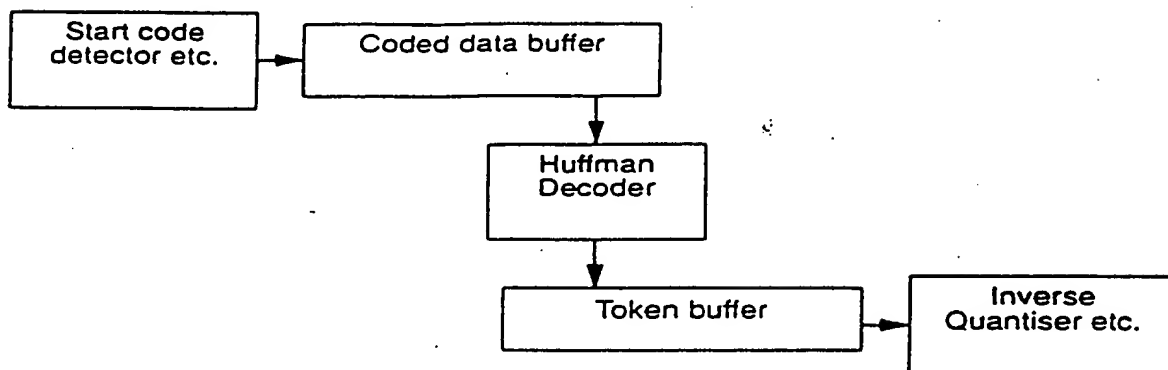


FIG.70

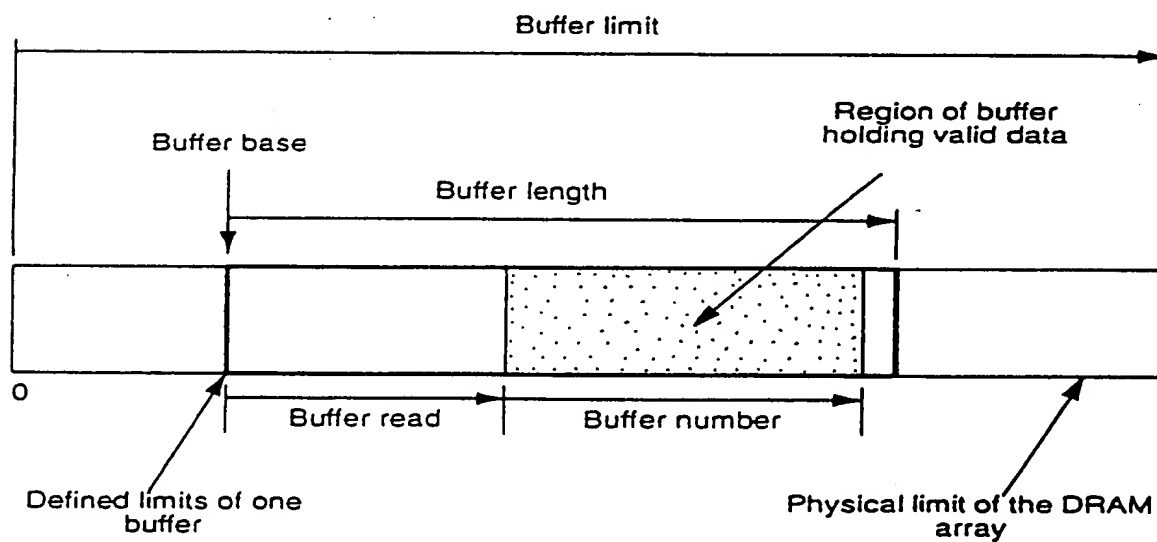


FIG.71

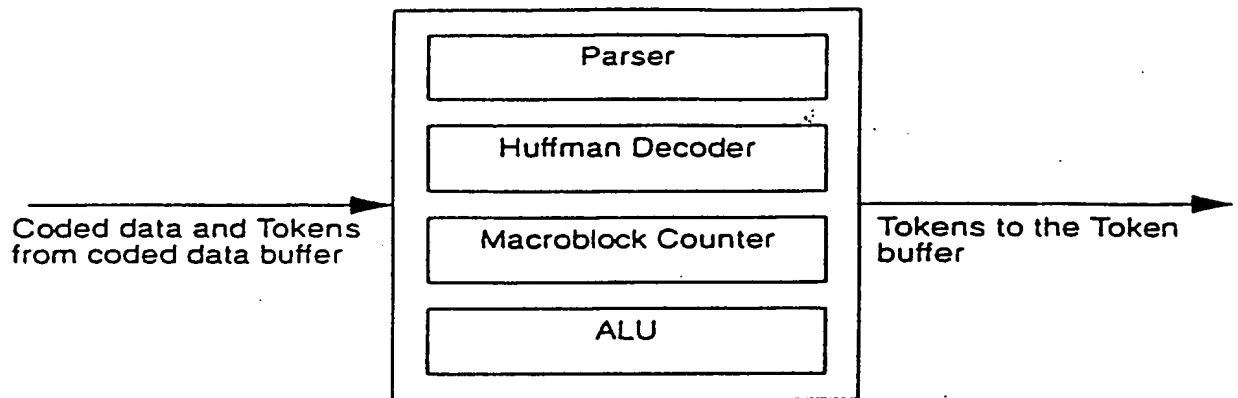


FIG.72

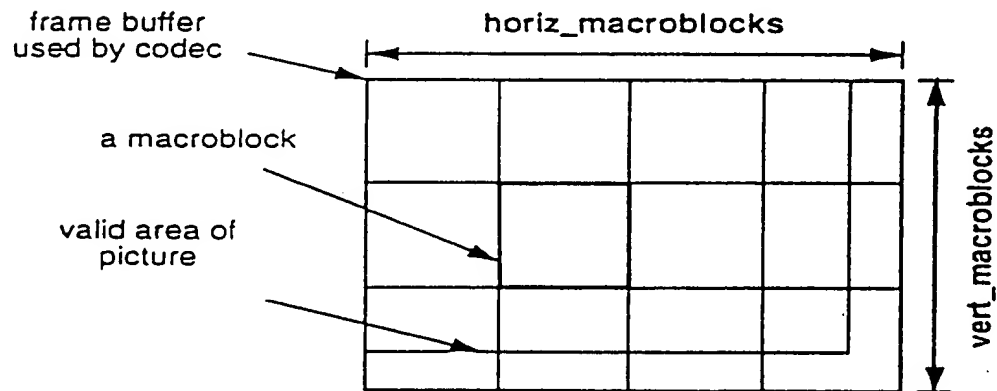


FIG.73

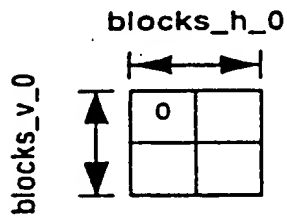


FIG.74A

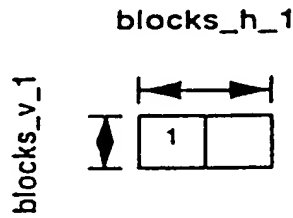


FIG.74B

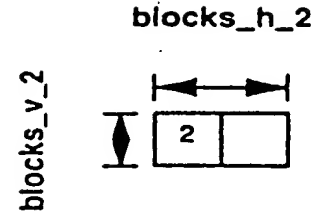


FIG.74C

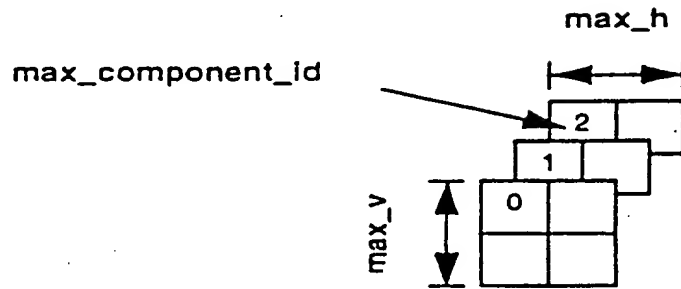


FIG.74D

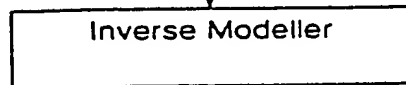
$$\left\{ \begin{array}{l} \text{horiz\_macroblocks} = \frac{\text{horiz\_pels} + 15}{16} \\ \text{vert\_macroblocks} = \frac{\text{vert\_pels} + 15}{16} \end{array} \right.$$

FIG.75

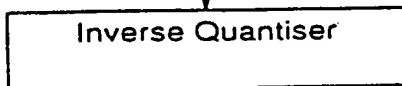
00771062 012001

From Token buffer

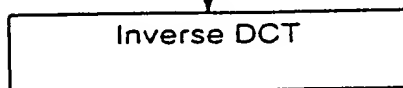
Run and Level representation of quantised coefficients



Expanded to 8x8 blocks of quantised coefficients



8x8 blocks of coefficients

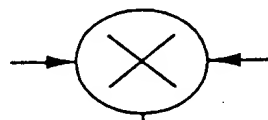


8x8 blocks of pixel information

To output of Spatial Decoder

FIG.76

Quantised values



Scale factor

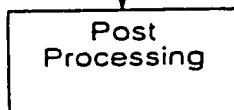


FIG.77



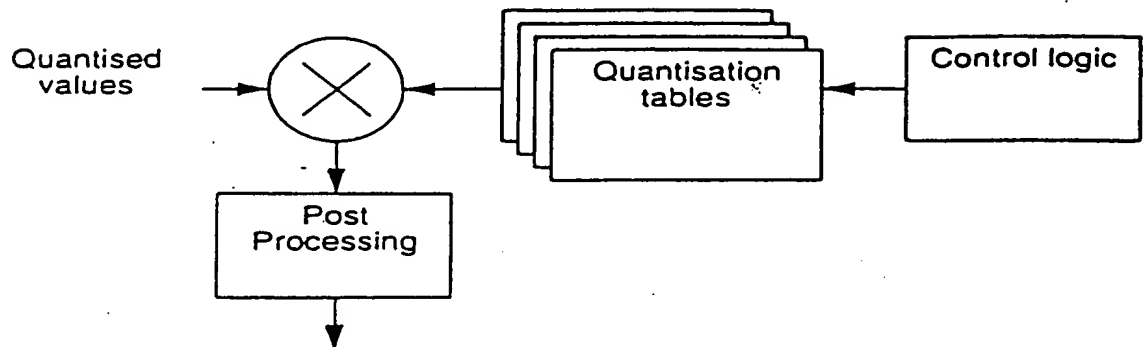


FIG. 78

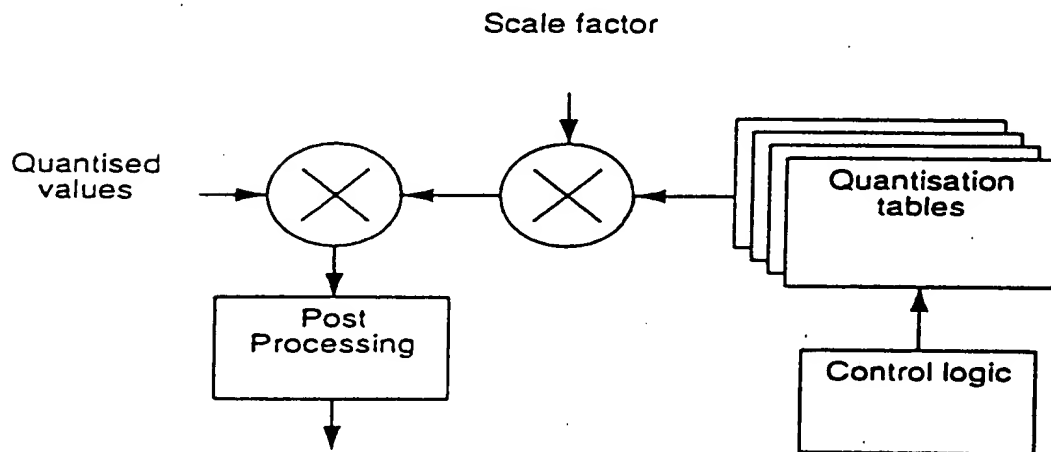


FIG. 79

00771062-012001

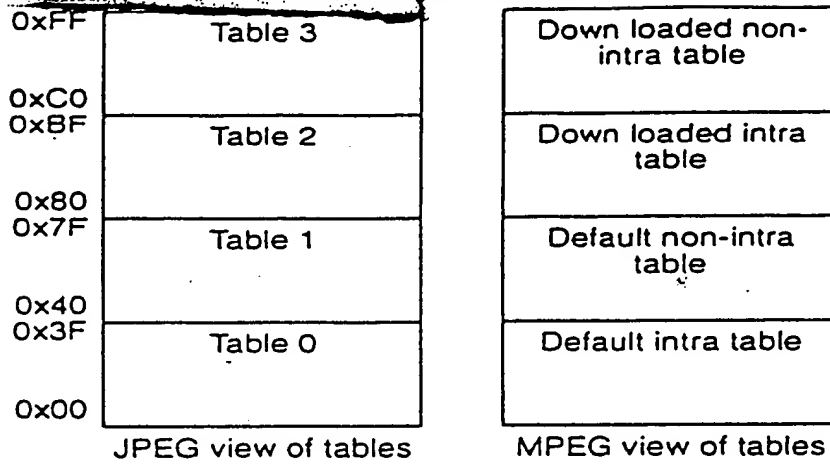


FIG.80

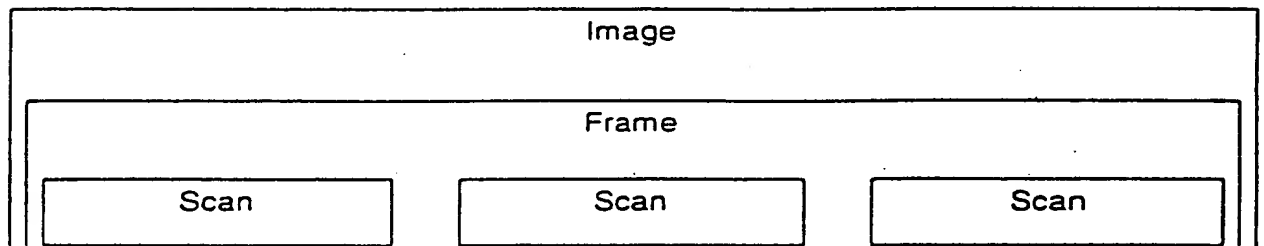


FIG.81

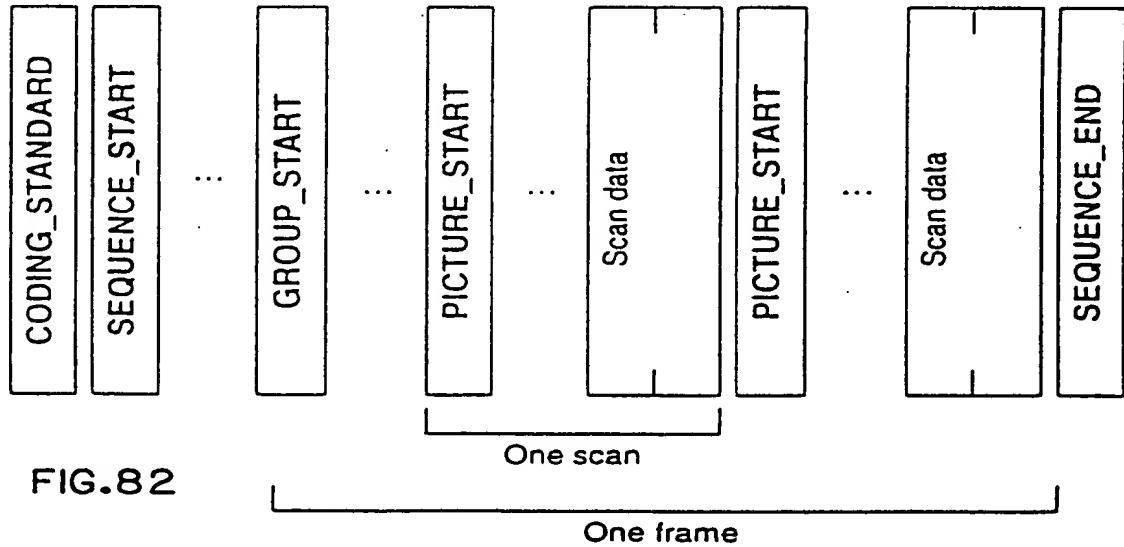


FIG.82

00771062 040001  
F062F0 2907760

00774052 03074200

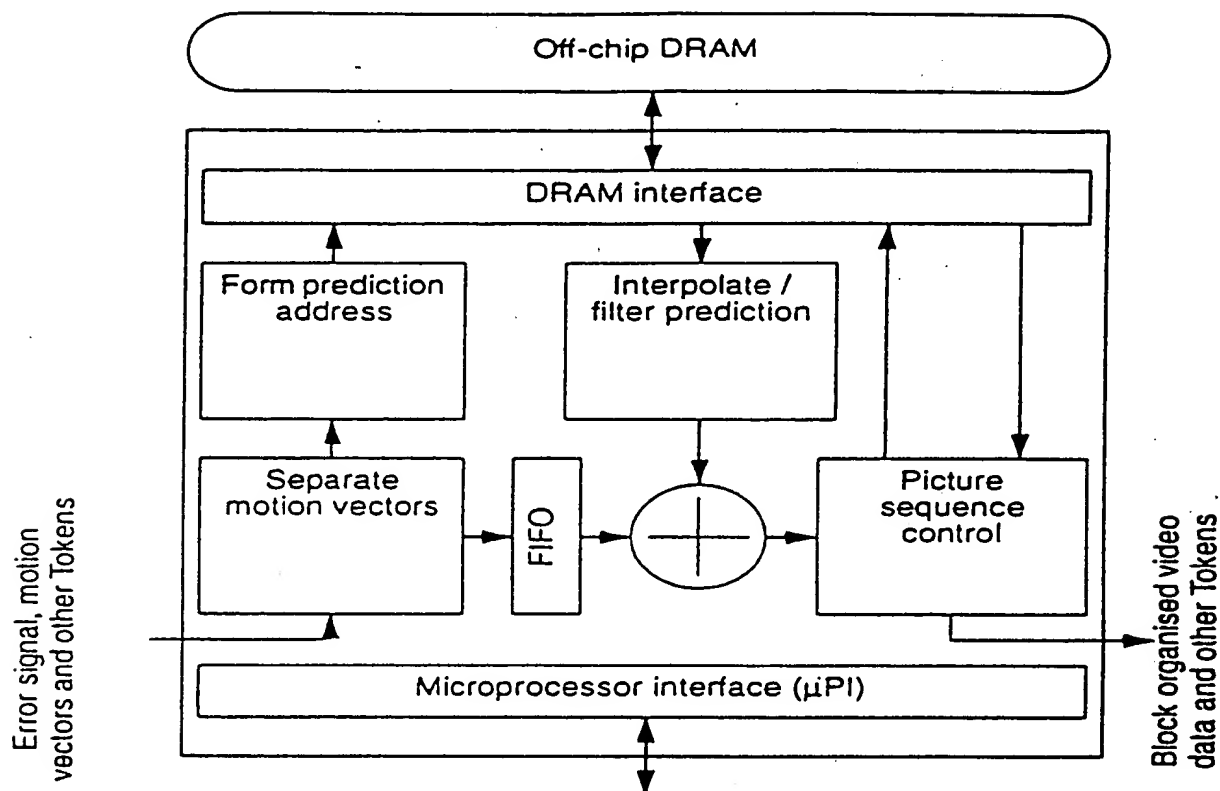


FIG.83

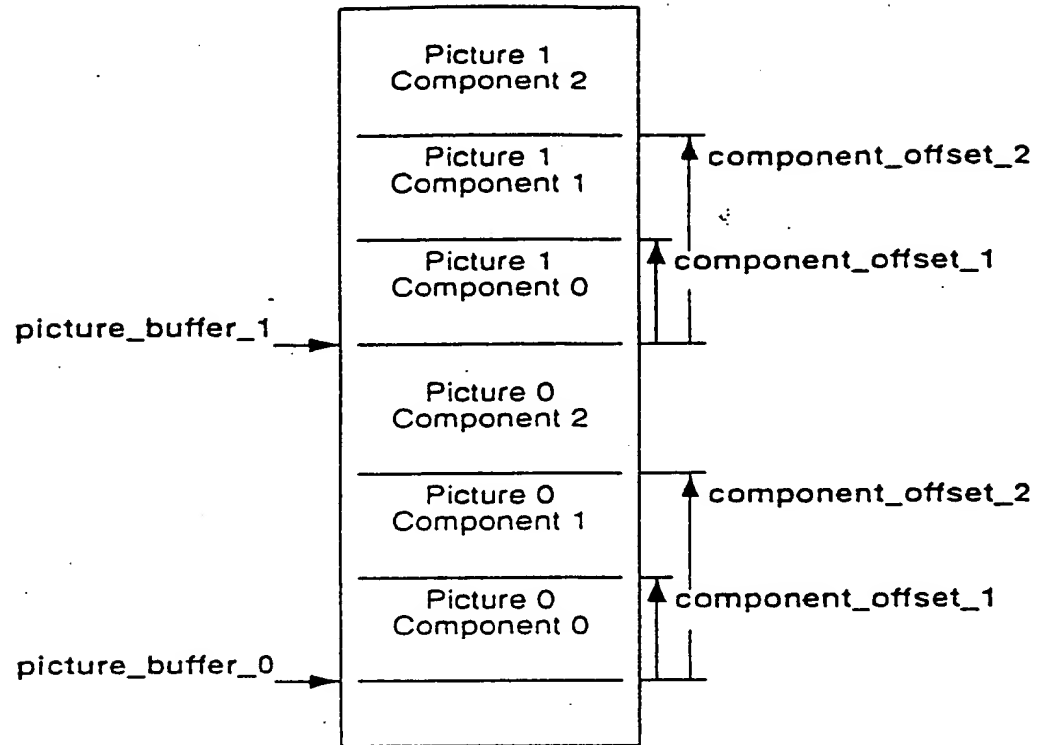


FIG.84

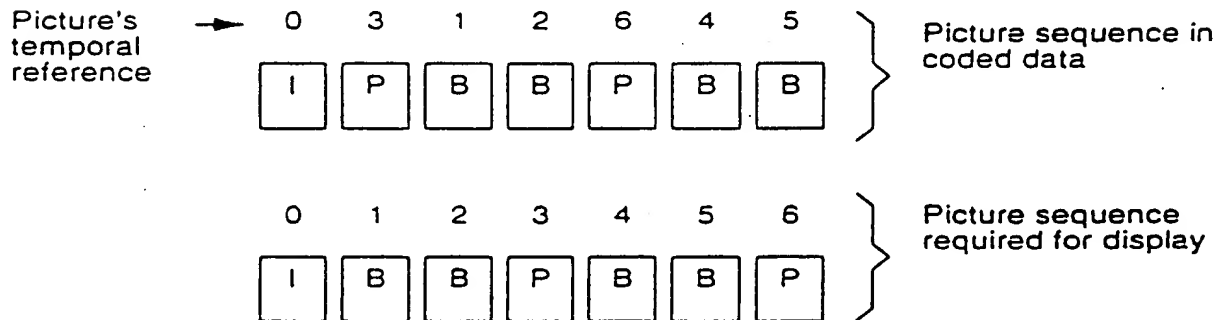


FIG.85

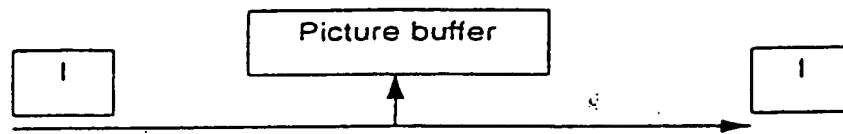


FIG. 86

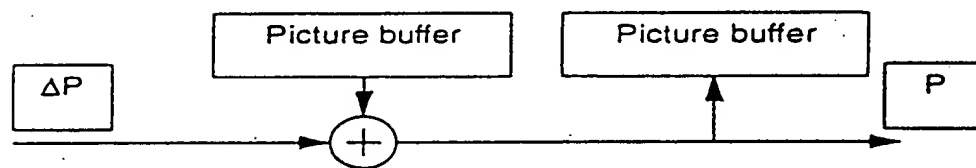


FIG. 87

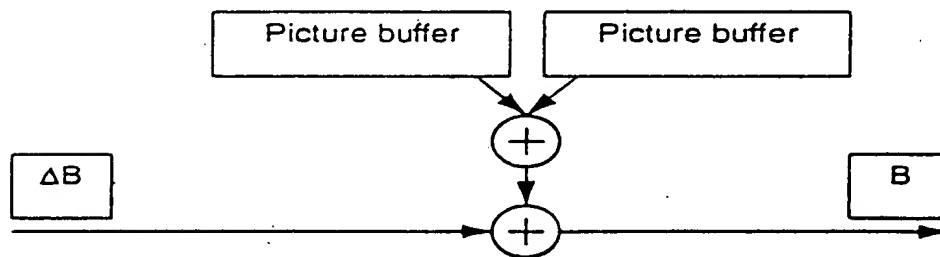


FIG. 88

00771062-012001

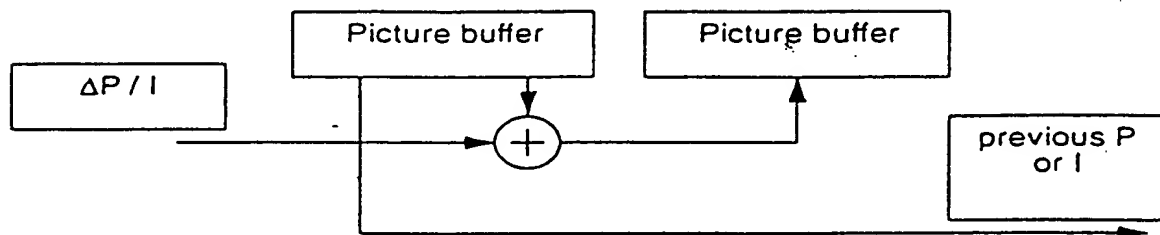


FIG.89

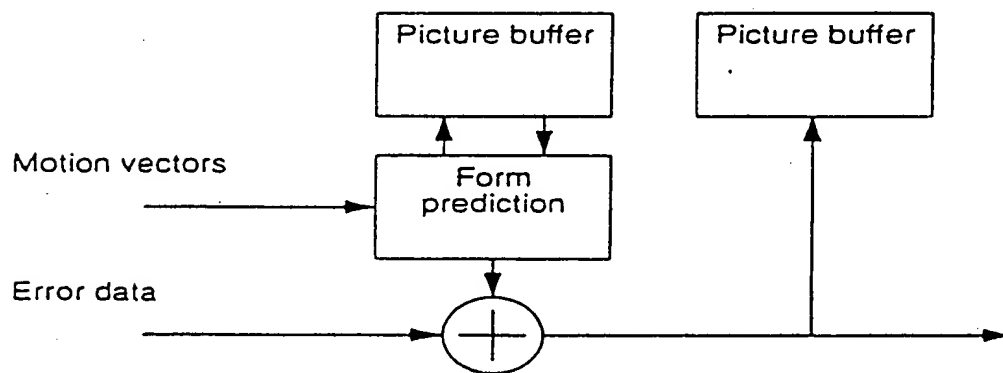


FIG.90

00771062-012901  
F062T0-290T4260

00771062 012901

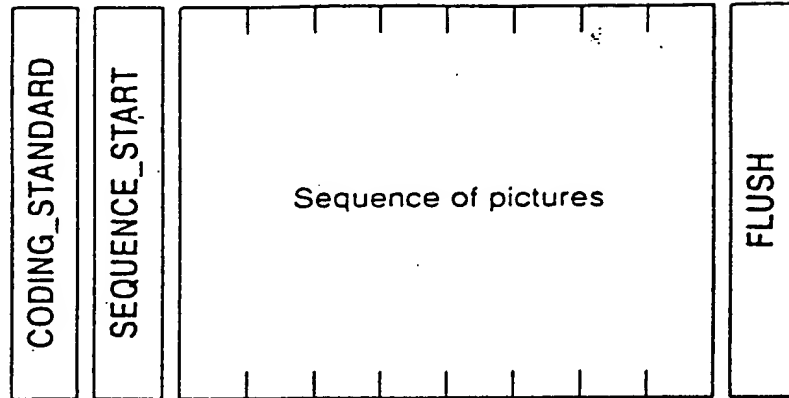


FIG.9 1

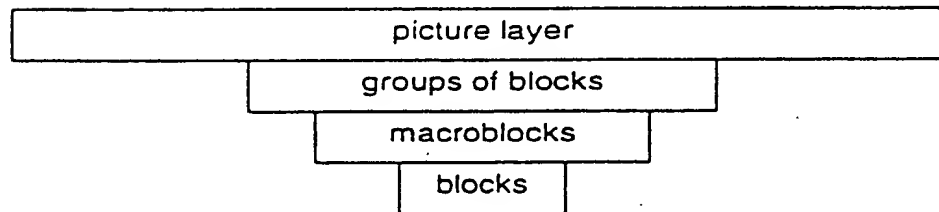


FIG.92

100270-2907260

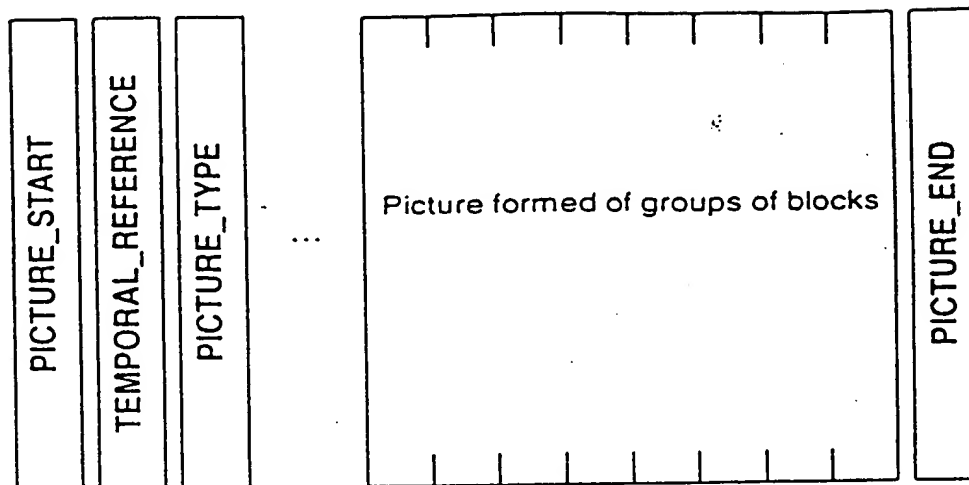


FIG.93

CIF		QCIF	
0	1	0	
2	3	2	
4	5	4	
6	7		
8	9		
10	11		

FIG.94



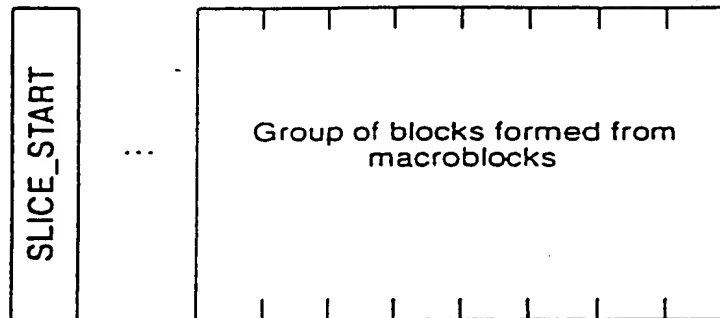


FIG.95

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22
23	24	25	26	27	28	29	30	31	32	33

FIG.96

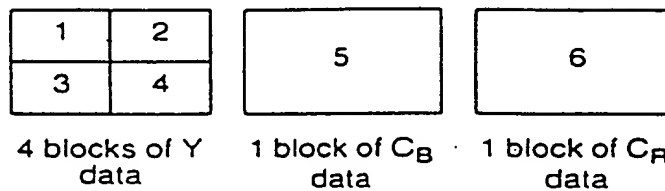


FIG.97

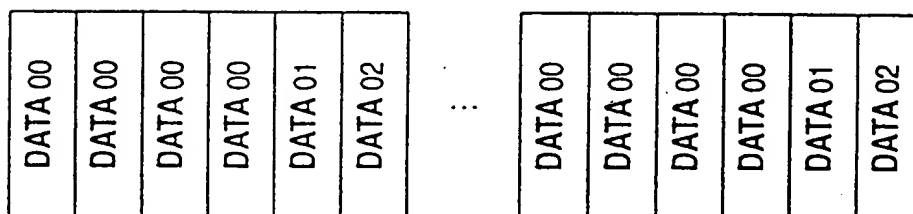


FIG.98

00771052-012001

1	2	3	4	5	6	7	8
9	10	11	12	13	14	15	16

...

59	58	59	60	61	62	63	64
----	----	----	----	----	----	----	----

FIG. 99

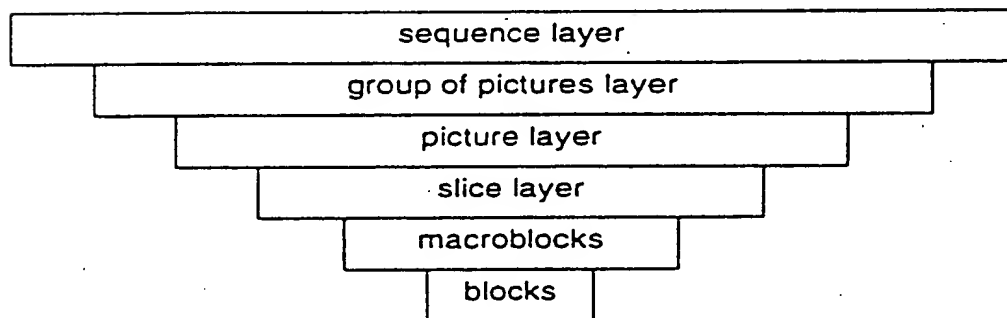


FIG. 100

0071052.012001

FIG. 101

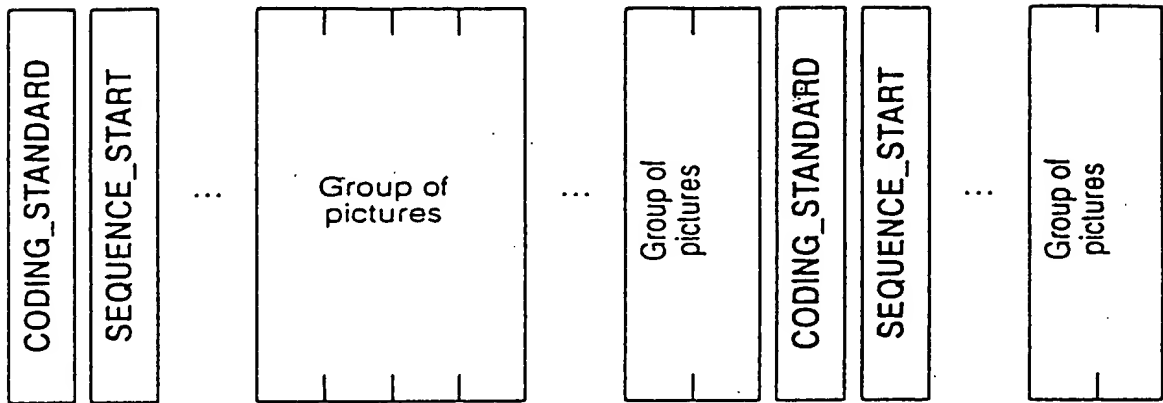


FIG. 101

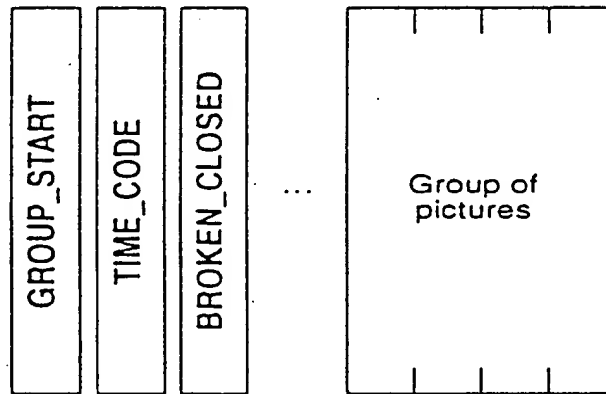


FIG. 102

00771055-012001

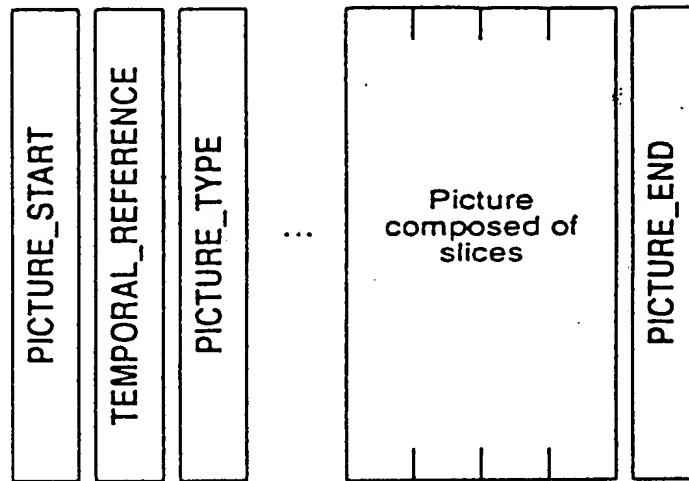


FIG. 1 03

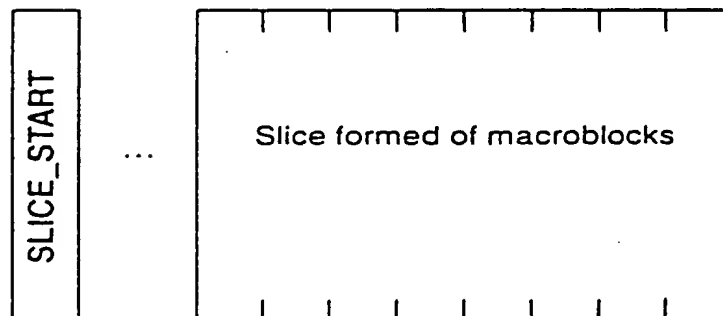


FIG. 1 04

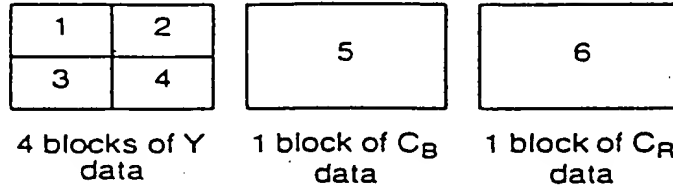


FIG. 1 O5

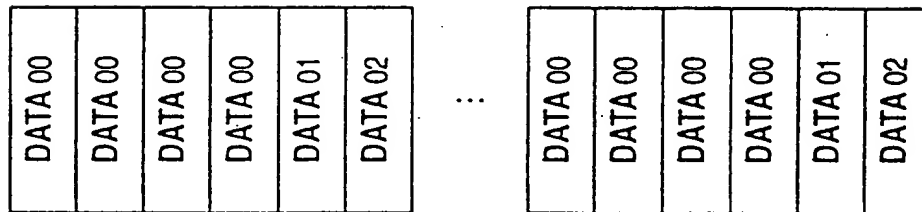


FIG. 1 O6

00771062-012001

00774052-0130014

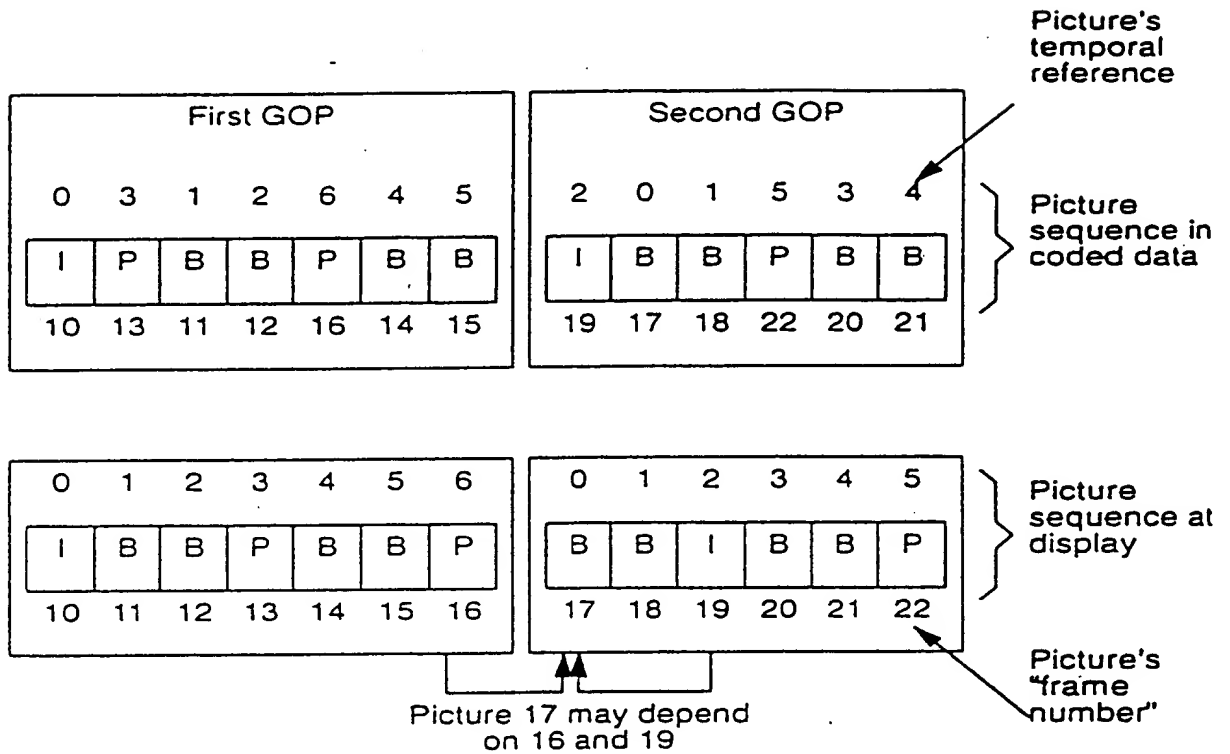


FIG. 107



FIG. 108

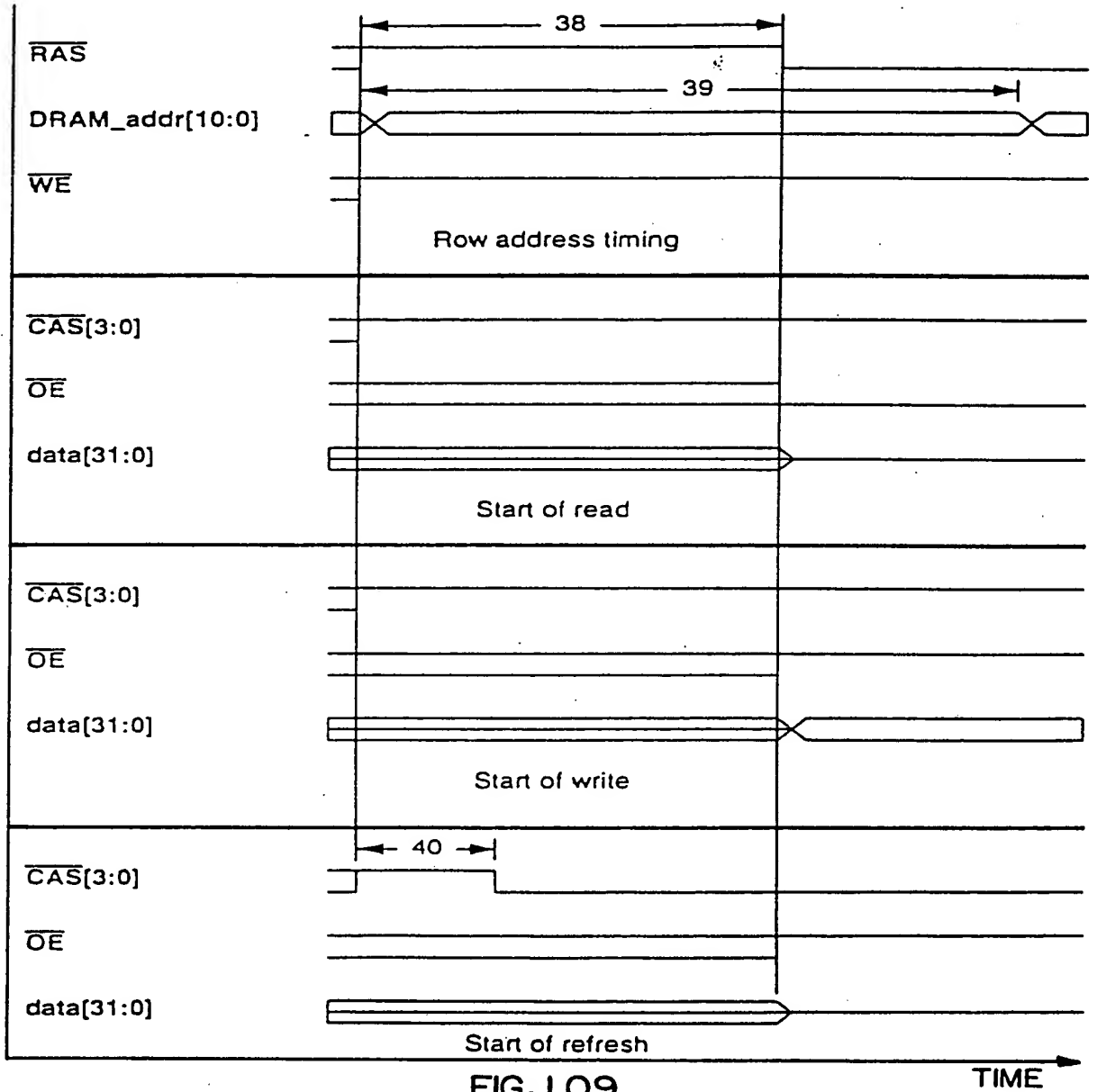
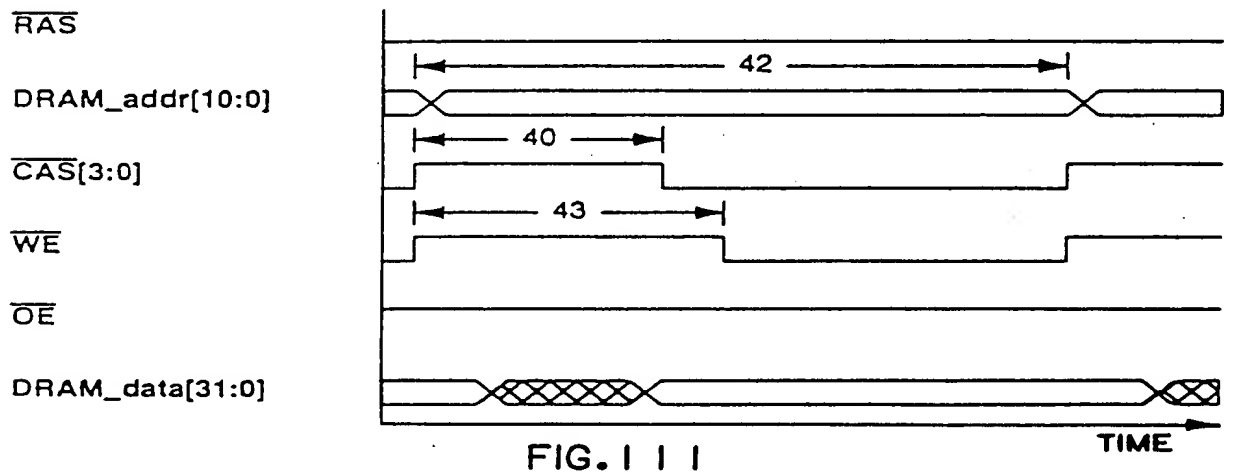
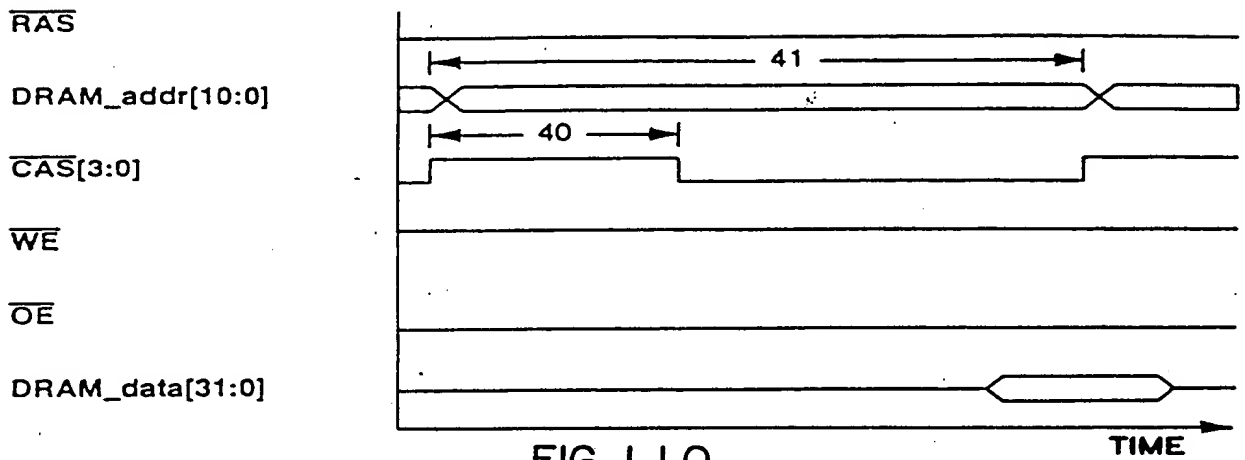


FIG. 109

00774062.012004





00771052-012001

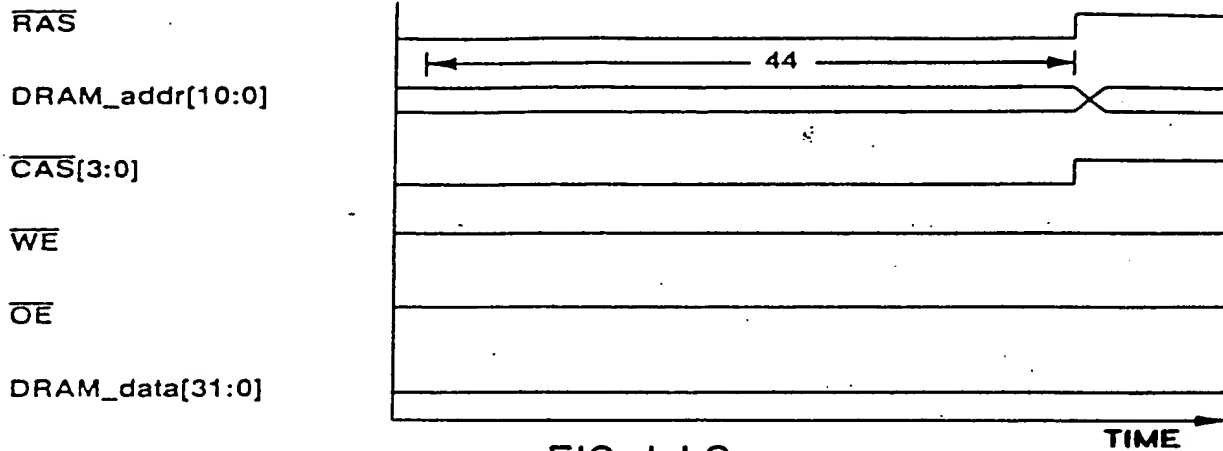


FIG. 1 | 2

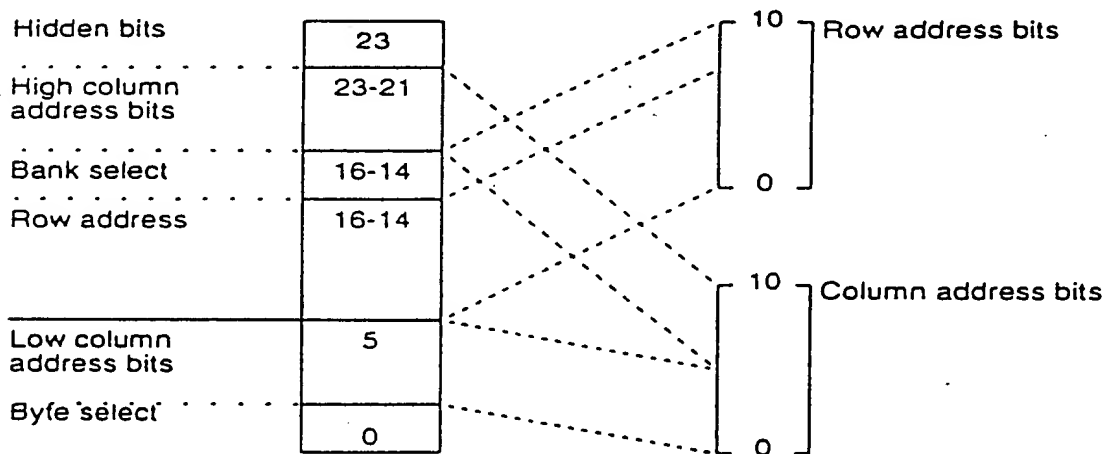


FIG. 1 | 3

09771062.012001

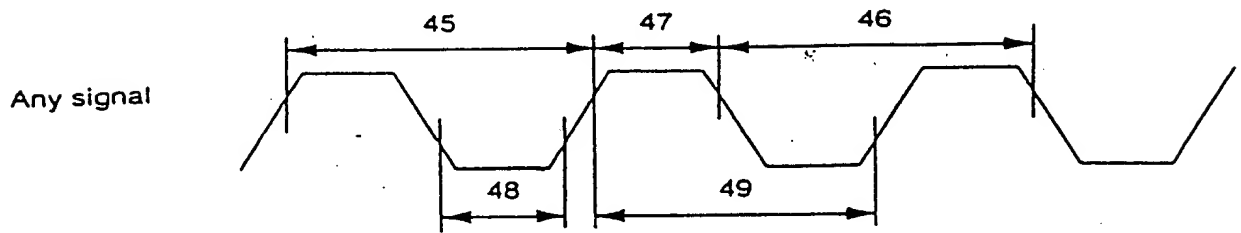


FIG. 114

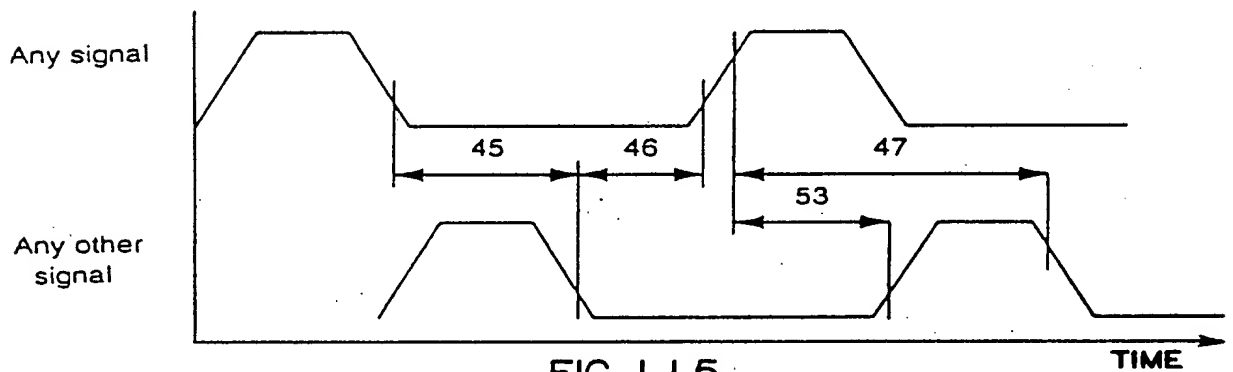
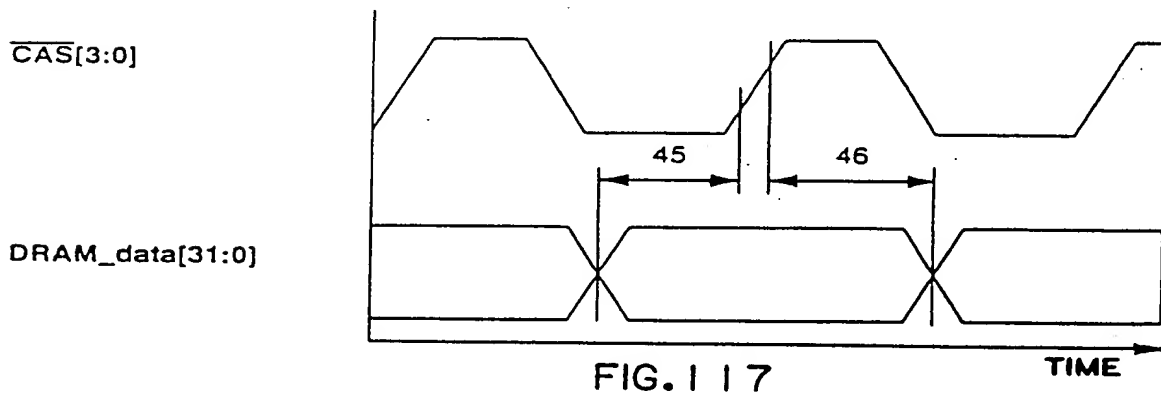
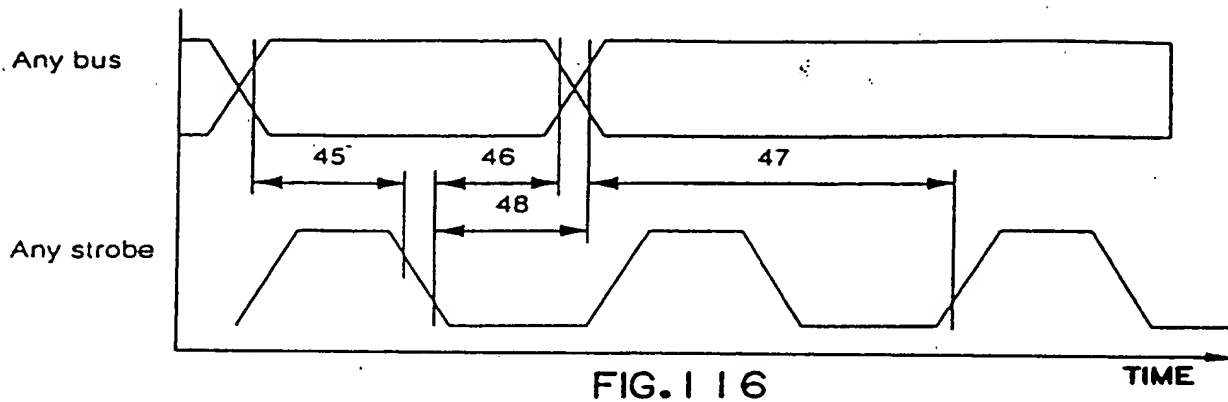


FIG. 115

00771062-012001



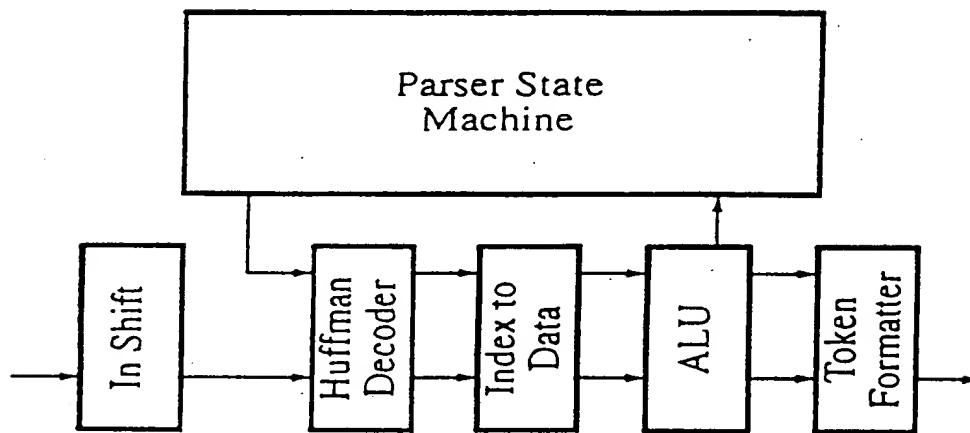


FIG. 118

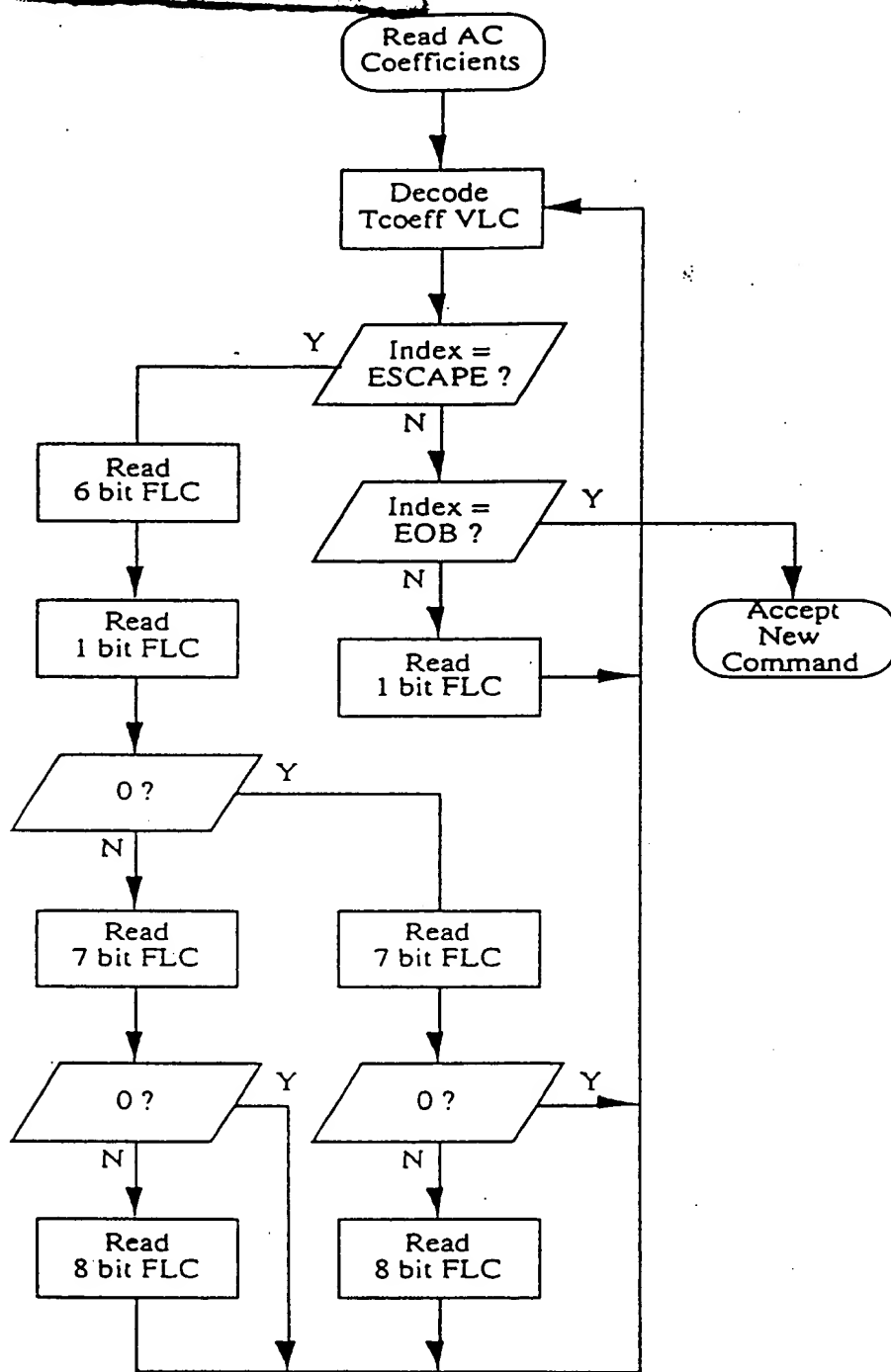


FIG. 119

00771062-012004

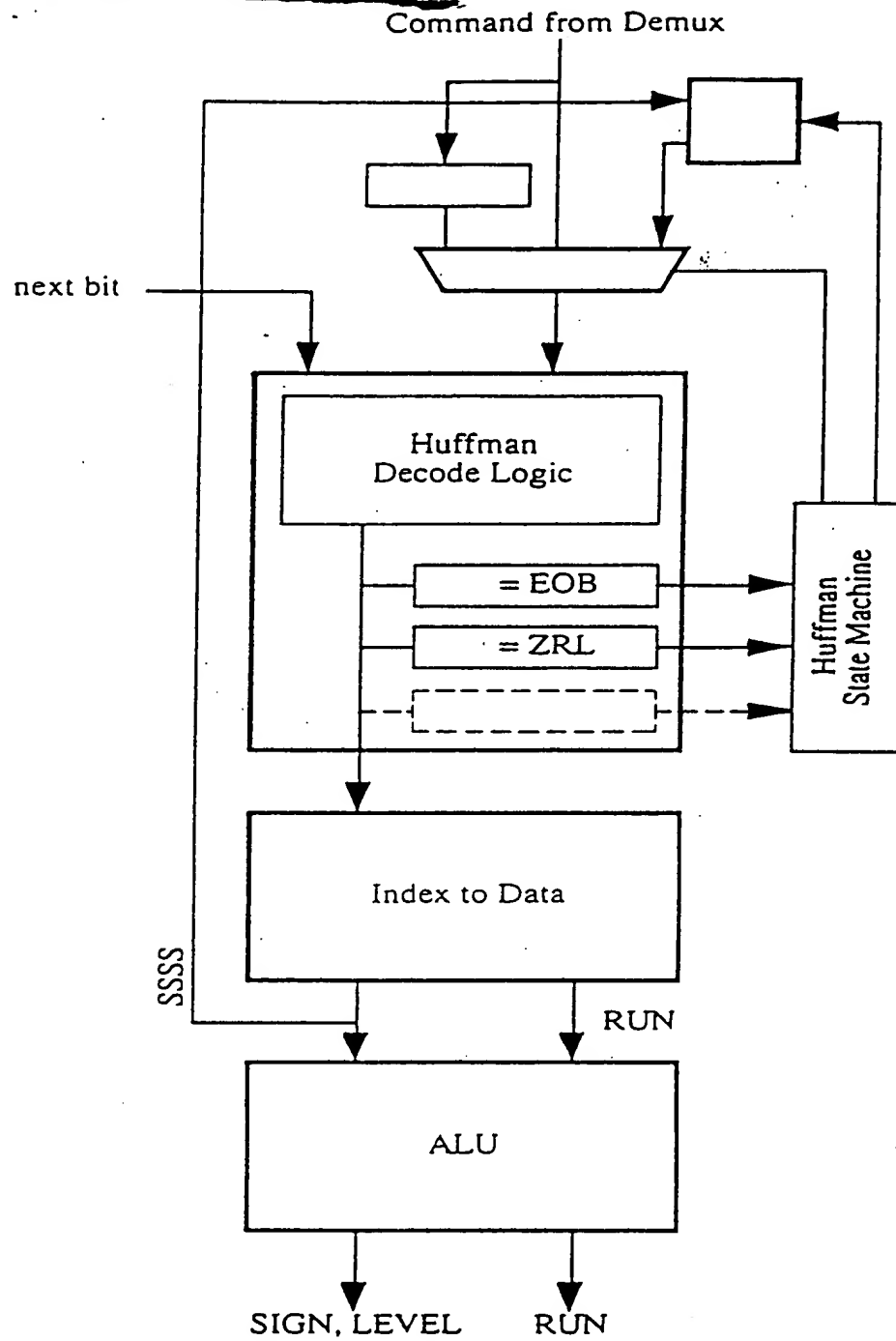


FIG. 120

0071052.012001

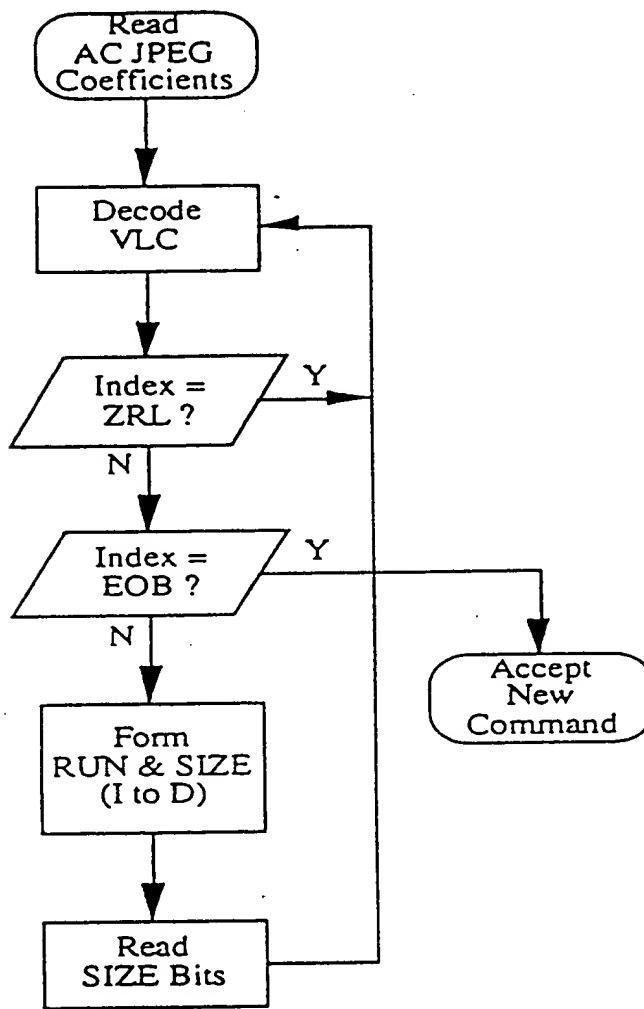


FIG. 121A

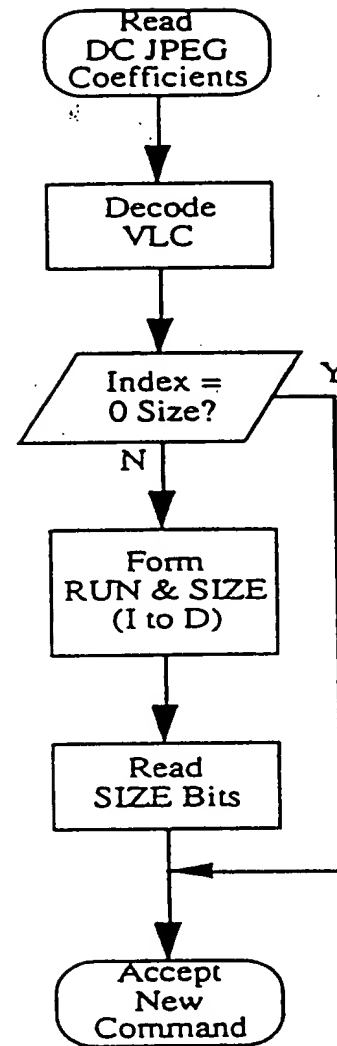


FIG. 121B

00771062-013001



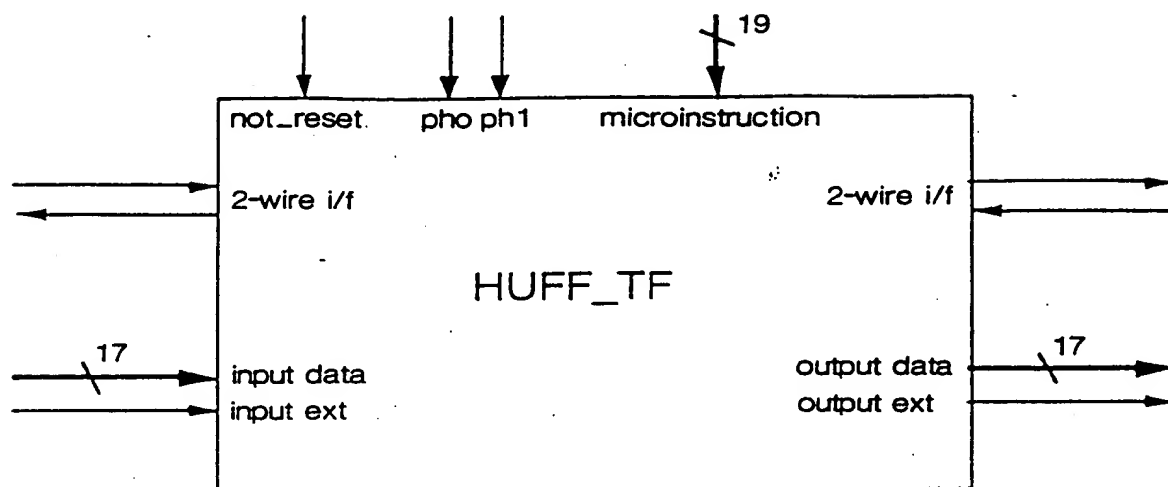


FIG. 1 22

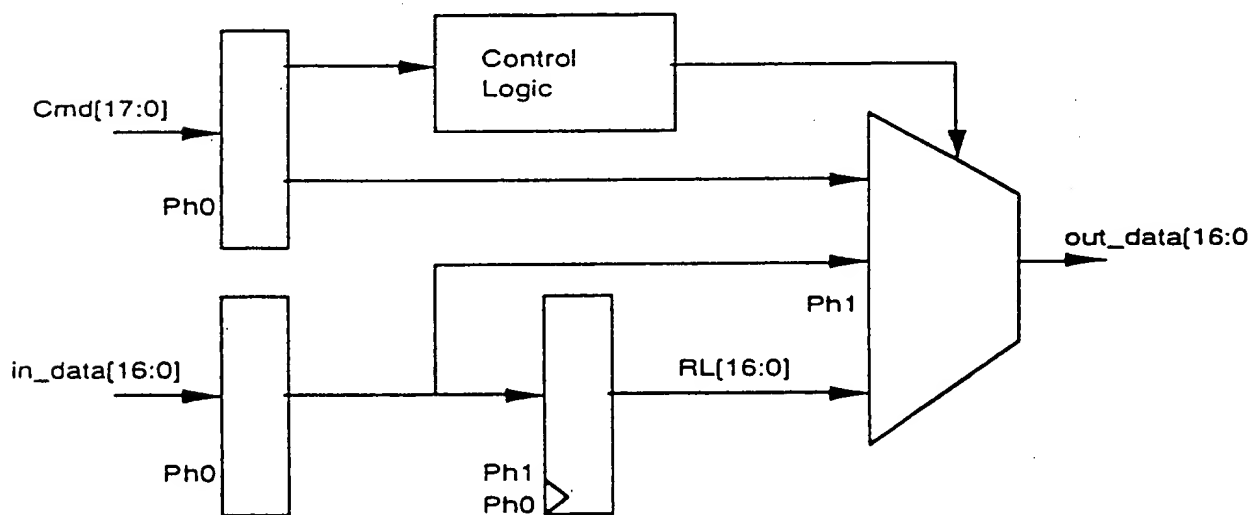


FIG. 1 23

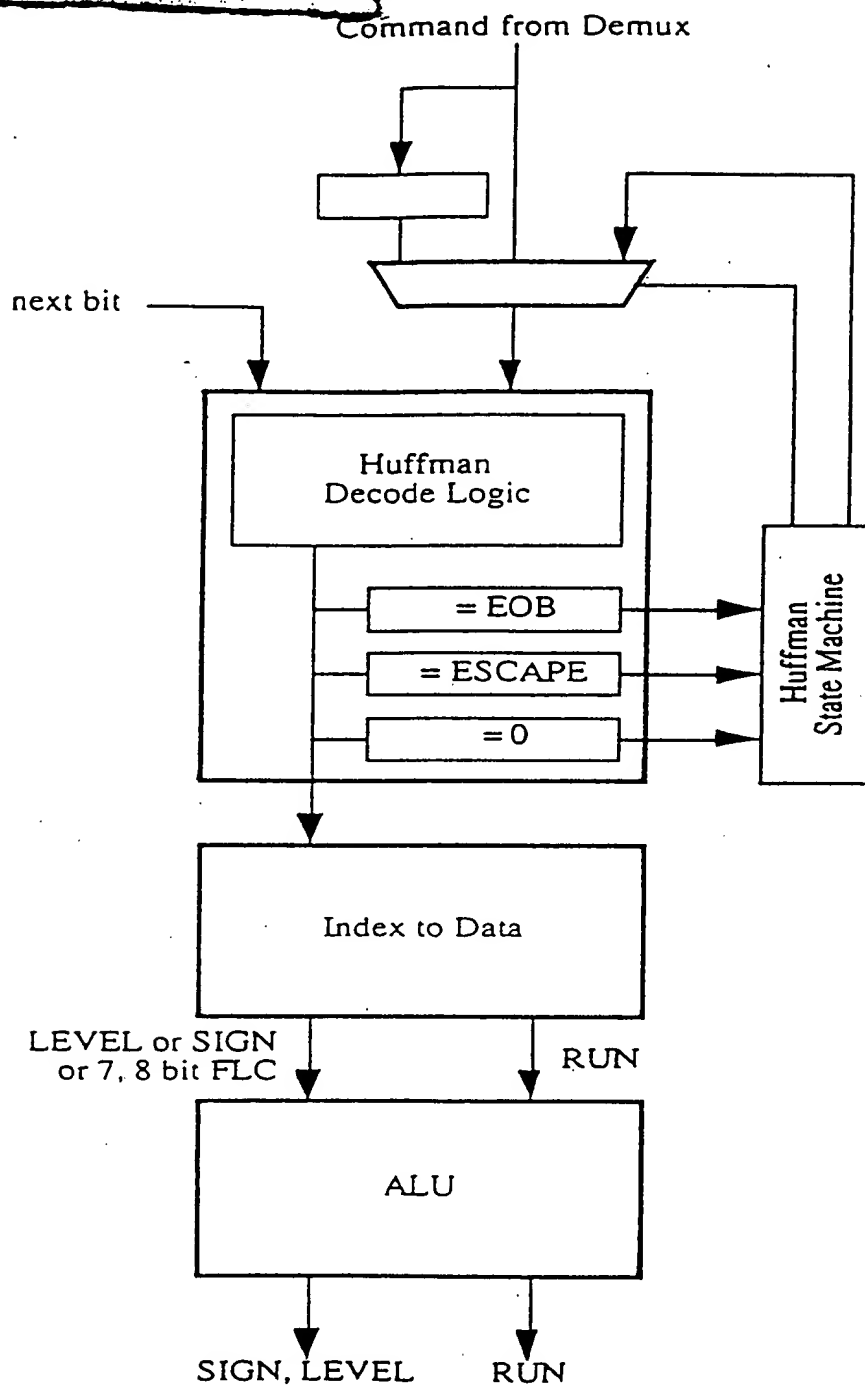


FIG. 124

0071062.010001

FIG. 10

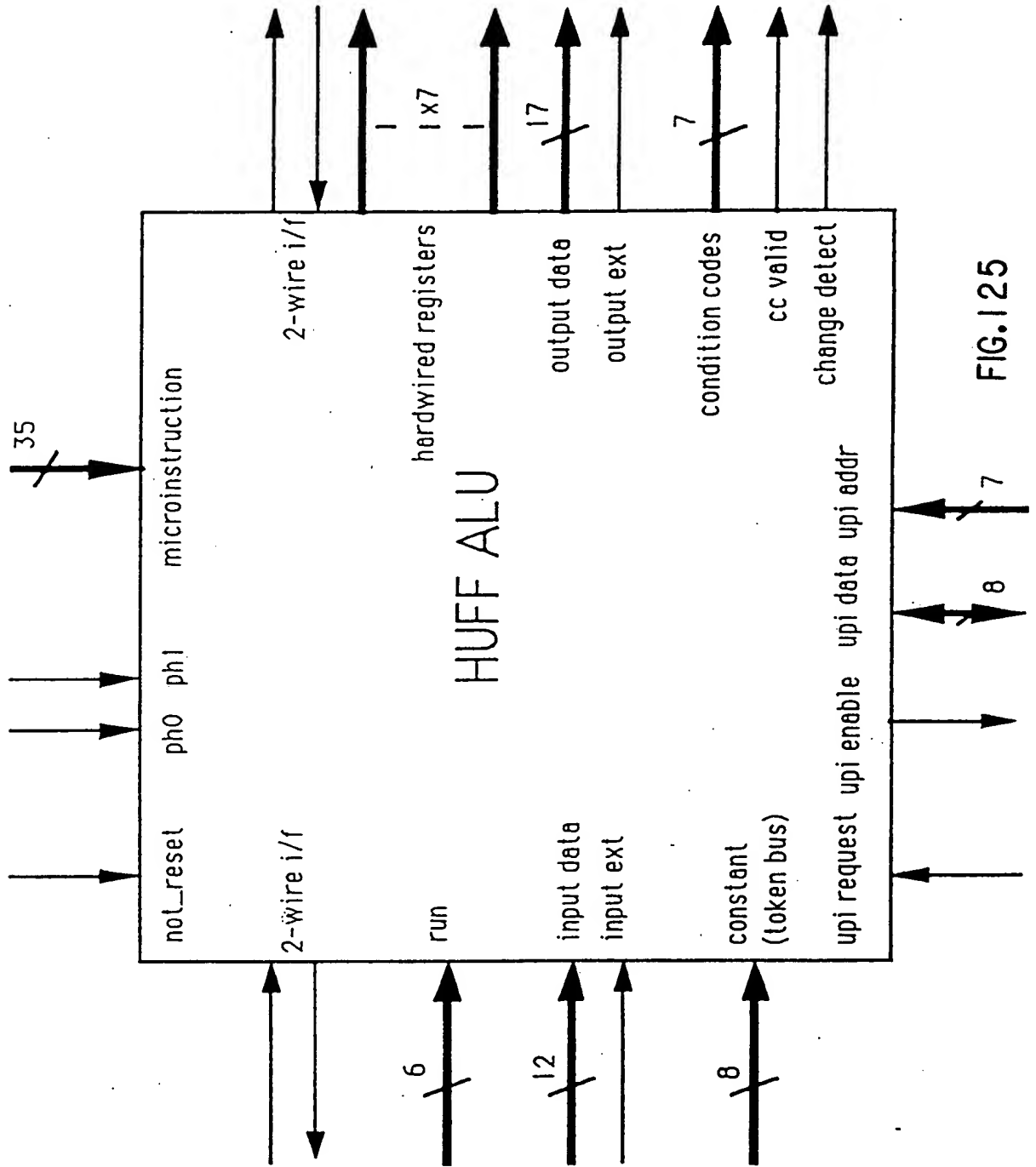


FIG. 125

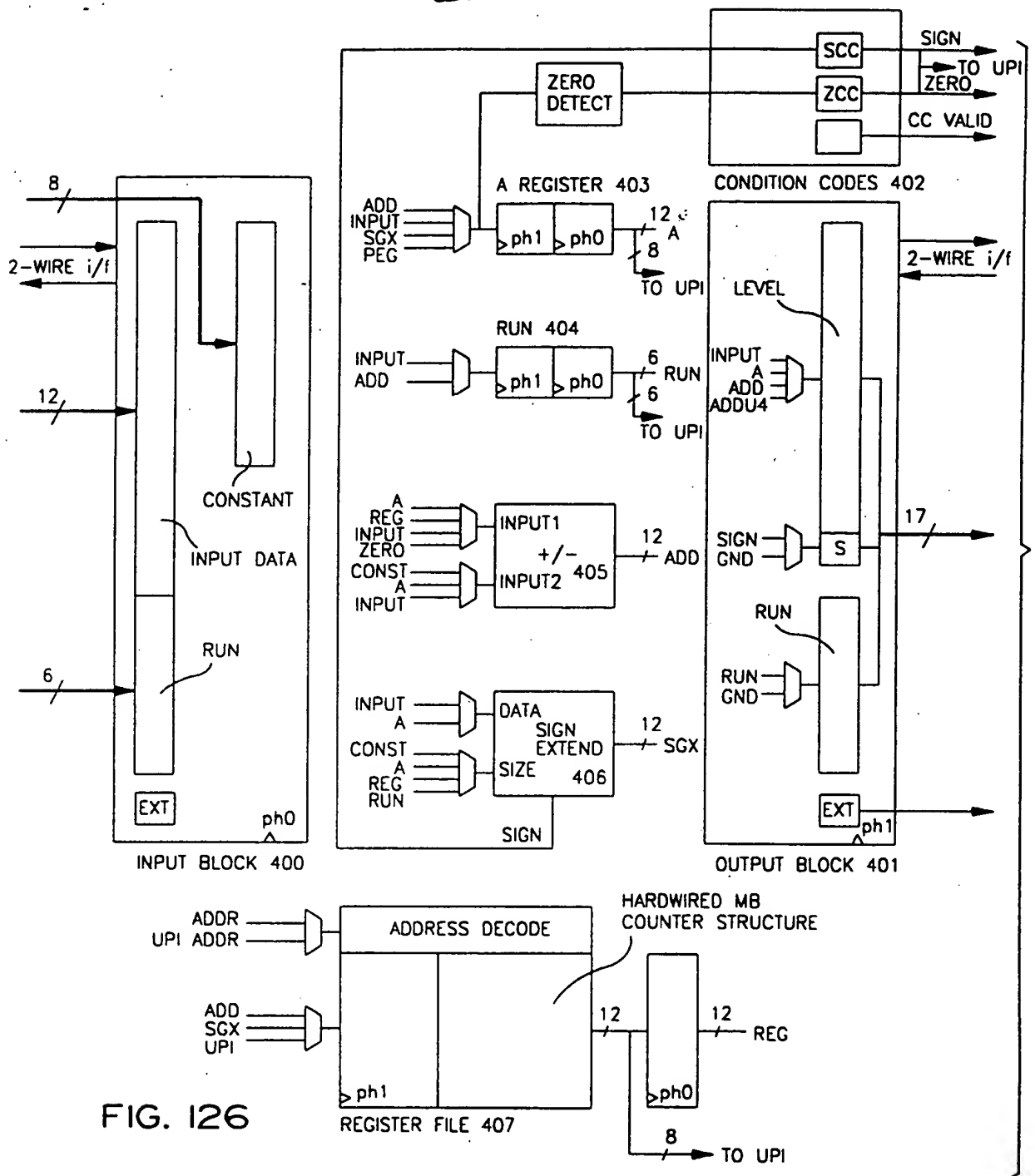


FIG. 126

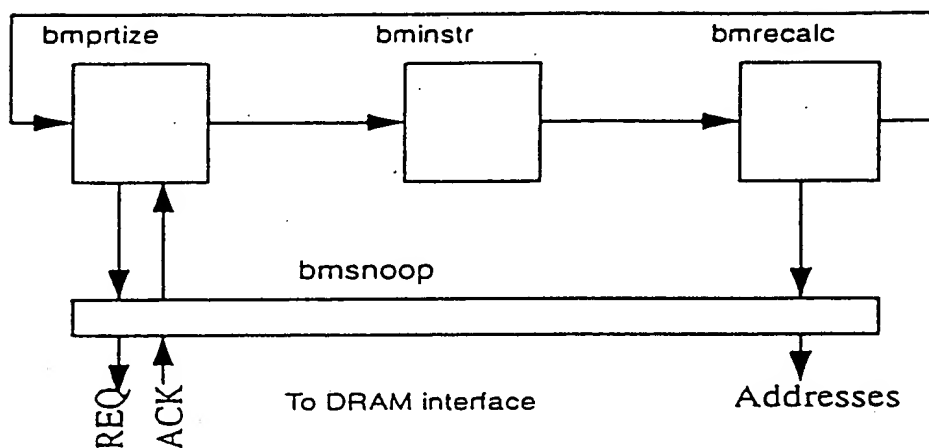


FIG. 127

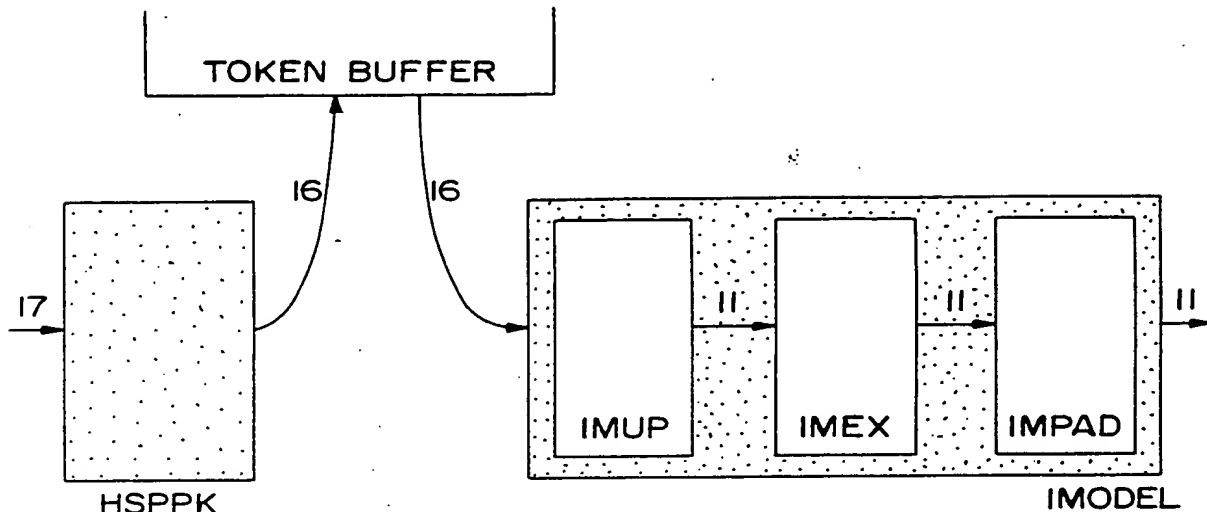


FIG. 128

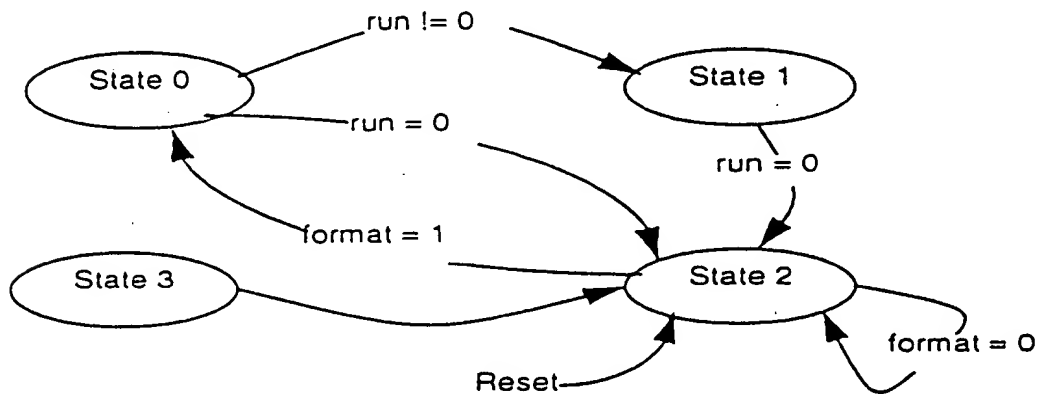


FIG. 129

00771062.012001

FIG. 130

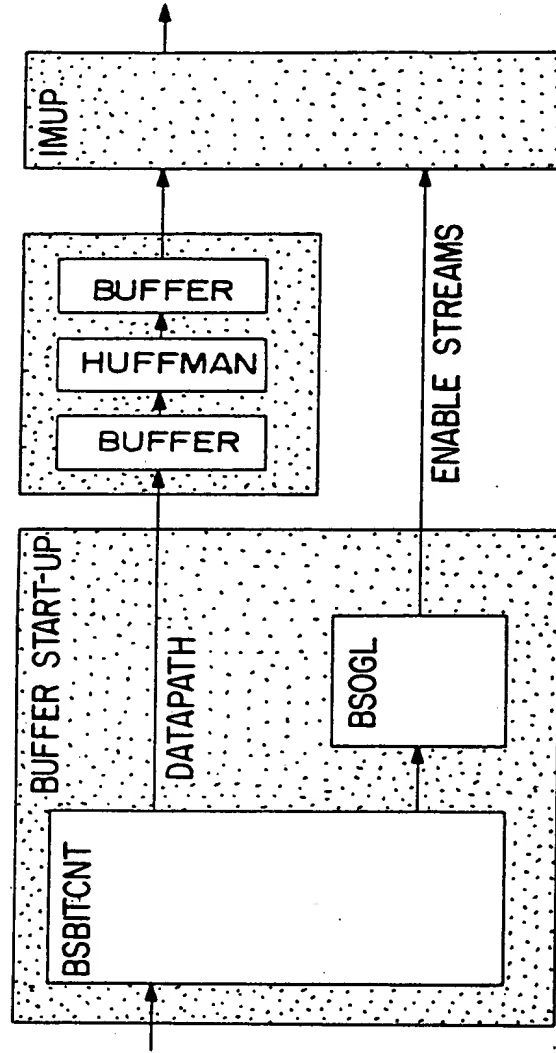


FIG. 130

0071052 101304  
F00210 23074260

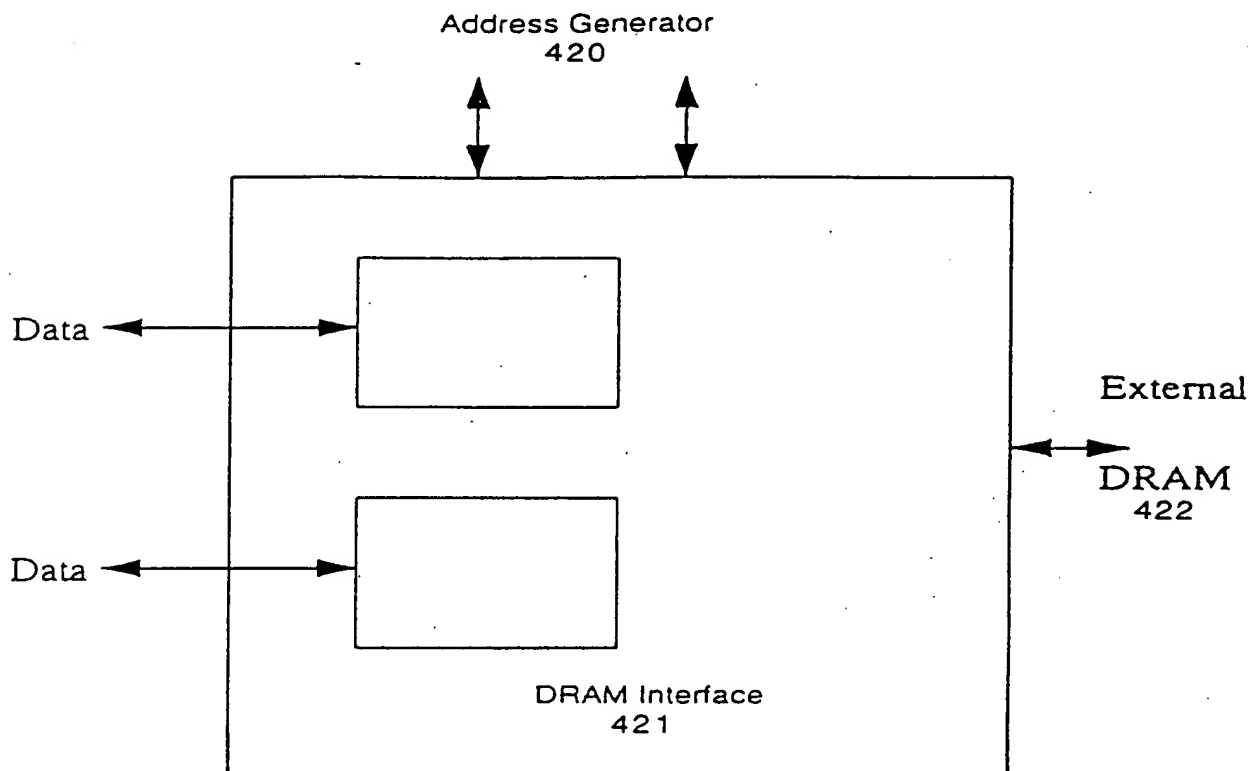


FIG. 131



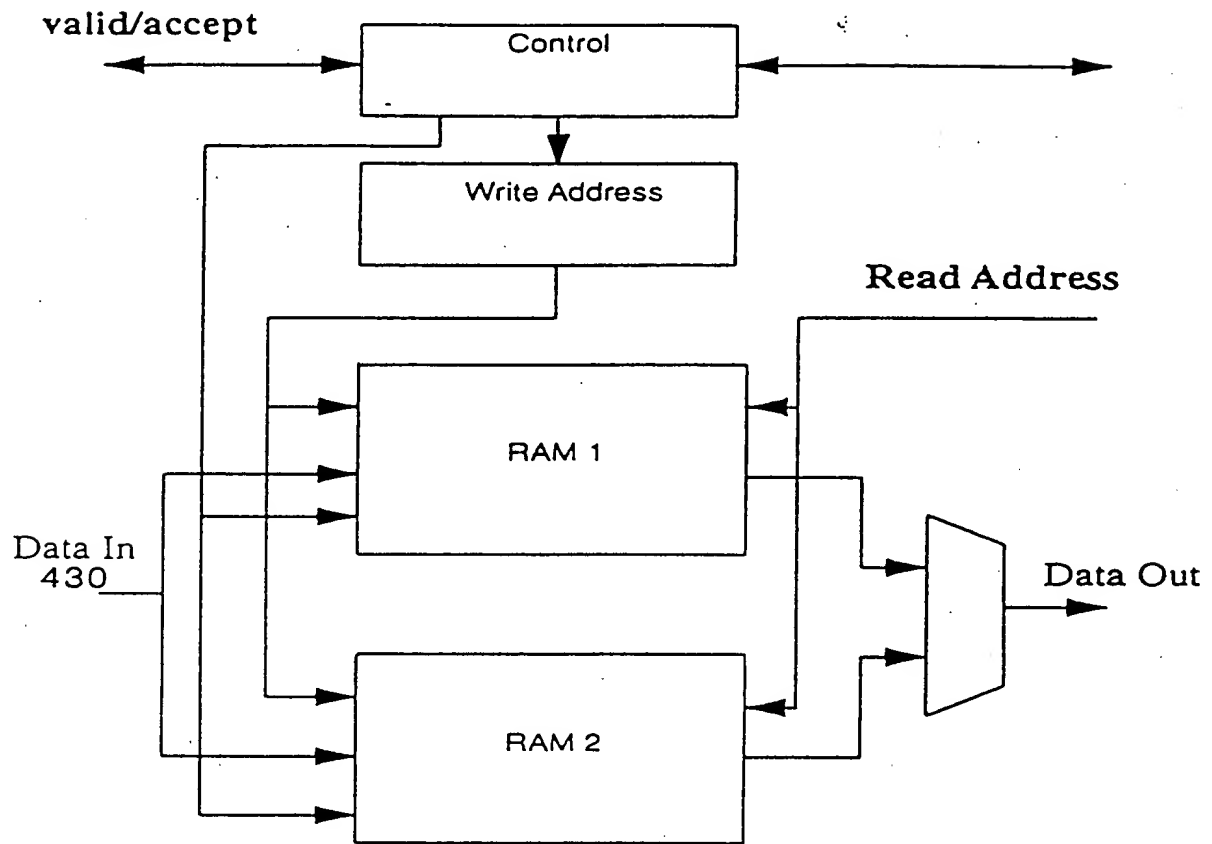


FIG. 132

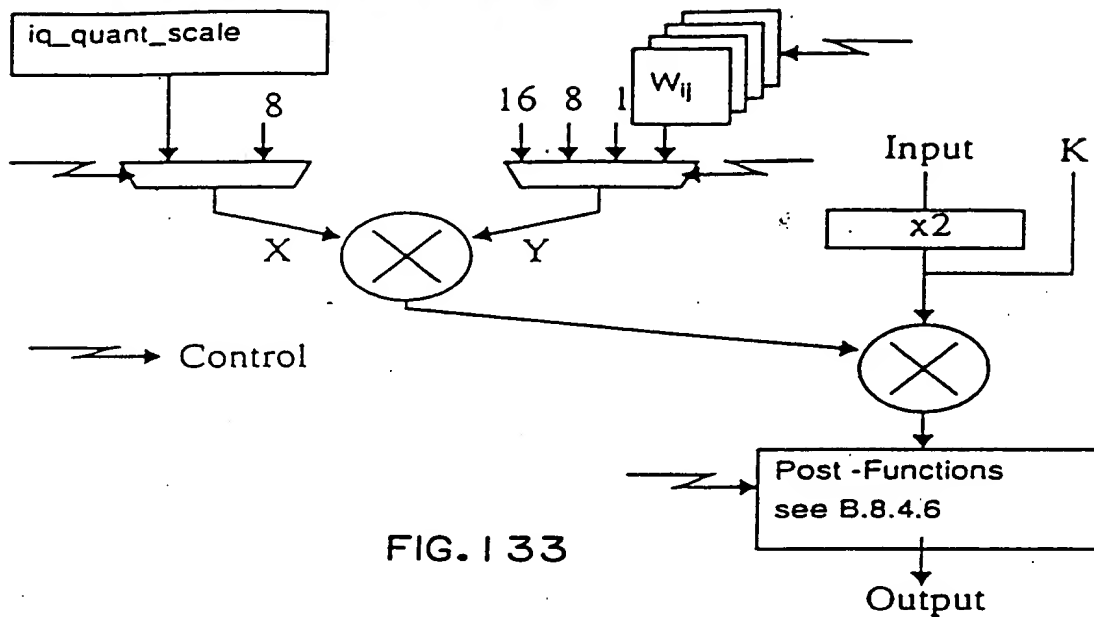


FIG. 133

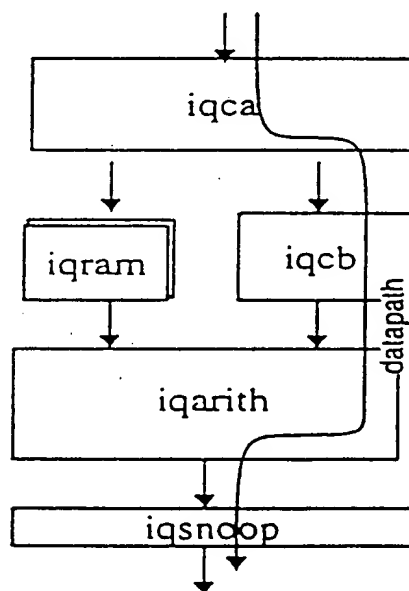


FIG. 134

09771062.012001

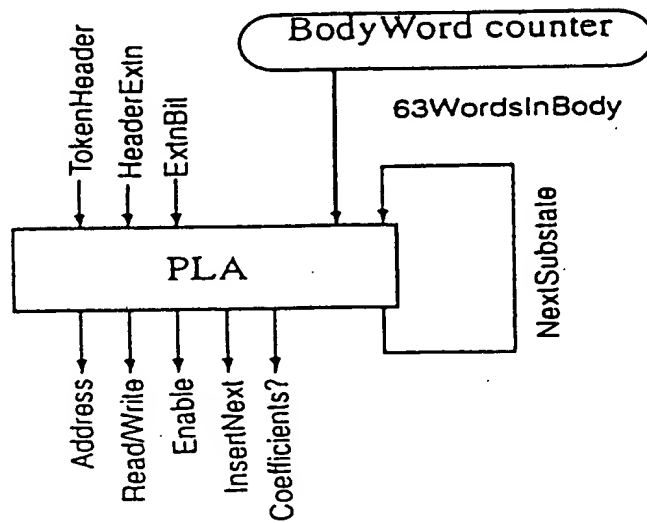
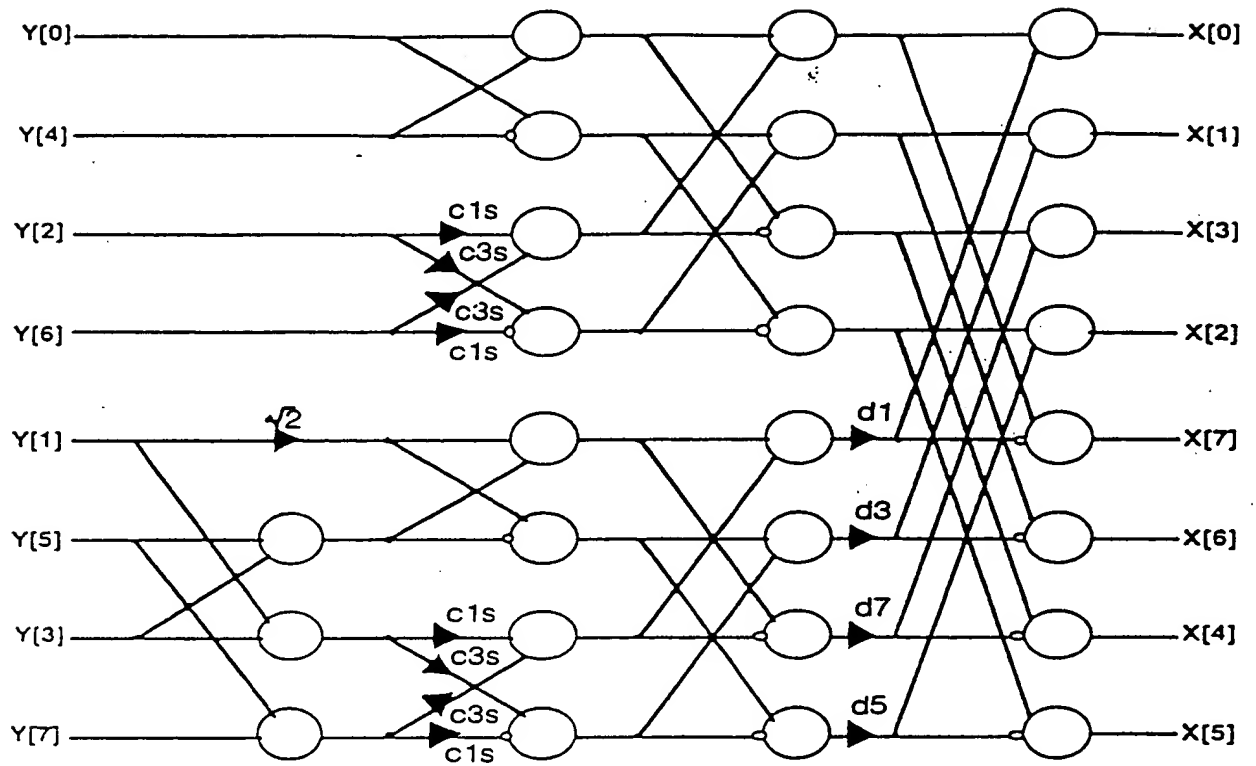


FIG. 135

0071052.013004



Key:-

coef  
constant coefficient  
multiplier

, ,

adder, subtractor

FIG. 136

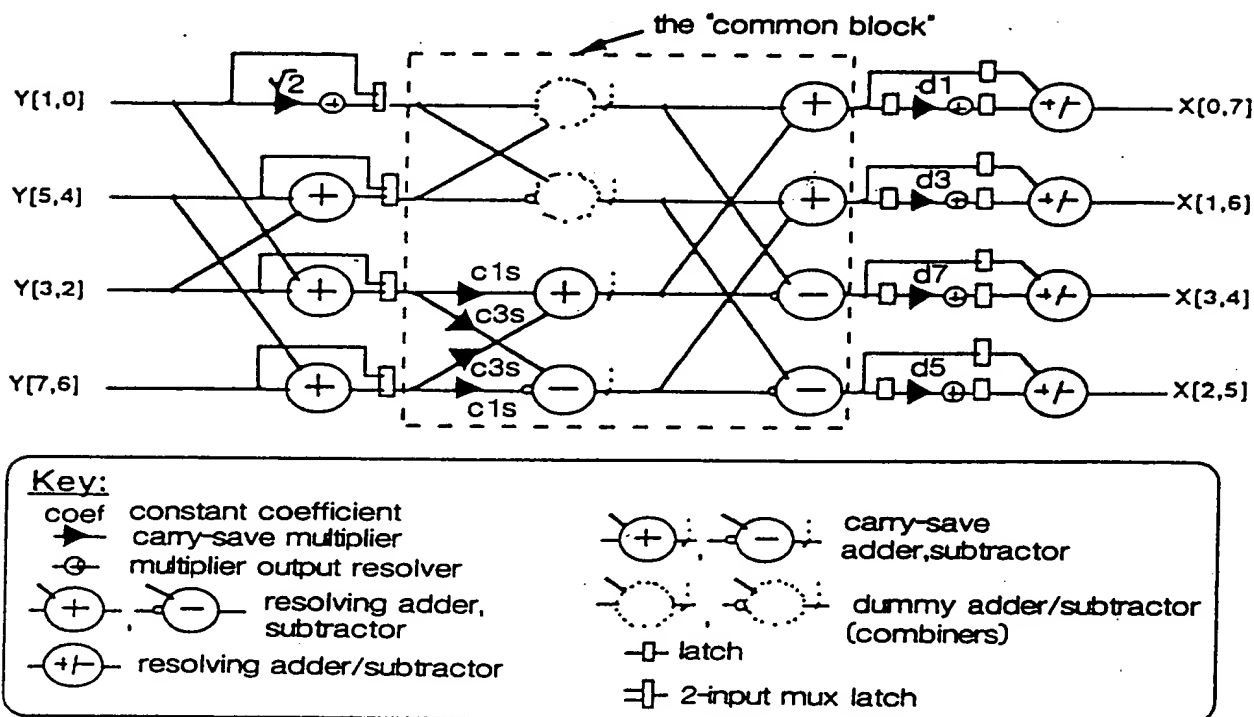


FIG. 137

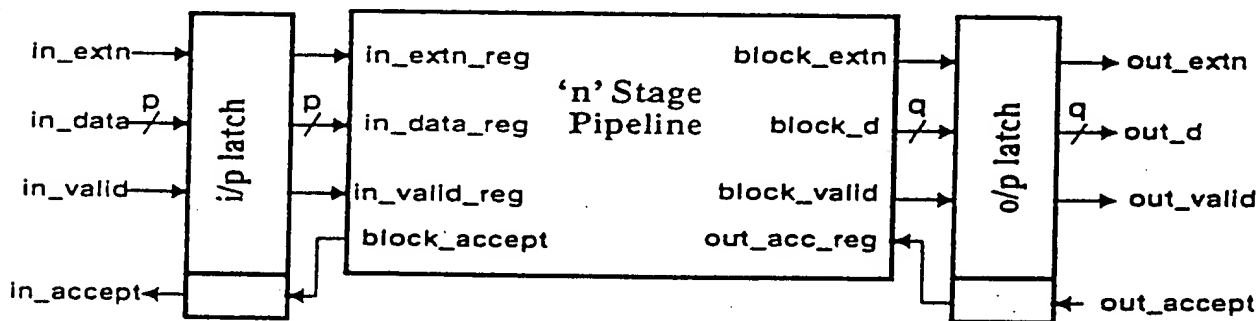


FIG. 138

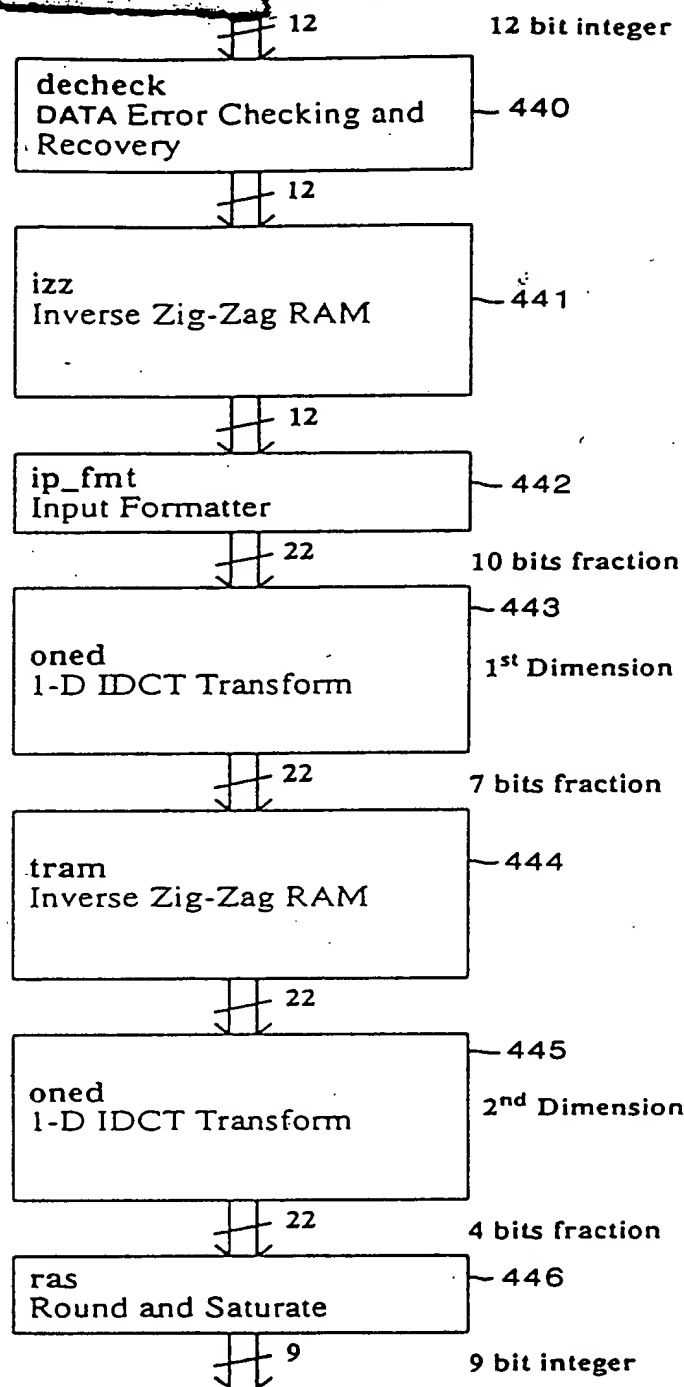


FIG. 139

00771062.012001

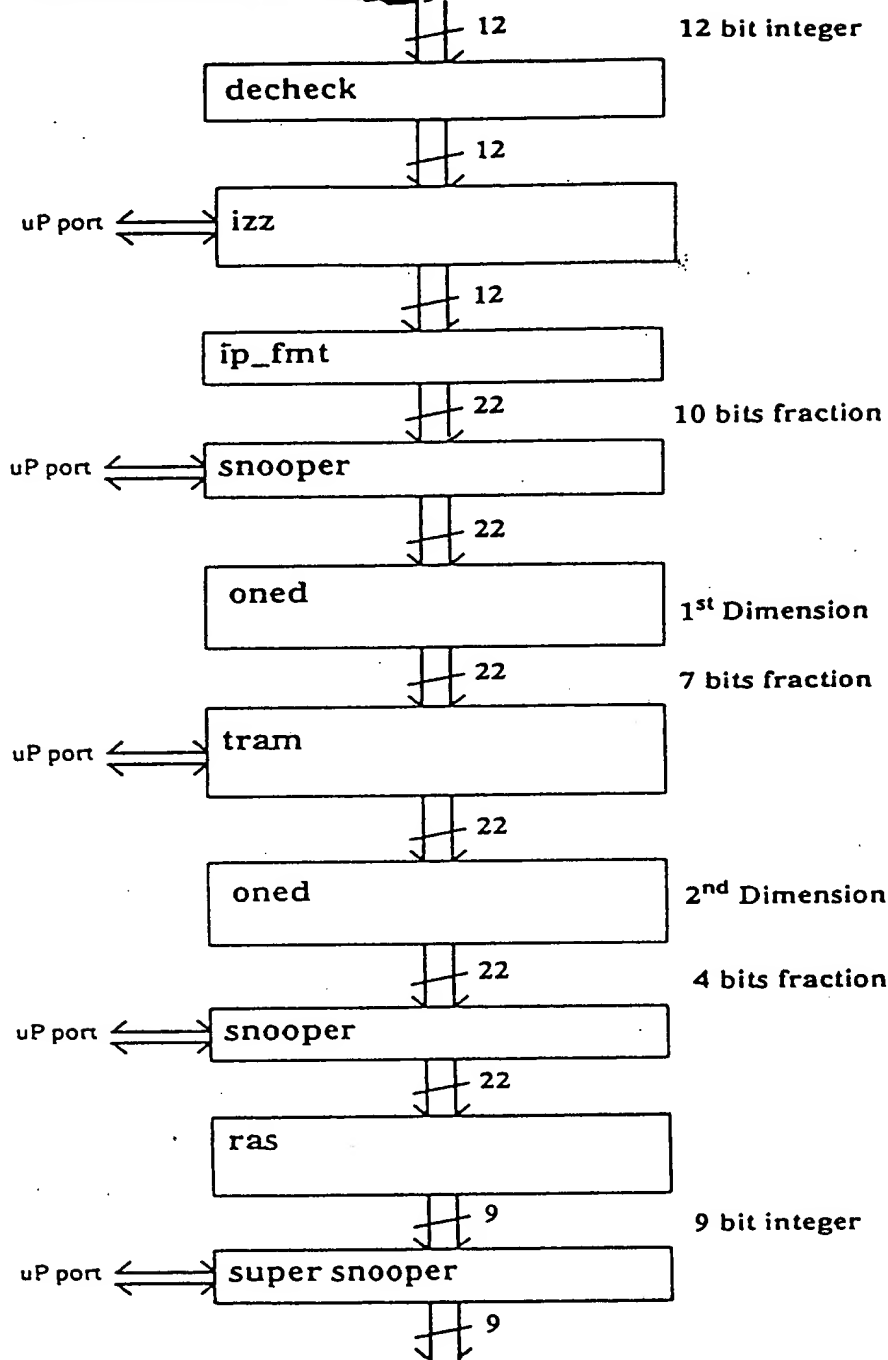


FIG. 140

00771062-012001

FIG. 140

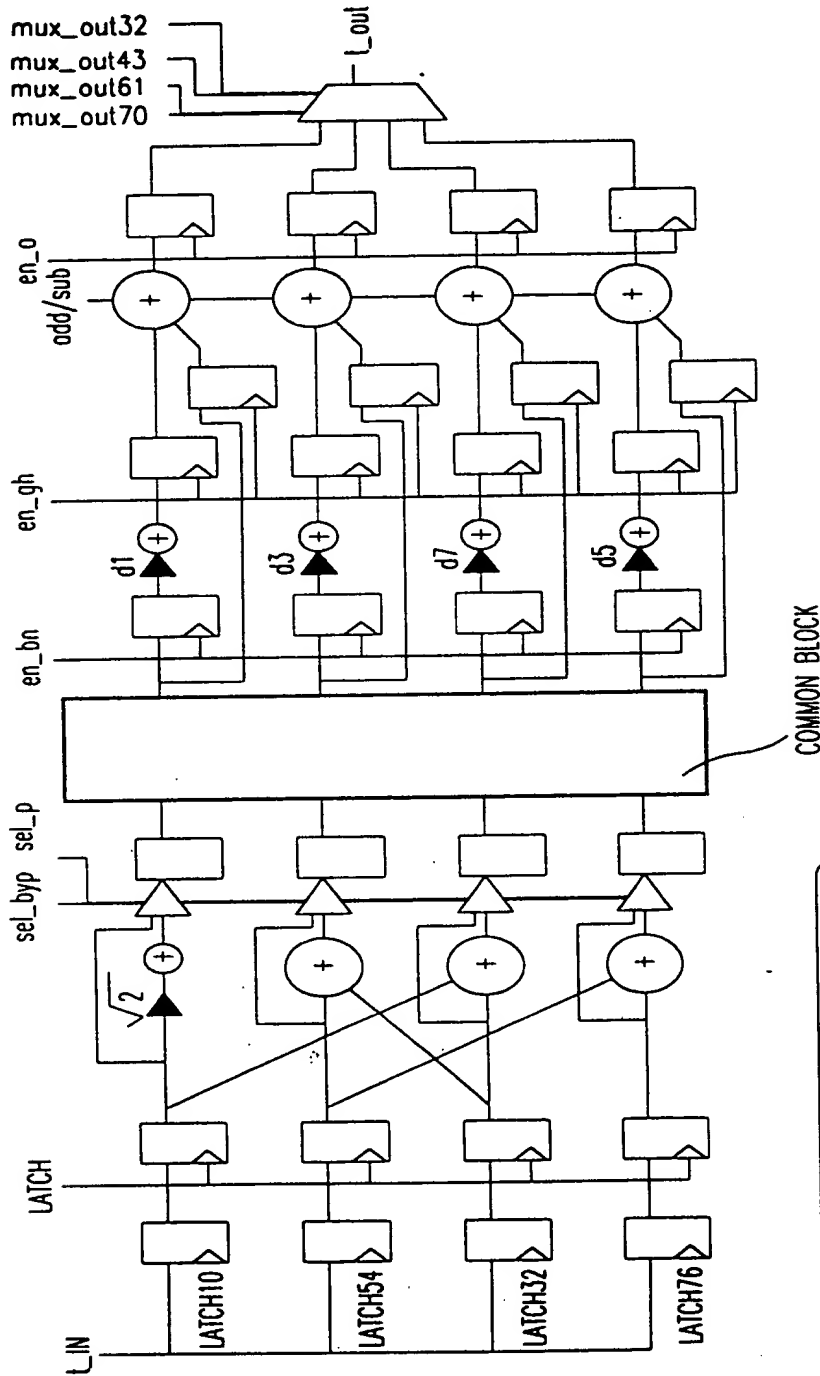
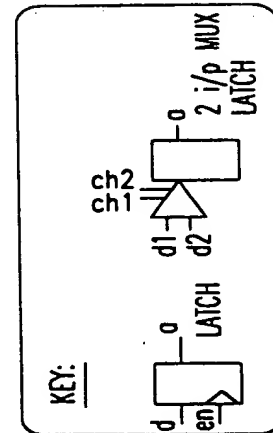


FIG. 141

NOTE: "COMMON BLOCK" IS ENTIRELY  
COMBINATIONAL (NO LATCHING)





0071062-010001

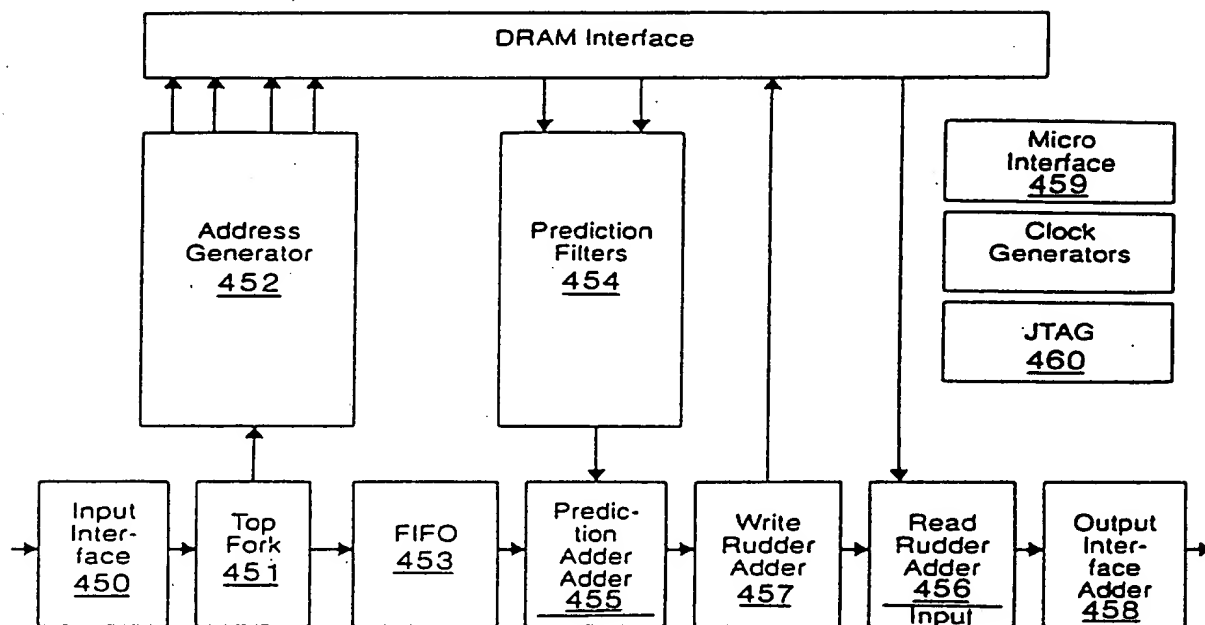
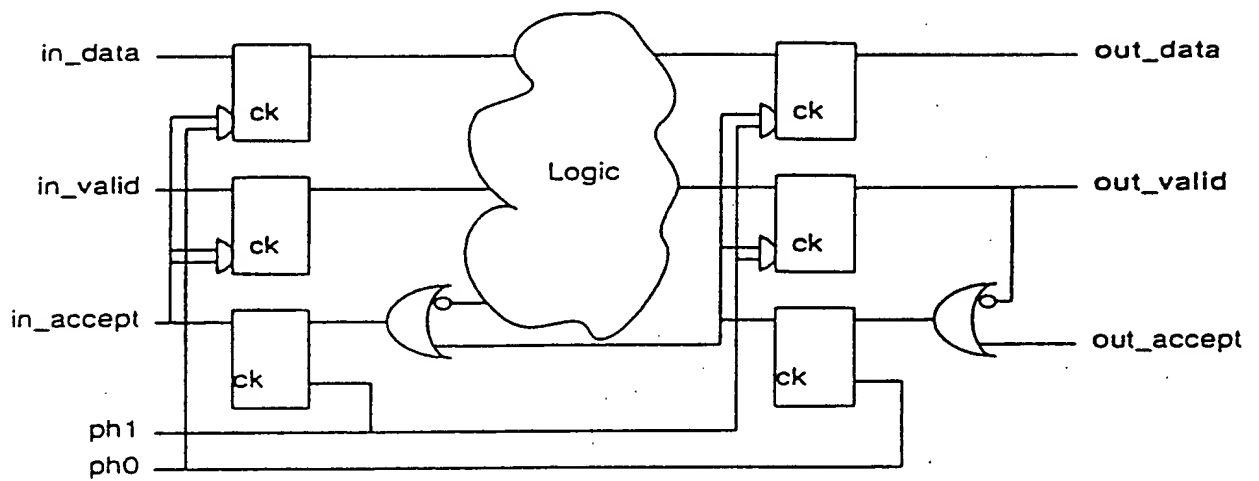


FIG. 1 42



0071053 013004  
F00210 25072200

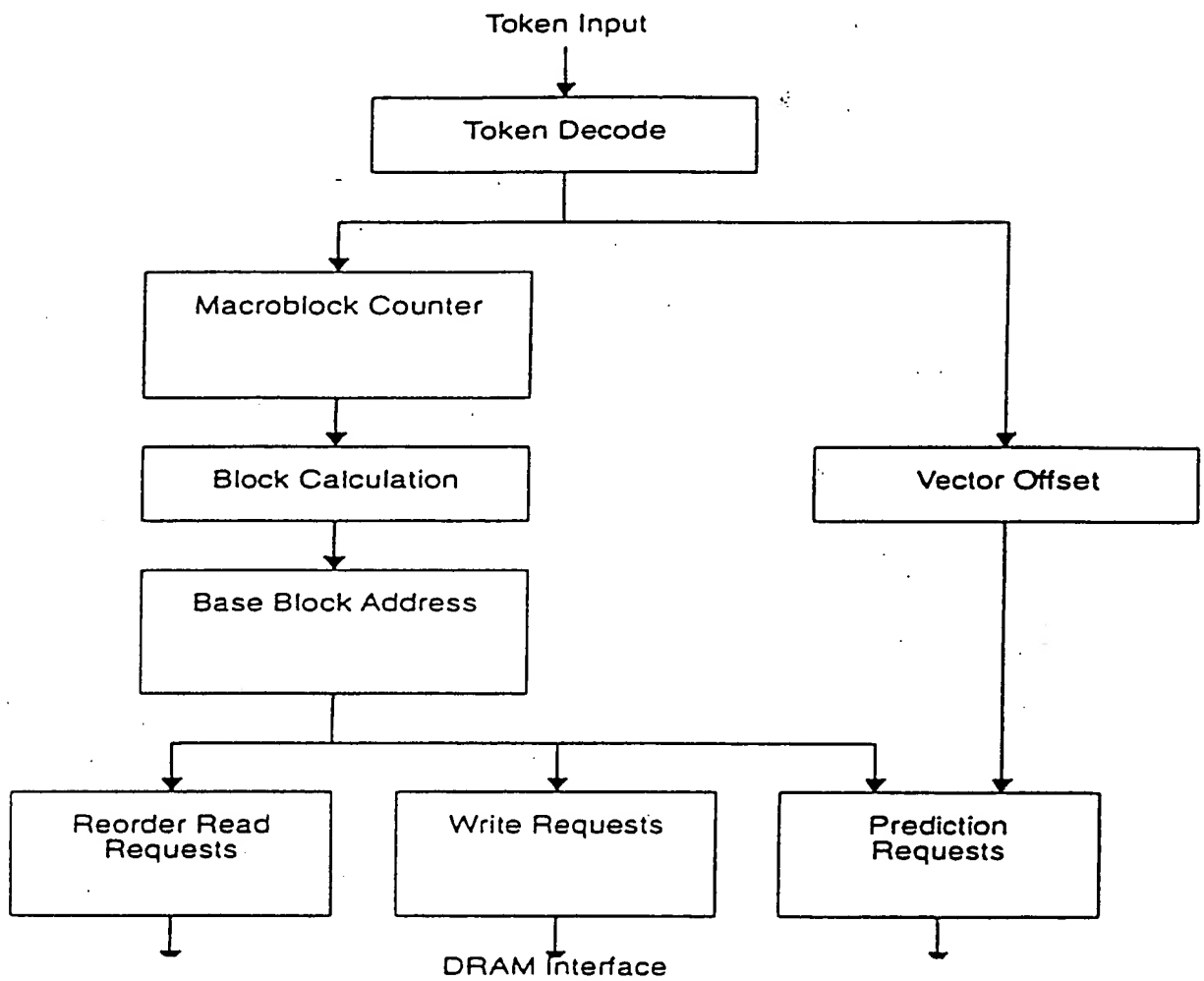


FIG. 144

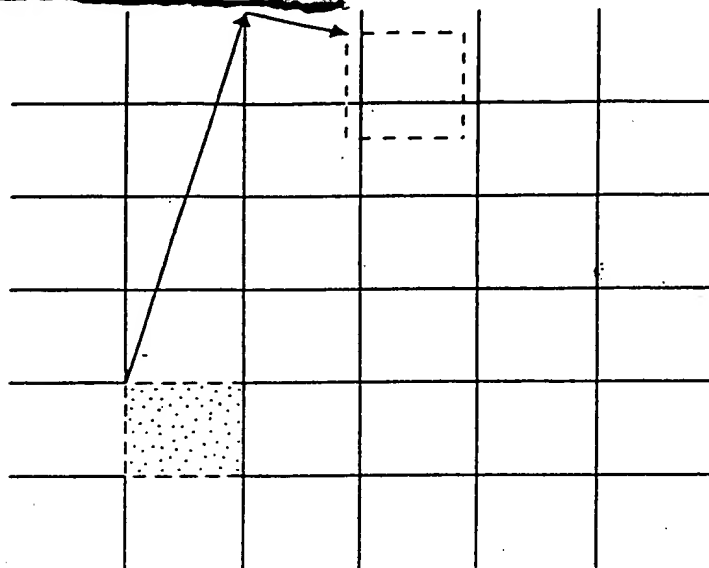


FIG. 1 45

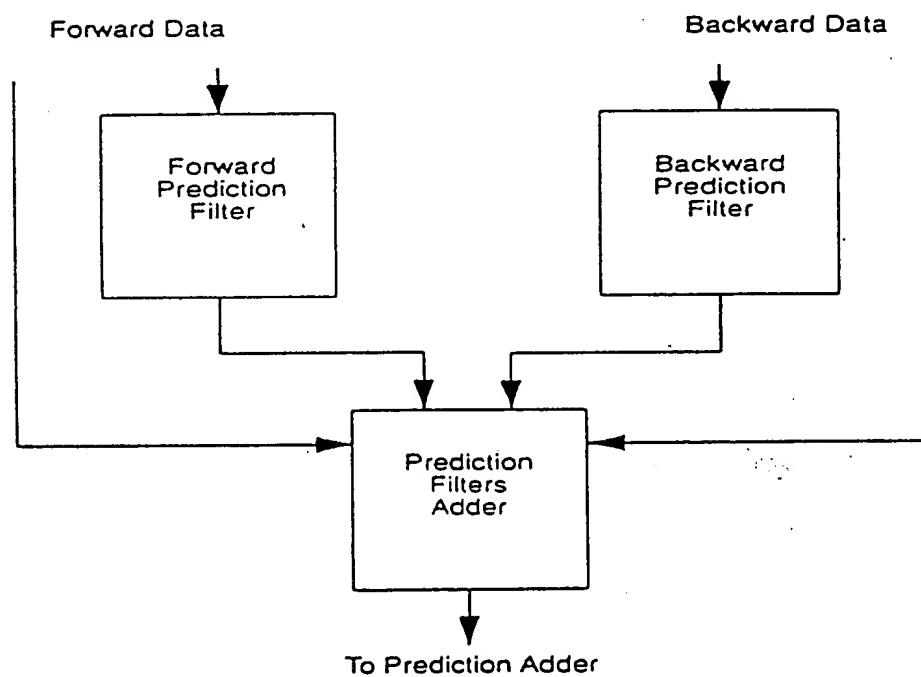


FIG. 1 46

00771062.012001

09774062.012004

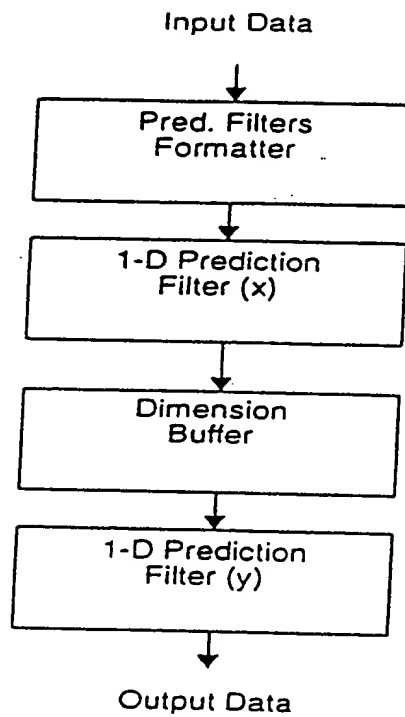


FIG. 1 47

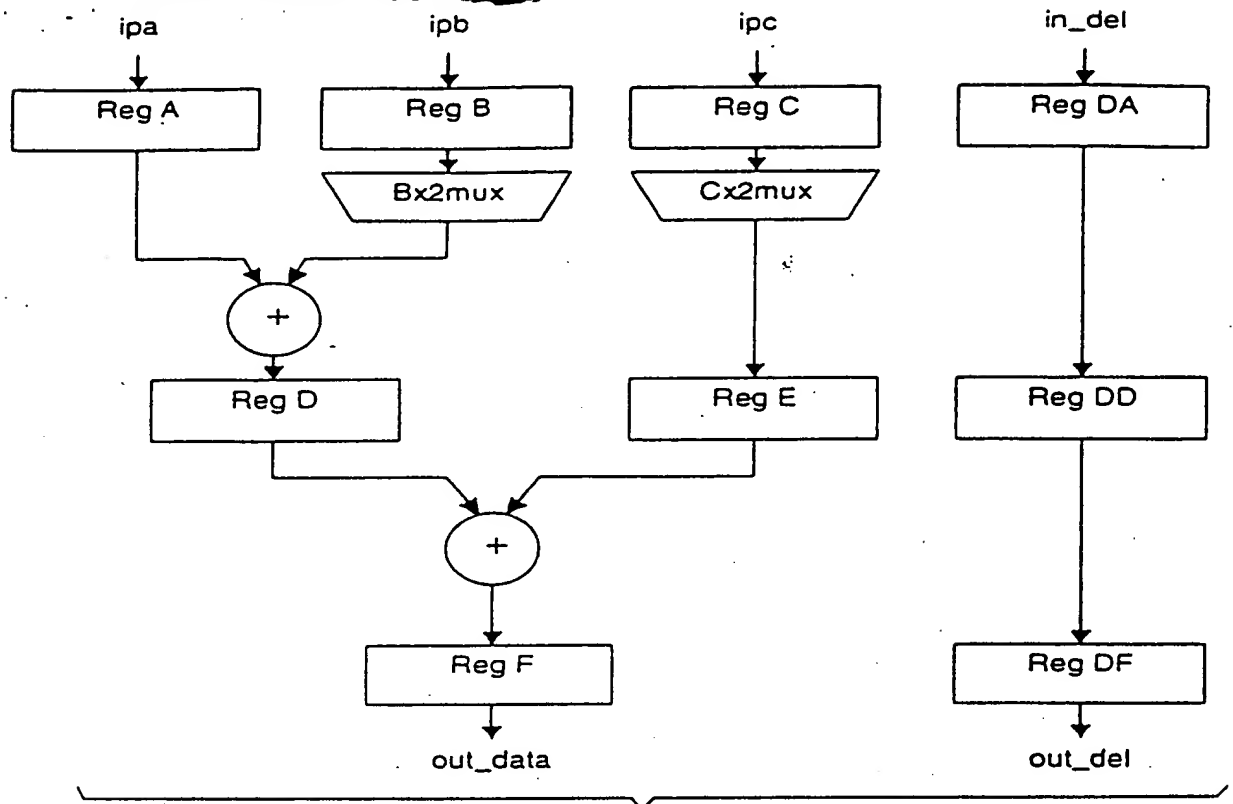


FIG. 148

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55
56	57	58	59	60	61	62	63

FIG. 149

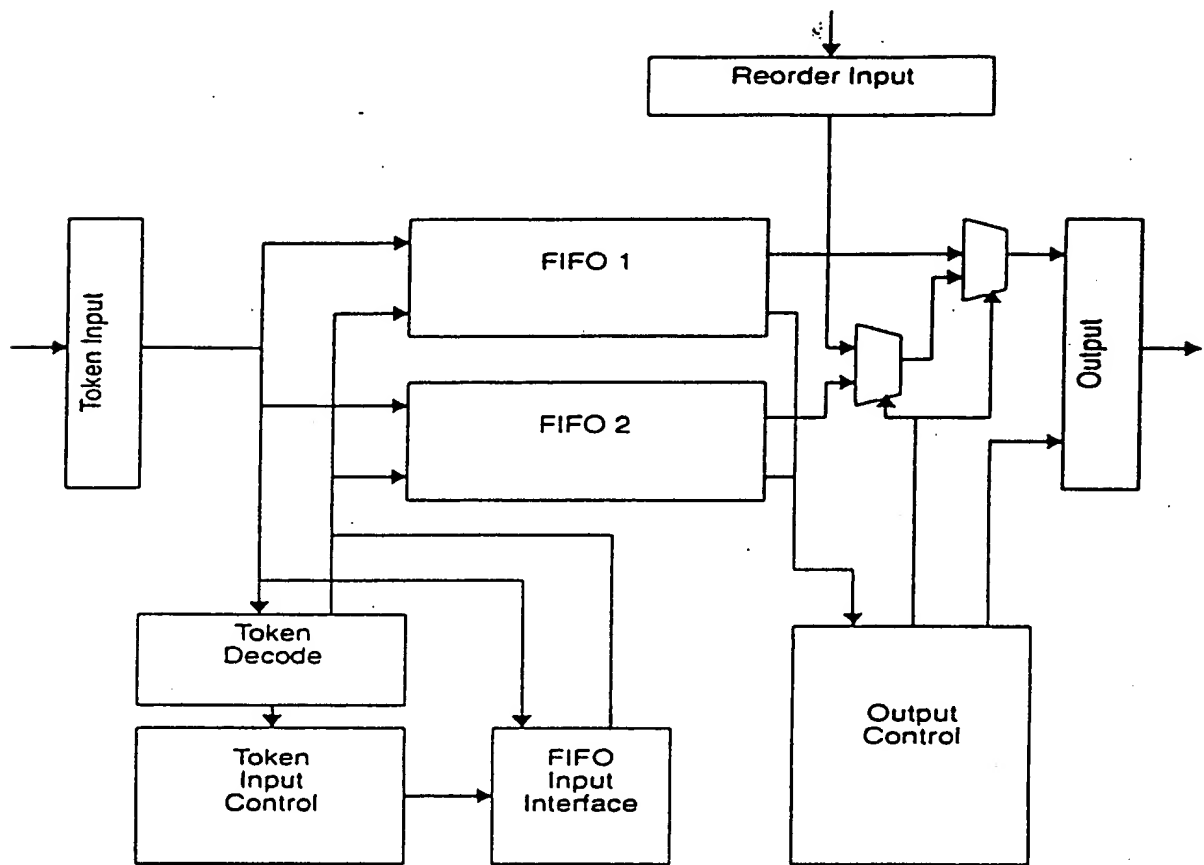


FIG. 150

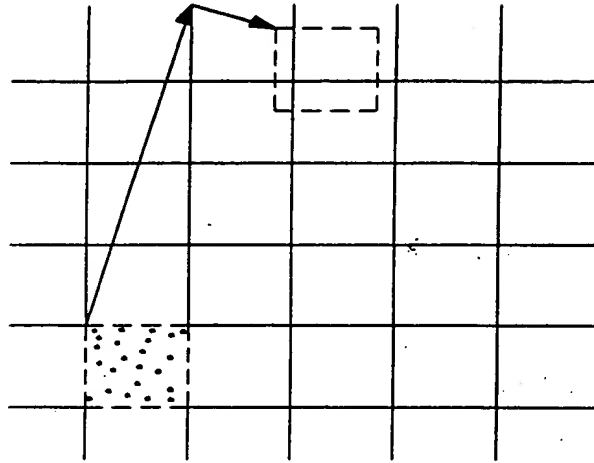


FIG. 151

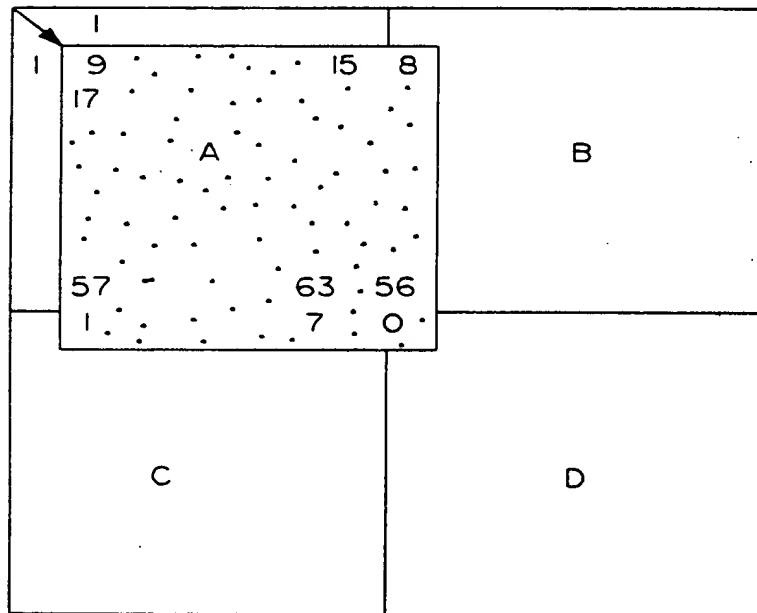


FIG. 152

00771052 .012001



Read Cycle

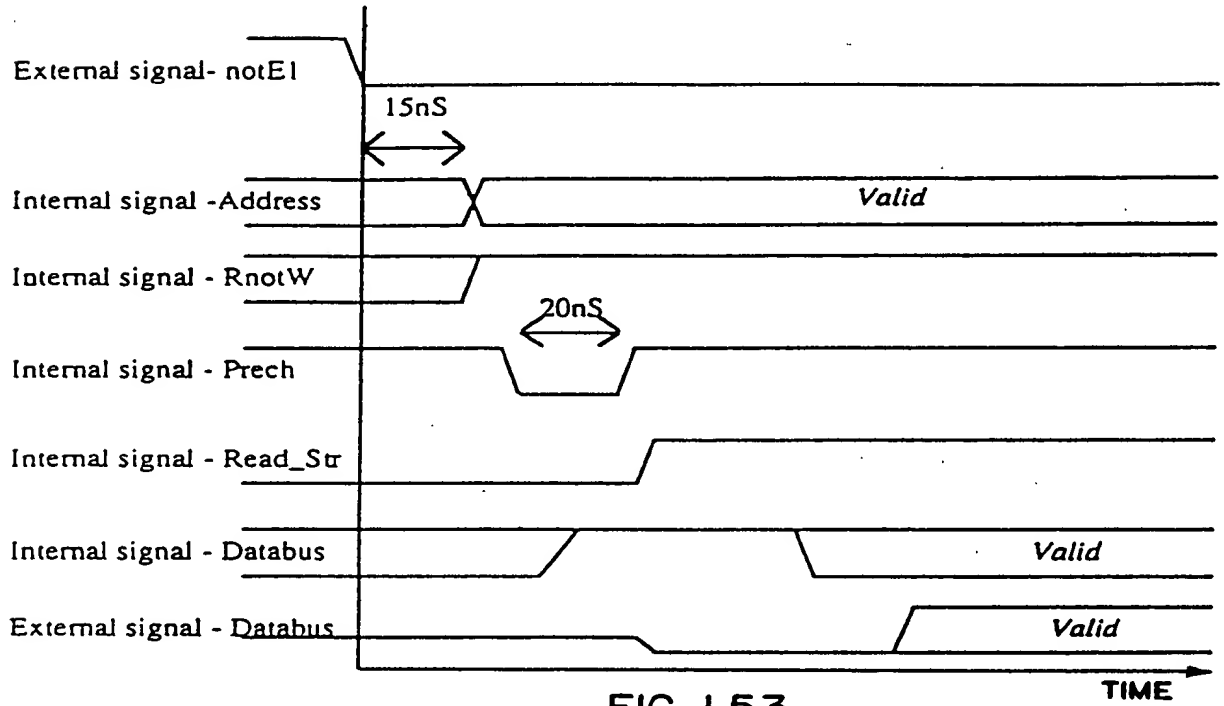
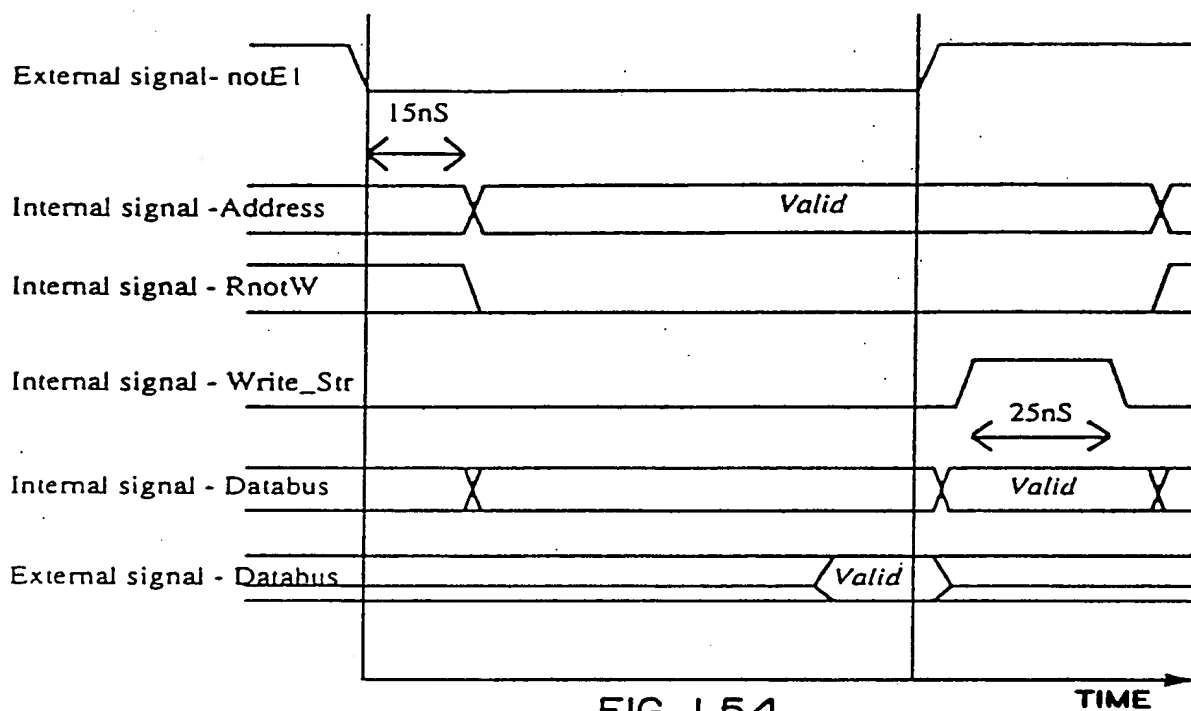


FIG. 153

00771062 0130004

### Write Cycle



00771062.012001

FIG. 155

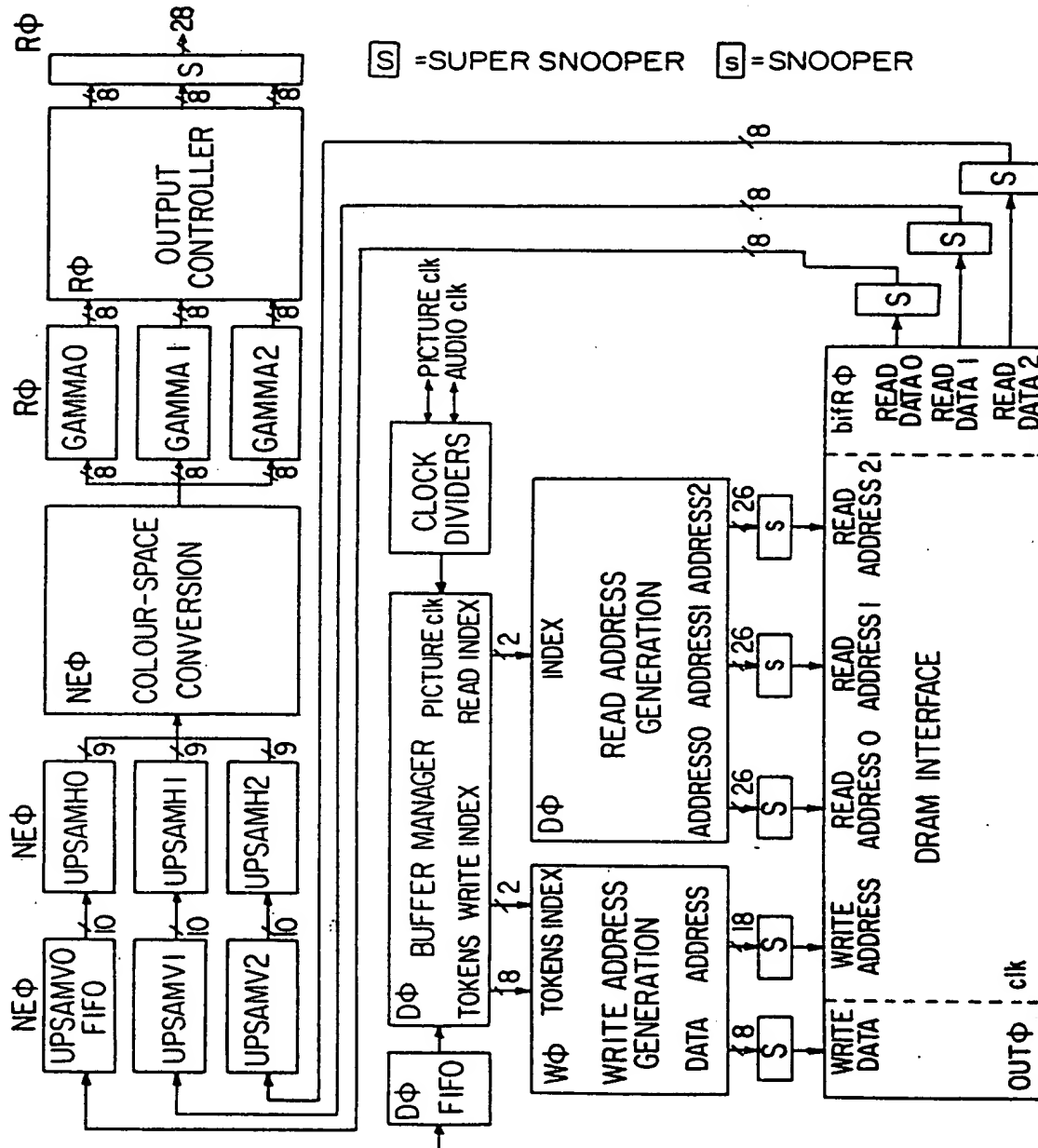


FIG. 155

00771052.012001

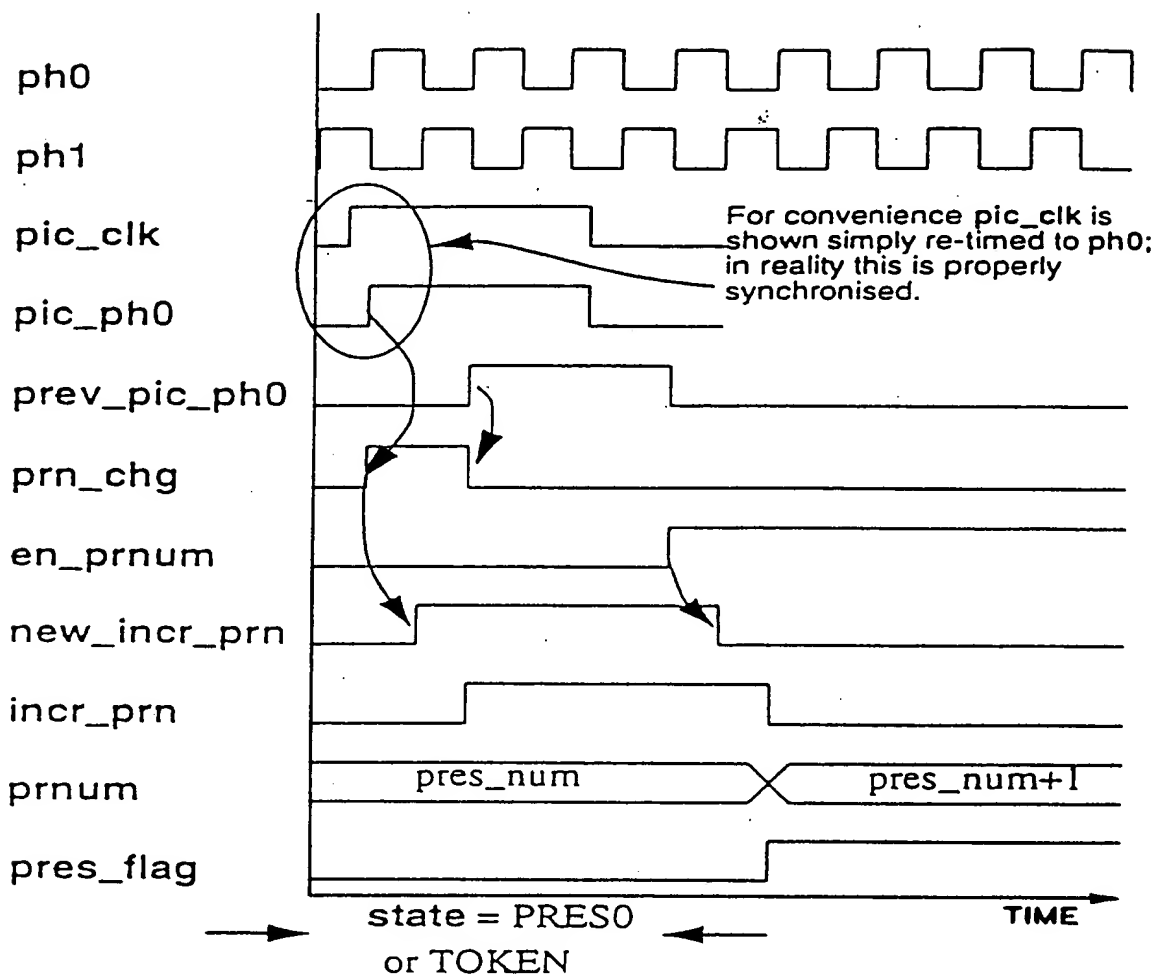
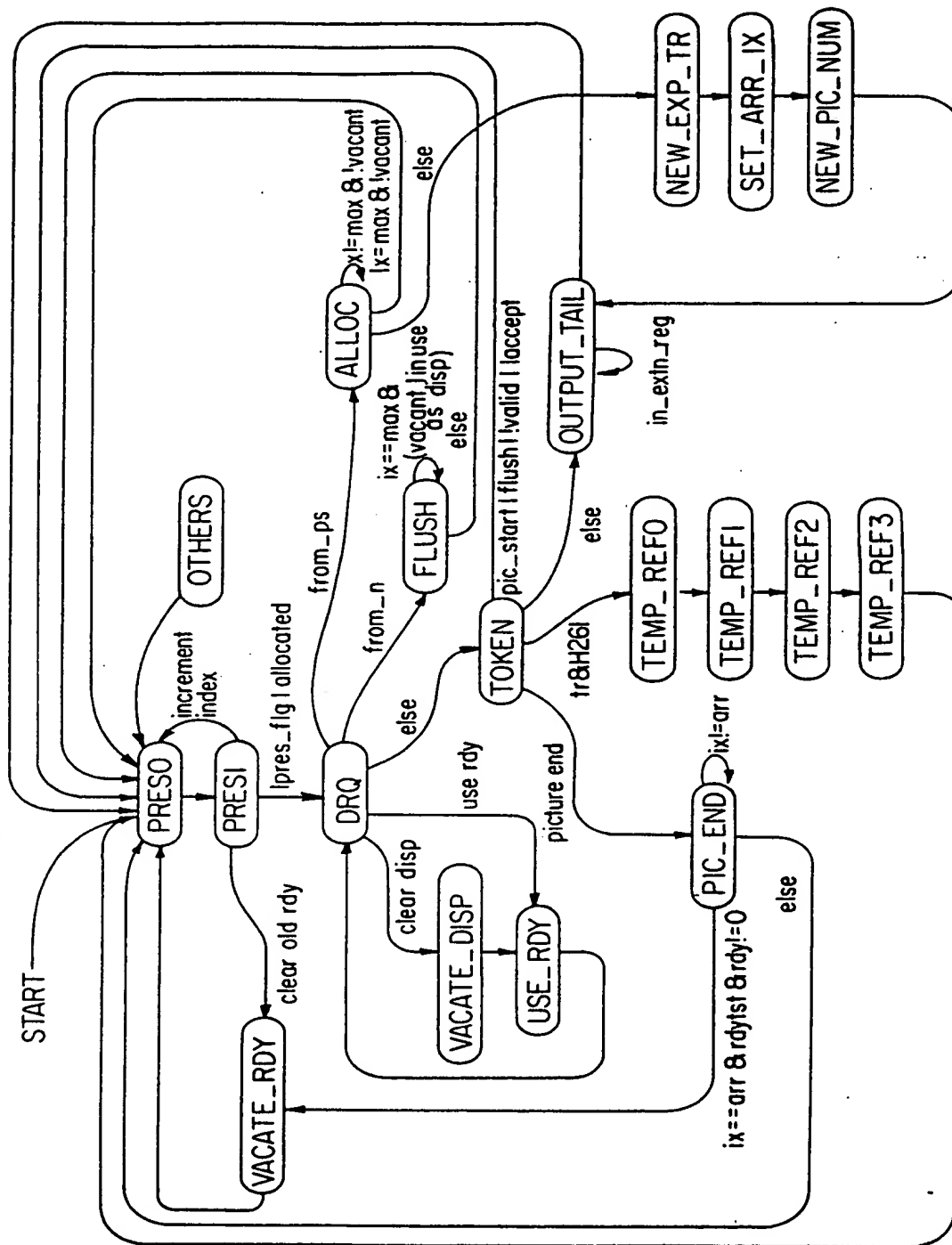


FIG. 156



00771062.012004

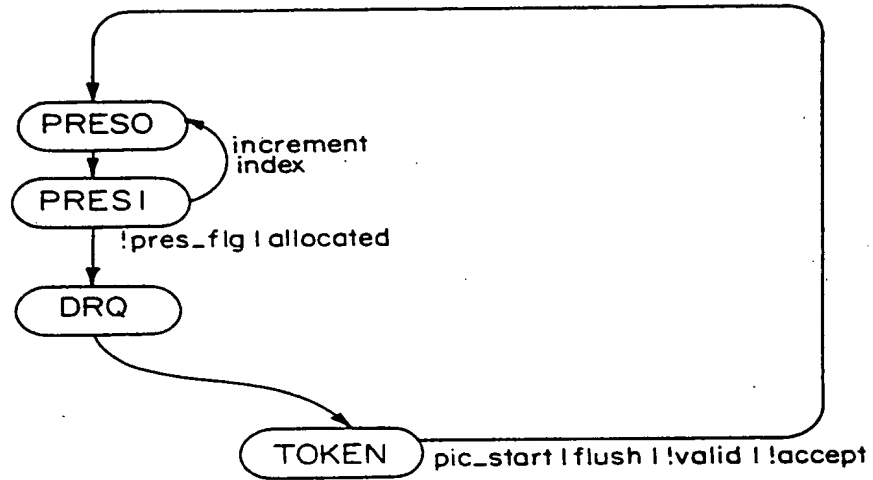


FIG. 158

Figure 1 illustrates a 2D array structure. The vertical axis is labeled **DISP\_VBS\_COMP0** and the horizontal axis is labeled **DISP\_VBS\_COMP1**. The array is organized into several components and blocks:

- Top Row:** Contains blocks labeled 0, 1, 2, 3, 4, 2A, and 2B.
- Second Row:** Contains blocks labeled 2C, 2D, 2E, 2F, and 57.
- Central Area:** A large block labeled **Component 0** is positioned in the center.
- Bottom Row:** Contains blocks labeled 5D8, 604, 605, 62D, 62E, and 62F.
- Right Side:** A block labeled 603 is located on the right side of the array.

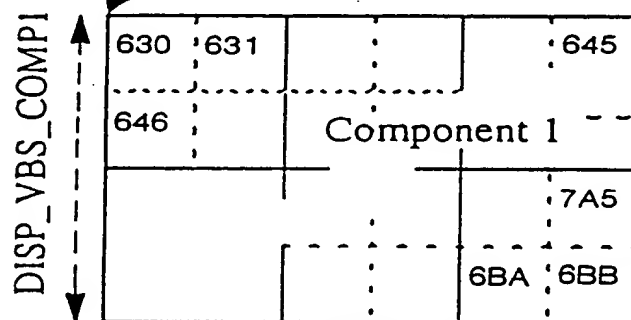


FIG. 159B

- DISP\_COMP\_OFFSET2 = 0x7BC

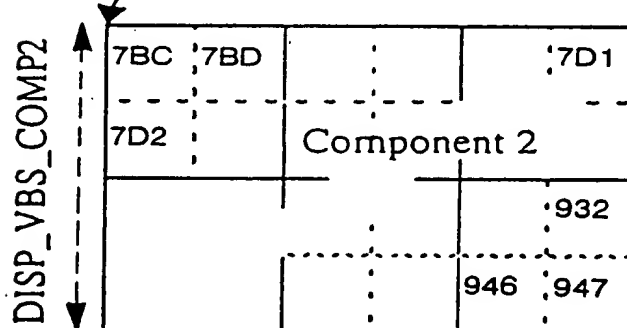


FIG. 159C

00000000 00000000 00000000 00000000

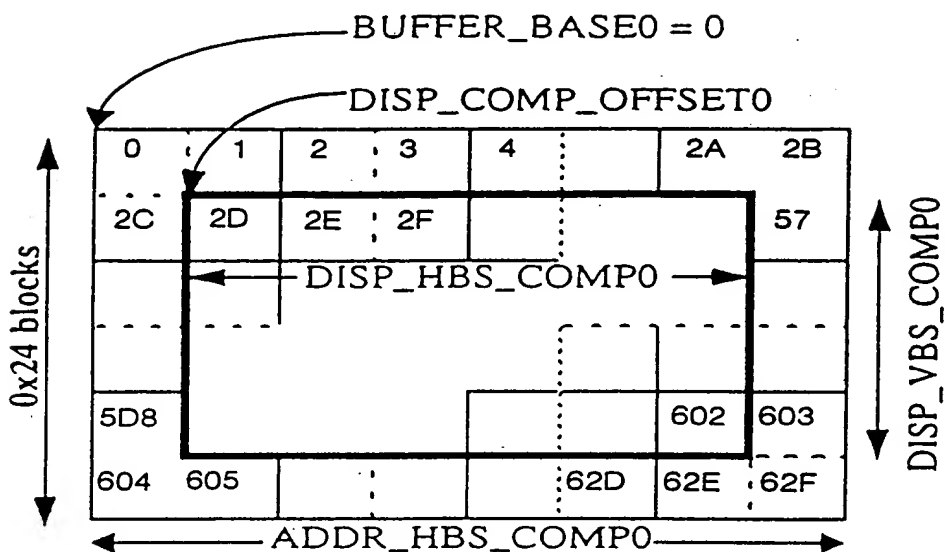


FIG. 160



BUFFER OFFSET 0x00

COMPONENT OFFSET 0x000 + .....

00	01	02	03	04	05	06	07	08	09	0A	0B
0C	0D	0E	0F	10	11	12	13	14	15	16	17
18	19	1A	1B	1C	1D	1E	1F	20	21	22	23
24	25	26	27	28	29	2A	2B	2C	2D	2E	2F
30	31	32	33	34	35	36	37	38	39	3A	3B
3C	3D	3E	3F	40	41	42	43	44	45	46	47
48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
54	55	56	57	58	59	5A	5B	5C	5D	5E	5F
60	61	62	63	64	65	66	67	68	69	6A	6B
6C	6D	6E	6F	70	71	72	73	74	75	76	77
78	79	7A	7B	7C	7D	7E	7F	80	81	82	83
84	85	86	87	88	89	8A	8B	8C	8D	8E	8F

FIG. 161A

COMPONENT1 OFFSET 0x100 + .....

00	01	02	03	04	05
06	07	08	09	0A	0B
0C	0D	0E	0F	10	11
12	13	14	15	16	17
18	19	1A	1B	1C	1D
1E	1F	20	21	22	23

FIG. 161B

COMPONENT1 OFFSET 0x200 + .....

00	01	02	03	04	05
06	07	08	09	0A	0B
0C	0D	0E	0F	10	11
12	13	14	15	16	17
18	19	1A	1B	1C	1D
1E	1F	20	21	22	23

FIG. 161C

0071062 012001

FIG. 162

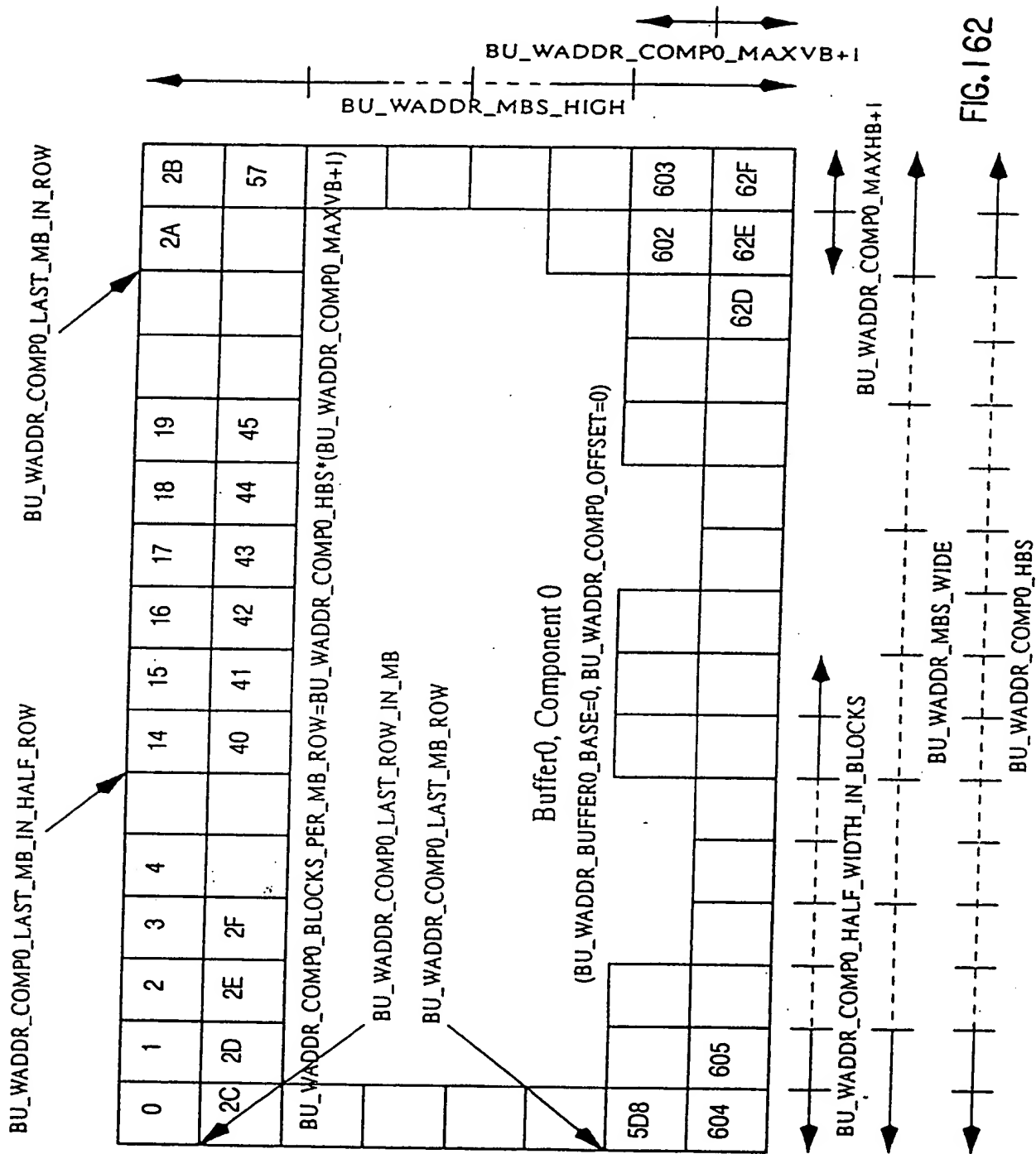
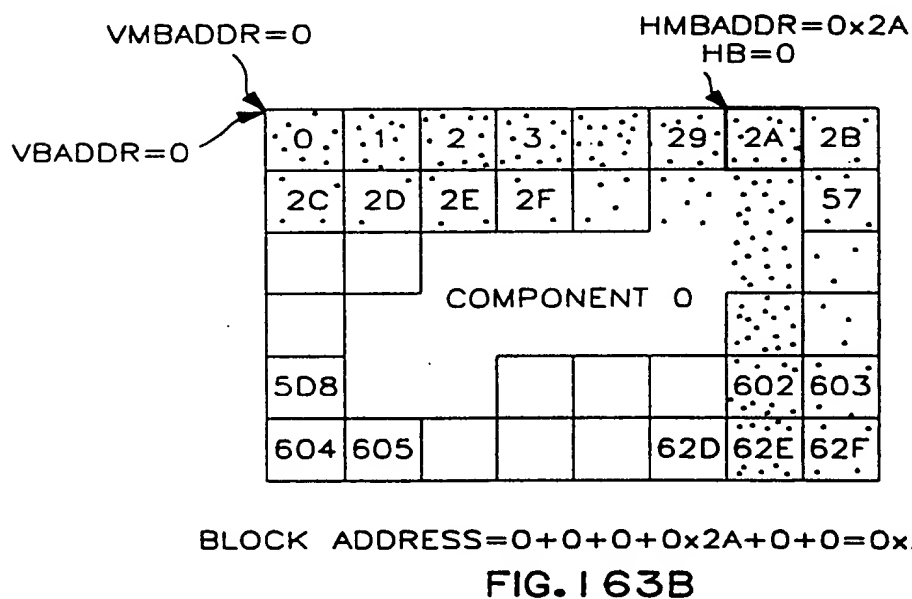
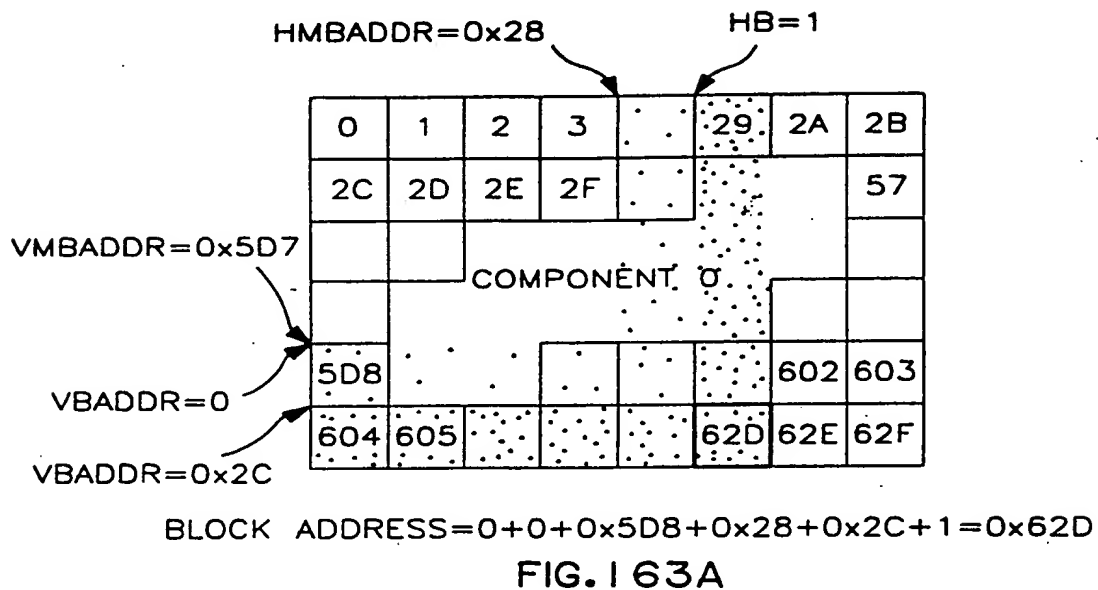


FIG. 162



0071401 2501400

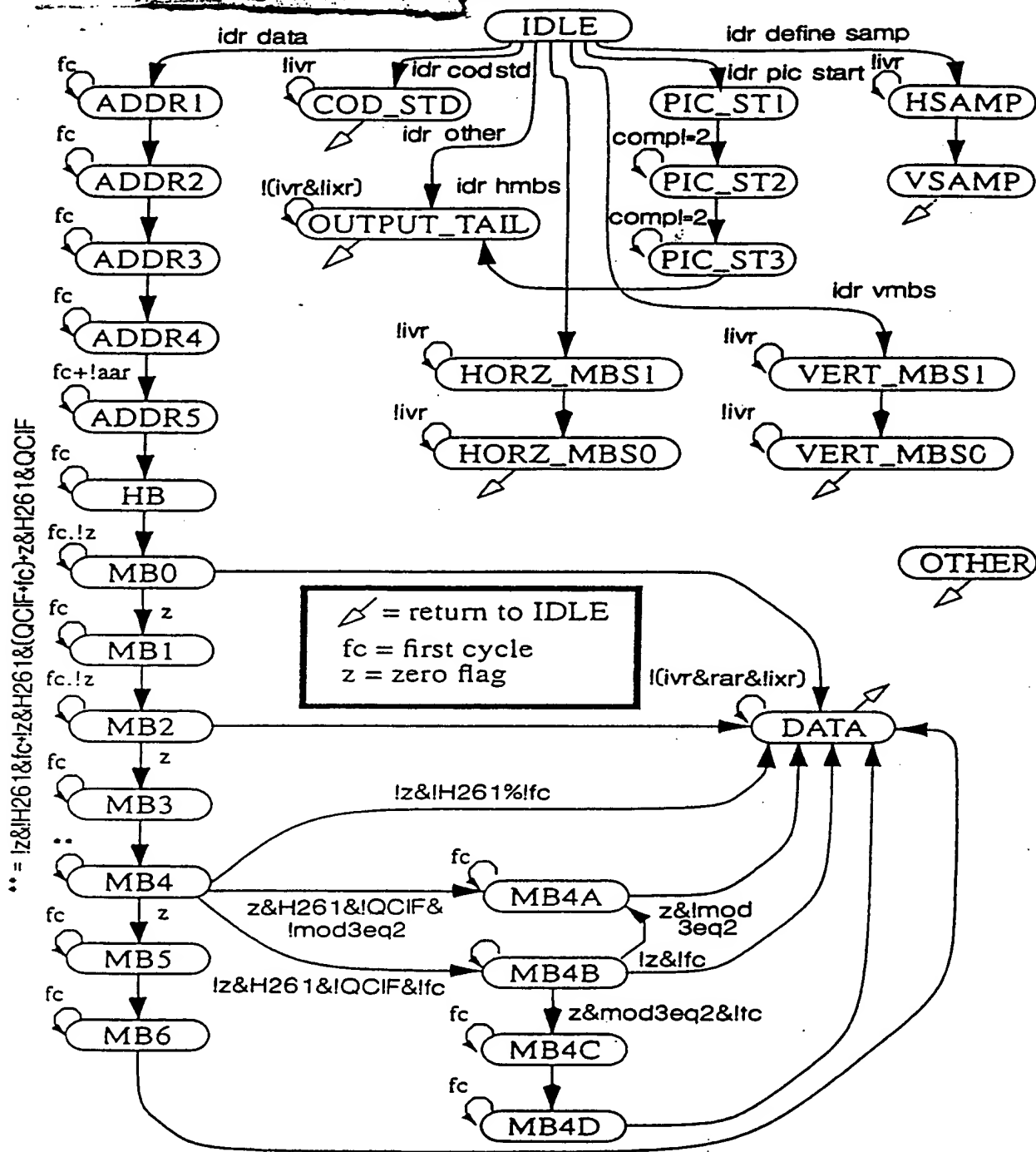


FIG. 164

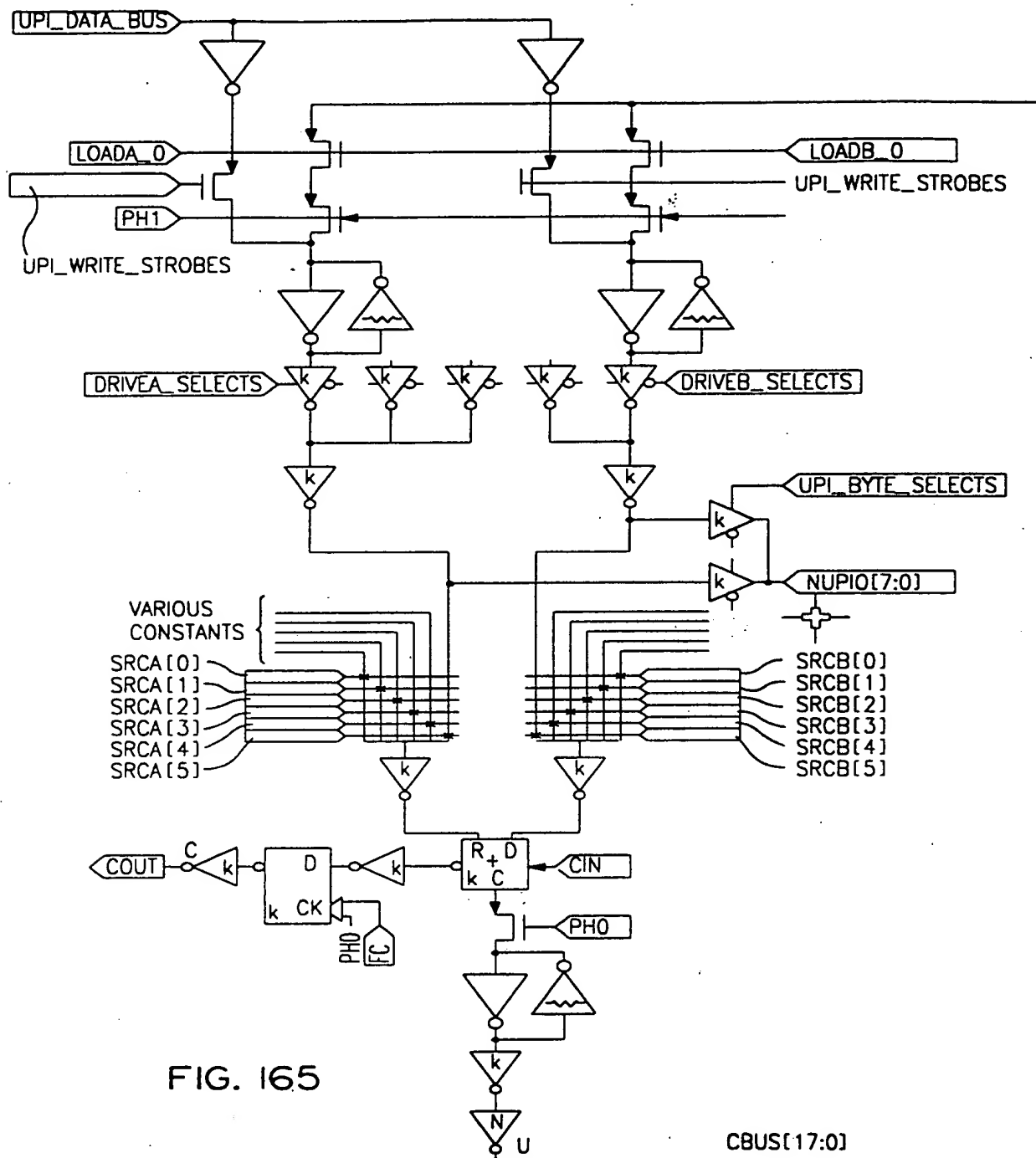


FIG. 165

00771052.012001

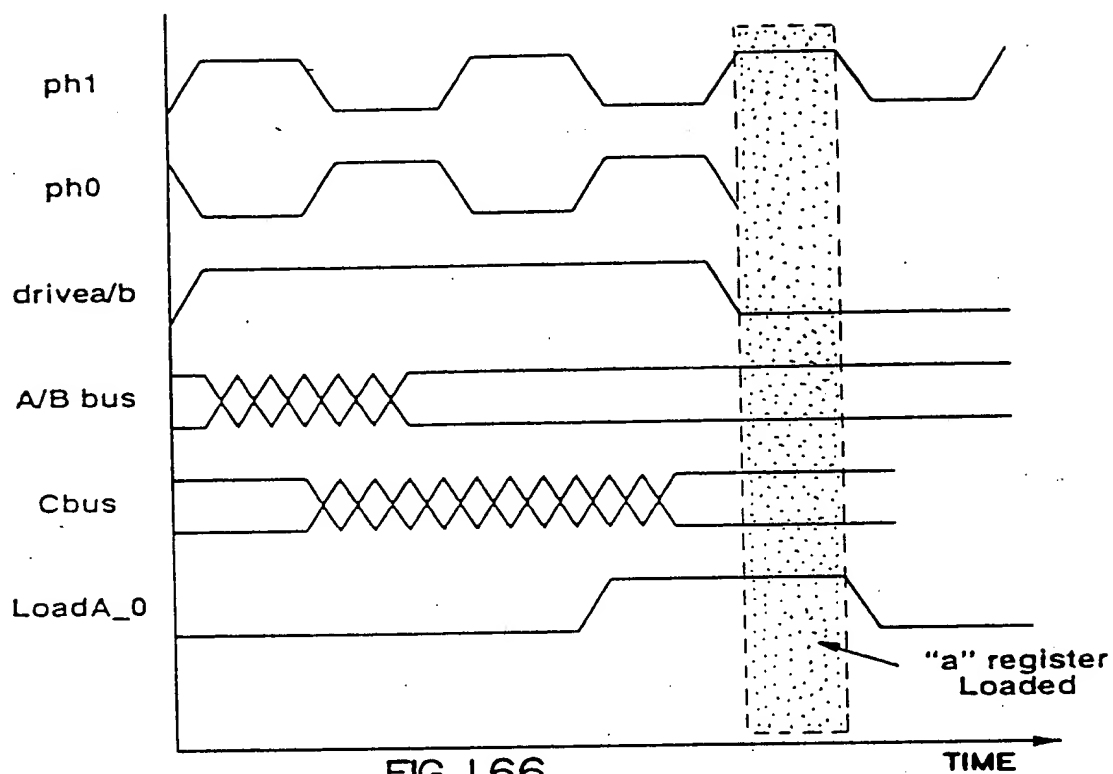


FIG. 166

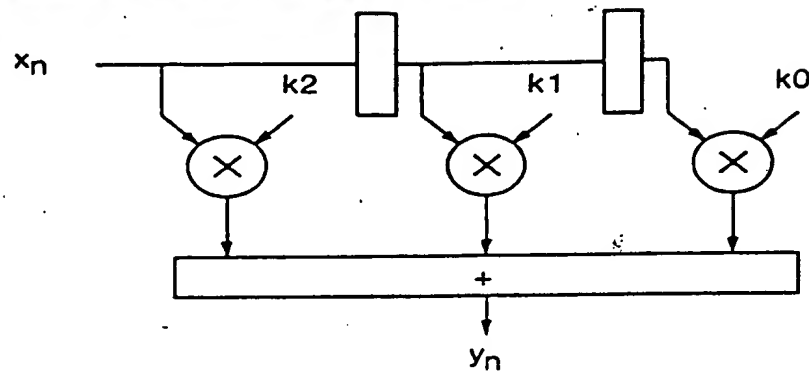


FIG. 167

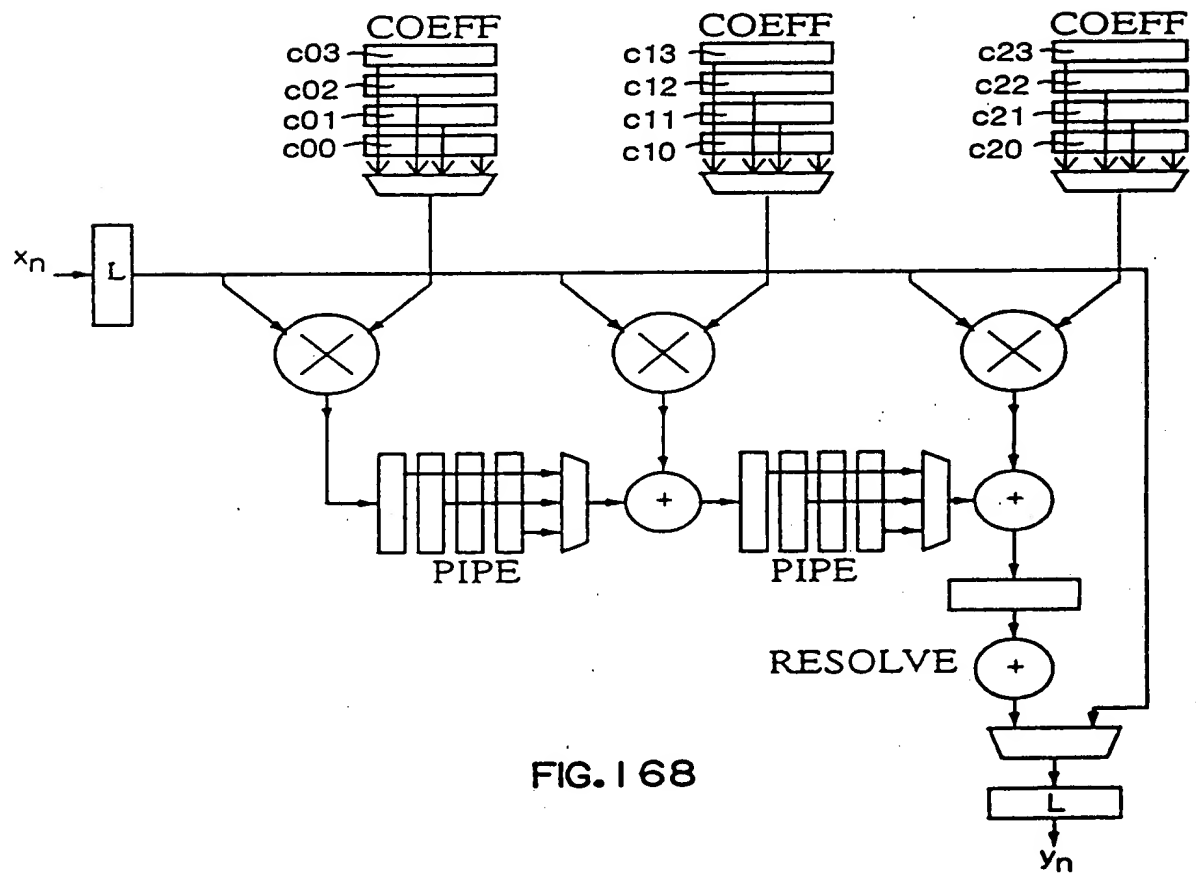


FIG. 168

00771062.012001

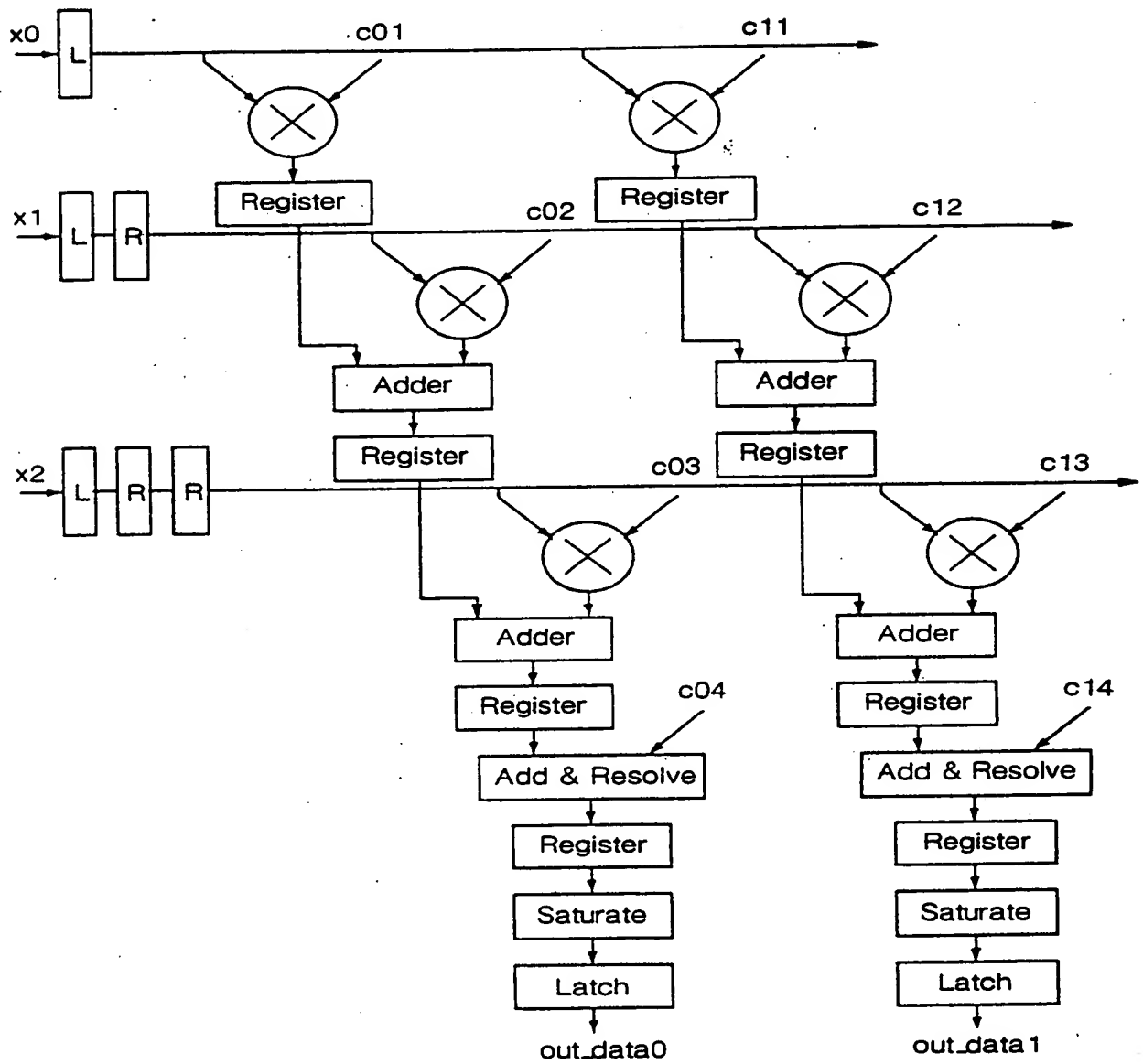


FIG. 169